About the Institute

Jaypee Institute of Information Technology (JIIT), Noida was established in the year 2001 and has been declared as a "Deemed-to-be University" under Section 3 of UGC Act 1956. The undergraduate programs of the university have been accredited by the National Board of Accreditation of AICTE.

JIIT's state-of-the-art, environmentally conditioned campus comprises smart buildings with Wi-Fi connectivity covering the Academic Block, Business School cum Research Block, Faculty Residences, Student Hostels, and Annapurna. Well-equipped modern laboratories and an intellectually stocked Learning Resource Centre with over 43750 books and 1.7 lacs e-resources provide a pleasant and stimulating ambience.

About the Department

The Electronics and Communication Engineering (ECE) department at JIIT runs a 4-year B. Tech. degree program in Electronics & Communication Engineering. Besides running a full-time UG program in Electronics and Communication Engineering, the Institute also runs two full-time PG programs with specializations in ECE and Microelectronics and Embedded Technology (MET). We also have a Ph. D. degree program in key areas of current technological importance. The department is committed to provide a supportive, friendly and challenging environment for teaching, learning and research.

For more info: http://www.jiit.ac.in

Two Days Workshop
on

Signal Processing and its Applications

(July 20th – 21st, 2019)

Organized by:

Department of Electronics and Communication Engineering

Jaypee Institute of Information Technology

Sector – 62 Noida

(Declared Deemed to be University under Section 3 of UGC Act 1956)

Chief Patron

Prof. S. C. Saxena, Vice-Chancellor, JIIT

Patrons

Prof. Hari Om Gupta, Director
Prof. Shweta Srivastava, HoD, ECE
Dr. Vikram Karwal, Co-ordinator, ECE-62

Conveners

Dr. Richa Gupta
Dr. Ankit Garg

About the Programme

This workshop focuses on to provide the basics, the state-of-the-art developments and emerging techniques in Signal Processing through a series of indigenous and invited lectures by speakers from Industry, Academia and JIIT faculty.

Key areas to be covered

- **Digital Signal Processing**
- **Image Processing**
- **Speech Processing**
- **Hands-on sessions on the above**

Objective of the Programme

- Comprehensive knowledge about Digital Signal Processing.
- Speech Processing.
- Laboratory session on MATLAB to get hands-on experience in simulating the theoretical concepts, analysing and interpreting the results of Digital Signal Processing.

- Practical information on Image Enhancement and Image De-noising.
- To enhance teaching ability with respect to content and delivery in the area of Image Processing.

Speakers

Dr. Tarun Kr Rawat
Assistant Professor, Department of Electronics and Communication Engineering, Netaji Subhash University of Technology, Delhi

Prof. Jyotsna Singh
Professor, Department of Electronics and Communication Engineering, Netaji Subhash University of Technology, Delhi

Prof. Rashmi Gupta
Professor, Department of Electronics and Communication Engineering, Ambedkar Institute of Advance Communication Technologies & Research

Dr. Neetu Singh
Assistant Professor (Sr. Grade), Department of ECE, JIIT Noida.

Participants

Faculty of Degree levels, PG students, Research Scholars, Practicing Engineers in Government & Industry can attend this workshop.

Registration

- The registration fee for all the participants is as below
  Industry: **INR 2000**
  Academician (External): **INR 1000**
  Research Scholar and students: **INR 500**
  Internal Faculty, research scholar: No fees
- All the participants would be provided **refreshment/lunch** on each day of the FDP, NO ACCOMMODATION and TA/DA would be provided by organizers.
- The selection will be on *first come first serve basis*. Tentative seats for this course are 30.
- **Candidates will be issued certificates on successful completion of the Programme**.

Important Dates

Last date for registration: **12th July, 2019**.

How to Apply

Registration can be done using the link below:
https://forms.gle/25Tt8s4WyX1H8g9
Registration fee should be paid through NEFT and the transaction detail should be mentioned in Google form.

Bank details for NEFT
Bank Name: IDBI Bank Ltd.
Bank Account Number: 10910200007436
Beneficiary's Name: Jaypee Institute of Information Technology
RTGS/IFSC code: IBKL0000109
Phone (O): 0120-2517812/14/15
E-Mail: avnish.sidhu@idbi.co.in

How to Reach

Metro: The nearest metro stations are Noida Electronic City and Vaishali. Taxi/Auto is easily available at the metro station for JIIT, Sec-62.

Address for Communication

Dr. Richa Gupta/ Dr. Ankit Garg
Electronics and Communication Engineering Department,
Jaypee Institute of Information Technology
A-10, Sector-62, Noida -201309, U.P., India
Ph: 9711304116, 9873125484

richa.gupta@jiit.ac.in , ankit88garg@gmail.com

DAY 1: 20/07/2019			
Time	**Programme**	**Speakers and Affiliation**	**Topic of Talk**
09:00am – 09:30am	Registration	-	-
09:30am – 10:00am	Inauguration	-	-
10:00am – 11:30am	Talk 1	**Dr. Tarun Kr Rawat** Assistant Professor, Department of Electronics and Communication Engineering, Netaji Subhash University of Technology, Delhi	Discrete Fourier Transform (DFT) and its application in Spectrum Estimation
11:30am – 12:00pm	Tea	-	-
12:00pm – 01:30pm	Talk 2	**Prof. Jyotsna Singh** Professor, Department of Electronics and Communication Engineering, Netaji Subhash University of Technology, Delhi	Audio processing techniques for enhancing digital security
01:30pm – 02:30pm	Lunch	-	-
02:30pm – 4:00pm	Lab Session	**Dr. Tarun Kr Rawat** Assistant Professor, Department of Electronics and Communication Engineering, Netaji Subhash University of Technology, Delhi	Digital Signal Processing
04:00pm – 4:15pm	Tea	-	-
DAY 2: 21/07/2019			
09:30am – 11:00am	Talk 1	**Prof. Rashmi Gupta** Professor, Department of Electronics and Communication Engineering, Ambedkar Institute of Advance Communication Technologies & Research	Digital Image Processing and its Research Aspects
11:00am – 11:30am	Tea	-	-
11:30am – 01:00pm	Lab Session	**Dr. Neetu Singh** Assistant Professor (Sr. Grade), Department of Electronics and Communication Engineering, JIIT, Noida	Image Enhancement and Analysis
01:00pm – 01:30pm	Valedictory Session	-	-
01:30pm – 02:30pm	Lunch	-	-