

Faculty Development Program

ON

Modelling and Simulation Tools in Electronics and Communication Engineering (July 10–15, 2017)



विद्या तत्त्व ज्योतिसमः

Organized by

Department of Electronics & Communication Engineering
Jaypee Institute of Information Technology, Noida
(Declared Deemed to be University under Section 3 of UGC Act 1956)

About the Institute

Jaypee Institute of Information Technology (JIIT), Noida was established in the year 2001 and has been declared as a “Deemed to be University” under Section 3 of UGC Act 1956. The institute was founded by revered Shri Jaiprakash Gaur ji and is run by Jaypee Sewa Sansthan with a vision to become a Centre of Excellence comparable to the best in the world for producing professionals with leadership quality in technology, innovation, entrepreneurship and management. The undergraduate programs of the University have been accredited by the National Board of Accreditation of AICTE. Well equipped modern laboratories and an intellectually stocked Learning Resource Centre provide a pleasant and stimulating ambience.

The Electronics and Communication Engineering department was formed in the year 2002. Besides running a 4 year B. Tech degree and 5 years Integrated B.Tech–

M.Tech degree program in Electronics and Communication Engineering, the department is also running two M.Tech degree program with specialization in Communication System (CS) and Microelectronics and Embedded Technology (MET) and PhD program in all key areas of electronics. The department is actively involved in research in the areas of Emerging Communication Technologies, Current-mode Analog Signal Processing, Digital Signal Processing, Image Processing, Microwave Engineering, VLSI Design, MEMS, Wireless communication etc.

Scope of the Program

The aim of this program is to provide an exposure to the various Modelling and Simulation Tools with hands on training and augment the experimental knowledge of faculty members and research scholars in Electronics and Communication field. Talks and hands on sessions on the tools will help in innovative teaching and learning of subjects like digital signal processing, microprocessor, VLSI designing and communication etc. Following topics will be covered in the FDP:

- VLSI designing using Cadence
- Applications of microprocessor kits
- Programming using C language
- Microwave circuit design using HFSS
- Paper writing skills using LaTeX
- Hands on session on DSP kits
- Model based design using Simulink
- Understanding behavior of networks using NS2

Renowned experts from industry and academics will address the participants.

Who should attend?

The program is open to all faculty members/scientists/engineers working in educational institutes/industries/R & D Organizations.

Duration and Venue

FDP will be held at: **JIIT, Sector-128 campus, Jaypee Wish Town, Sector-128, NOIDA** from 10th -15th July, 2017.

Registration Details

The applicant need to **pay through NEFT** to the beneficiary using details: **Beneficiary Account Name:** Jaypee Institute of Information Technology. **Account number :** 910010050443719 **Bank Name :** AXIS bank **IFSC-Code :** UTIB0000372 **Bank Address :** Karkardooma, Delhi

- The Registration fee is **Rs. 2500/-** per participant.
 - Registration fee is waived for the participant from JIIT and its sister institutions
 - Last date to receive duly filled registration form along with payment receipt is **July 1, 2017**.
 - The participants have to bear their own travelling and accommodation expenses.
 - The registration fee covers High Tea and Lunch.
- For more info please visit www.jiit.ac.in.

Correspondence address

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REGISTRATION FORM

NAME : _____

Academic Qualification : _____

Designation : _____

Department : _____

Organization : _____

Address (O) : _____

Phone/Fax : _____

Email : _____

Payment Details (Transaction No.) : _____

Signature of the Participant