

Six Days Faculty Development Program On

Tools and Its Application in: VLSI, Embedded Systems and Machine Learning

8th- 13th July 2019

Organized by:



Department of Electronics and
Communication Engineering

Jaypee Institute of Information Technology
(Declared Deemed to be University u/s Sec3 of UGC Act 1956)

Venue: A-10, Sector 62, NOIDA, U.P., INDIA

ABOUT JIIT

Jaypee Institute of Information Technology (JIIT), Noida was established in the year 2001 and has been declared as a “Deemed to be University” under Section 3 of UGC Act 1956. The undergraduate programs of the university have been accredited by the National Board of Accreditation of AICTE. JIIT’s state-of-the-art, environmentally conditioned campus comprises smart buildings with Wi-Fi connectivity covering the Academic Block, Business School cum Research Block, Faculty Residences, Student Hostels and Annapurna. Well equipped modern laboratories and an intellectually stocked Learning Resource Centre with over 43750 books and 1.7 lacs e-Resources and resources provide a pleasant and stimulating ambience.

The Electronics and Communication Engineering (ECE) Department at JIIT runs a 4-year B.Tech degree program in Electronics & Communication Engineering. Besides UG program, the Institute also runs two full time PG programs with specializations in Electronics and Communication Engineering (ECE) and Microelectronics and Embedded Technology (MET). We also have Ph.D. degree program in key areas of current technological importance. For more info: <http://www.jiit.ac.in>

ABOUT THE FDP

In this FDP, we have planned to cover-up following domains: Machine Learning, Embedded systems and VLSI. This FDP will start with fundamental concepts of machine learning and then overview of machine learning algorithms and their applications.

Today the Internet has become ubiquitous, and has touched almost every corner of the globe, and is affecting human life in unimaginable ways. We are entering an era of the “Internet of Things” (abbreviated as IoT). During this FDP, Applications and Challenges in IOT and AI will be discussed followed by Hands on session on Raspberry Pi. This FDP will also focus on IoT Leaf Node design by **Prof. Dhananjay Gadre**.

VLSI is broadly categorized into Analogue and Digital design. The focus will be on Analogue domain. Practically every electronic product today contains one or more analog-to-digital converters (ADCs). We are confining VLSI talk on high-speed pipelined ADCs, high-resolution and high speed SAR ADCs, and wide-bandwidth continuous time delta sigma ADCs. Resource persons will be eminent experts from industry and academia as follows:

1. **Machine Learning: Dr. Deepak Joshi (IIT Delhi)**
2. **Deep Learning: Dr. SatishChandra (JIIT Noida)**
3. **Embedded Systems: Prof. Dhananjay Gadre (NSIT Delhi)**
4. **Applications and Challenges in IOT and AI: Dr. Gaurav Verma (JIIT Noida)**
5. **ADC Design and Challenges: Mr Ashish Kumar Sharma (Vervesemi Microelectronics Pvt Ltd, Noida)**
6. **System on Chip: Dr. Amrita Rai, (GL Bajaj Institute of Technology and Management, Greater Noida)**

HOW TO REACH

Metro: The station closest to the campus is ‘Electronics City Metro station’

CHIEF PATRONS

Hon'ble Shri Jaiprakash Gaur ji
Hon'ble Shri Manoj Gaur ji

PATRONS

Prof. S.C. Saxena, Vice Chancellor, JIIT
Prof Hari Om Gupta , Director (Sec-128)
Prof. R.C.Mittal, Dean (RID)
Prof. D.K. Rai , Dean(AR)
Prof Shweta Srivastava (HOD,ECE)

CONVENERS

Dr. Vijay Khare
Dr. Gaurav Verma
Dr. Shamim Akhter

ORGANIZING COMMITTEE:

Dr. Rahul Kaushik	Dr. Garima Kapur
Dr. Archana Pandey	Dr. Kirmender Singh
Dr. Shruti Kalra	Dr. Saurabh Chaturvedi
Dr. Ekta Goel	Dr. Amit K. Goyal
Dr. Rachna	Dr. Prakash Kumar
Mr. Atul K. Srivastava	Ms. Shradha Saxena
Mr. Shivaji Tyagi	Mr. Ankur Bhardwaj
Ms. Ruby Beniwal	Mr. Mandeep S. Narula
Mr. Ritesh K. Sharma	Mr. Ajay Kumar

HOW TO REGISTER

To register yourself, kindly fill the form given on following link: <https://bit.ly/2YUjYzO>

Registration fee should be paid through **NEFT/DD** and the transaction details should be mentioned in Google form.

Registration fees:

The FPD is for Faculty, Industry personals and research scholar and having registration fee of Rs. 2500/-.

Last date of Registration: 30th June 2019.

Bank Details for NEFT:

Account Name : Jaypee Institute of Information
Technology Noida Sec 62
Bank name : IDBI bank, Sector 18 Noida
Account Number: 109102000007436
IFSC Code : IBKL0000109

Demand Draft drawn in favour of “**Jaypee Institute of Information Technology**” payable at Noida.

ADDRESS OF COMMUNICATION

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ACCOMMODATION & SUPPORT

- The registration fee contains Lunch and Tea during FDP.
- The participants will have to bear their own travelling and accommodation expenses.
- Limited accommodation will be provided on first come first served basis to the participants, based on the availability of rooms in the institute Guest Room and Hostels on payment basis.

REGISTRATION FORM

Full Name:

Designation:

Institution / Organization:

Qualification:

Mailing Address:

Contact No (Mobile):

Email:

PAYMENT DETAILS:

Bank Name:

Amount:

DD No/NEFT Transaction Number:

Date:

Signature of Applicant

NOTE:

Scanned copy of filled registration form and DD should be sent through email gaurav.verma@jiit.ac.in. Original copy and DD should be sent by post to the mailing address given in brochure. Online registration can also be done at: <https://bit.ly/2YUjYzO>