## ONE WEEK ONLINE FACULTY DEVELOPMENT PROGRAM

RECENT TRENDS OF ARTIFICIAL INTELLIGENCE IN ELECTRONIC **DEVICES AND SYSTEMS** 



**Department of Electronics and Communication Engineering Jaypee Institute of Information Technology** A-10, Sector-62, Noida, **UP-201309, INDIA** 

#### PROGRAM COORDINATORS

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### JAYPEE INSTITUTE OF INFORMATION TECHNOLOGY AND DEPARTMENT OF **ELECTRONICS AND COMMUNICATION**

Jaypee Institute of Information Technology (JIIT), Noida, an AICTE approved, NIRF (MHRD) ranked and NAAC accredited University which has been constantly ranked amongst the top engineering institutes in north India.

(http://www.jiit.ac.in). To keep pace with the rapidly evolving electronics industry requires unwavering passion and urge to innovate. This, precisely, is what Electronics and Communication Engineering (ECE) Department at JIIT is about. Started in the year 2002, the department of ECE runs full time 4-year BTech degree program at UG level. The department also offers 5-year Integrated degree program in ECE and two PG degree programs with specialization in ECE and microelectronics & embedded systems. The department is having a team of highly motivated 72 faculty members having extensive experience in industry, research as well as teaching. Our strength is a reflection of their skills, innovation and drive. At present, there are around 40 Research scholars involved in thrust areas of research such as VLSI Design, 4G and 5G Technologies, channel modeling, VLSI, DSP architectures and many more.

#### FACULTY DEVELOPMENT PROGRAM

The theme of this FDP is to introduce, innovate and explore the recent trends of Artificial Intelligence (AI) in electronic devices and system design methodologies to the faculty members and researchers. According to a research conducted by IBM, 41% of the electronic industry will be changing its design model in the upcoming 2 years due to the immense capability of AI to be explored and utilized for AI enabled electronic devices. AI is actually paving the way for the future of Electronic Devices and Systems. Thus, our goal is to develop technical skills in our faculty members and research scholars, motivating them to put AI work in electronics. The program also aims at establishing strong research links with the electronics industry in India and abroad.

#### THE OBJECTIVE OF FDP IS TO:

- 1. To perceive the gaps in attaining learning outcomes as per bloom's taxonomy focusing on AI based projects.
- 2. Motivating female professionals/researchers for improved gender balance in academic research.
- 3. To build up research activities in order to bridge the Gap between Academics and Industries through project-based learning in artificial intelligence domain.
- 4. To reshape strategic vision effectively in teaching contents. teaching methods and experimental modes.

#### AFTER THE COMPLETION OF FDP:

- 1. The faculty members would be able to conduct researchbased activities to build up the VLSI domain of their respective institutes
- 2. The faculty members can relook Experiments, Assignments, projects in VLSI domain to recognize the gaps in attainting learning outcomes as per Bloom's Taxonomy.
- 3. The faculty members can design project-based learning activities to strengthen the courses in VLSI domain.

#### WHO CAN PARTICIPATE?

- 1. The program is open to all the faculty members/ scientists/engineers working in educational institutes/ industries/ R&D organizations.
- 2. Single registration to attend all lectures
- 3. E-certificate will be issued to the participants on successful participation (>80% attendance).
- 4. Google meet link and other instructions will be shared via e-mail to all registered participants.

#### REGISTRATION FEE

NO REGISTRATION FEE

### **HOW TO REGISTER**

To complete the registration, the applicants are requested to fill the Google form provided in the link below. https://forms.gle/GTWSGC7eHnhcEPZ8A

Last Date of Registration: July 18th, 2021.

## **HONORARY SPEAKER**



Prof. Shweta Srivastava,
Head, Department of Electronics and Communication
JIIT,Noida

# **DISTINGUISHED SPEAKERS**



Dr. Vinod Kumar Yadav Professor, DTU, Delhi



Dr. Nidhi Taneja Professor, IGDTU, Delhi



Dr. Sonal Singhal Associate Professor, SNU, Noida



Dr. Krishna Singh, Associate Professor, GB Pant Engg. College, Okhla, Delhi



Dr. Sandeep Joshi, Assistant Professor, BITS, Pilani



Dr. Akash Gupta Assistant Professor, LNMIT, Jaipur



Dr. Manan Jani Assistant Professor, NIEC, Delhi



Dr. Baljit Kaur Assistant Professor, NIT, Delhi



Dr. Abhishek Srivastava Assistant Professor, IIT, Mumbai



Mr. Ajit Singh, Zscaler Inc, San Jose, California,US