

**M.Tech ECE (MET) Programme Structure w.e.f. 2018-19 Batch****FIRST SEMESTER**

Sl. No.	Course Code	Title	Contact Hours				Credits
			L	T	P	Total	
1.	17M21EC111	Microelectronic Devices Technology and Design Interface	3	-	-	3	3
2.	17M21EC112	Digital Integrated Circuit Design	3	-	-	3	3
3.		Elective – I	3	-	-	3	3
4.		Elective – II	3	-	-	3	3
5.		Elective – III	3	-	-	3	3
6.	18M11GE111	Research Methodology and Intellectual Property Rights	2			2	2
7.	17M25EC111	VLSI Design and Simulation Lab-1	-		6	6	3
		<b>TOTAL</b>				<b>23</b>	<b>20</b>

**SECOND SEMESTER**

Sl. No.	Course Code	Title	Contact Hours				Credits
			L	T	P	Total	
1.	17M21EC114	Advanced Embedded System	3	-	-	3	3
2.	17M21EC115	Analogue Integrated Circuit Design	3	-	-	3	3
3.		Elective – IV	3	-	-	3	3
4.		Elective – V	3	-	-	3	3
5.		Audit-I	2	-	-	2	Qualifying
6.	17M21EC113	Project Based Learning - I				4	2
7.	17M25EC112	VLSI Design and Simulation Lab-2	-	-	6	6	3
		<b>TOTAL</b>				<b>24</b>	<b>17</b>

**THIRD SEMESTER**

Sl. No.	Course Code	Title	Contact Hours				Credits
			L	T	P	Total	
		Open Electives	3			3	3
1.	17M27EC211	Seminar & Term Paper <b>OR</b> Earn credits by transfer eg. MOOCs, Course Work at another Institute, Supervised Study				4	4
2.	17M21EC116	Project Based Learning - II				8	4
3.	17M27EC212/ 17M27EC213/ 17M27EC214	Dissertation /Industrial Project / Entrepreneurial Project				8	4
		Audit-II	2			2	Qualifying
		<b>TOTAL</b>				<b>25</b>	<b>15</b>

## FOURTH SEMESTER

Sl. No.	Title	Contact Hours				Credits
		L	T	P	Total	
1.	17M27EC215/ 17M27EC216/ 17M27EC217				32	16
	TOTAL				32	16

**TOTAL CREDITS:68**

### **Courses for Audit-I and II:**

1. English for Research Paper Writing
2. Disaster Management
3. Sanskrit for Technical Knowledge
4. Value Education
5. Constitution of India
6. Pedagogy Studies
7. Stress Management by Yoga
8. Personality Development through life enlightenment skills

### **Subjects for Open Electives:**

1. Business Analytics
2. Industrial Safety
3. Operations Research
4. Cost Management of Engineering Projects
5. Composite Materials
6. Waste to Energy

### **Electives**

1. HDL Based Digital Design
2. Advanced Optical Communication Systems
3. Advanced DSP
4. RF Microelectronics
5. Estimation over Distributed Networks
6. VLSI physical design
7. Digital System Testing
8. CMOS IC Interface Design
9. Selected Topics in Communication
10. Statistical Signal Processing
11. Advanced Embedded Systems
12. DSP Architecture
13. Advanced Video Processing