

Detailed Syllabus
Lab-wise Breakup

Course Code	17M11EC130	Semester ODD (specify Odd/Even)	Semester 4th Session 2018 -2019 Month from Jan to May
Course Name	Project Based Learning-I		
Credits	2	Contact Hours	4

Faculty (Names)	Coordinator(s)	Dr. Madhu Jain
	Teacher(s) (Alphabetically)	Dr. Garima Kapoor, Dr. Gaurav Verma, Mr. Mandeep Narula, Dr. Neetu Singh Ms. Ruby Beniwal, Ms. Smriti Bhatnagar

COURSE OUTCOMES		COGNITIVE LEVELS
CO1	Demonstrate a depth of knowledge of Electronics and Microelectronics Engineering	Understanding (Level II)
CO2	Analyze various feasible methods of solving a problem to slot a suitable solution methodology	Analyzing and Designing (Level IV)
CO3	Demonstrate an ability to present and defend their research work to a panel of experts.	Evaluating (Level V)
CO4	Evaluate /Validate sound conclusions based on evidence and analysis	Create Level (Level 6)

Evaluation Criteria		
(i)	Each fortnightly assessment (First assessment should be at the end of 3 rd week from the beginning of the semester and thereafter fortnightly assessment. A total of six assessments giving a total percentage 6 x 8 = 48%)	- 8%
(ii)	Report at the end of the semester	- 10%
(iii)	Semester end presentation by the students	- 10%
(iv)	Viva-voce at the end of the semester	- 16%
(v)	Peer group evaluation (i.e. evaluation by the fellow students not belonging to the same batch)	- 8%
(vi)	Self assessment by the student concerned (can be moderated by the instructor by discussing with the student concerned)	- 8%

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Course Outcomes

Course Code	17M17EC219/ 17M17EC220/ 17M27EC212/ 17M27EC213 & 17M17EC511/ 17M17EC512 / 17M17EC222 / 17M17EC223/ 17M27EC215/ 17M27EC216	Semester ODD & EVEN	Semester 3 rd & 4 th for M.Tech / 11 th for Dual Degree Session 2019 -2020 Month from July to Dec/Jan to May
Course Name	Dissertation /Industrial Project		
Credits	M.Tech – 4 & 16 DD - 22	Contact Hours	8 & 32

Faculty (Names)	Coordinator(s)	Ms. Bhawna Gupta, Dr. Rachna Singh
	Teacher(s) (Alphabetically)	All faculty of ECE Deptt.

COURSE OUTCOMES		COGNITIVE LEVELS
CO1	Summarize the contemporary scholarly literature, activities, and explored tools/ techniques/software/hardware for hands-on in the respective project area in various domain of Electronics Engineering.	Understanding (Level II)
CO2	Gain knowledge of the State-of-Art in the chosen field of study. Analyze various feasible methods of solving a problem to slot a suitable solution methodology	Analyzing and Designing (Level IV)
CO3	Use latest techniques and software tools for achieving the defined objectives. Evaluate /Validate sound conclusions based on evidence and analysis	Evaluating (Level V)
CO4	Demonstrate the oral and written communication skills. Describe the importance of possible future developments in the selected domain	Create Level (Level VI)

Evaluation Criteria

(Dissertation at the end of third semester for M.Tech only)

Components	Maximum Marks
End Term Viva	60
Day to Day	40
Total	100

(Dissertation at the end of final semester for M.Tech / DD)

Components	Maximum Marks
End Term Viva	50
Special Contribution	10
Day to Day	40
Total	100

OR

(Industrial Project at the end of final semester for M.Tech / DD)

Components	Maximum Marks
End Term Viva	30
Day To Day	20 (Awarded by Internal Supervisor)
Day To Day	50 (Awarded by Supervisor from Industry)
Total	100