

Department of Electronics and Communication Engineering

VLSI/VHDL Lab

The VLSI lab staff is a highly motivated team of researchers, graduate students and engineers striving to conduct innovative and cutting-edge research in diverse areas of VLSI and sensor design. The various research activities focus on modern VLSI processor architectures for big data systems, very high speed on chip interconnect channels, reconfigurable data flow architectures, memristor model development and circuit design, high frequency RFIC and mixed mode systems, novel thermal and 3D imaging sensors and also miniaturizing electron accelerators. By using the state of the art VLSI design tools and technologies we aim to provide the ideal environment for teaching and training the next generation of VLSI researchers and engineers and who will serve as leading contributors to academia and industry.

Various Experimental Kits :

ADVANCE FPGA/CPLD TRAINER BOARD ALTERA

ADVANCE FPGA/CPLD BASED ON X LINE

LOW COST FPGA/CPLD TRAINER BOARD ALTERA