



2013 International Conference

on

Signal Processing and Communication

2013-ICSC

December 12-14, 2013

Jaypee Institute of Information Technology, NOIDA

Uttar Pradesh, India

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PREAMBLE

Signal processing is concerned with the representation, manipulation and transformation of signals and the information that they carry. It provides techniques for processing acquired time-series data for the purpose of efficient analysis and synthesis. Over the years, it has been developed to give great advancements in the areas of microchips, digital systems and computer hardware. The applications of signal processing are vast and interdisciplinary, ranging from engineering to economics and astronomy to biology. The research in many areas such as signal coding and de-noising has paved the path for advancements. Recent decade has witnessed major revolution in communication and processing of digital media. As a consequence solutions to major problems in processing, transmission and reception have made signal processing an integral part of modern electronic systems.

There is a need to meet the ever increasing demand for providing a forum to scientists and researchers to discuss and put forward their ideas and research findings with the co-researchers from all over the world. This conference will provide an opportunity to highlight recent developments and to identify emerging and future areas of growth in these exciting fields. It will further give impetus to the researchers towards bringing out newer and efficient techniques. Keynote speakers with their vast research experience in various fields will arouse other researchers for development of signal processing and communication engineering. With the participation of so many experts, it is expected that this conference will help in meeting future challenges.

Authors are invited to submit original unpublished manuscript that demonstrates the recent advances in the following areas of interest, but are not limited to:

Signal Processing

Signal Processing for Communication
Image & Video Signal Processing
Medical Imaging
Image Forensics
DSP Algorithms & Architectures
Pattern Analysis & Classification
Large Dimensional Signal Processing
Compressive Sensing
Statistical Signal Processing
VLSI for Digital Signal Processing
Speech & Audio Processing
Biomedical Signal Processing

VLSI Technology and Embedded Systems

MEMS
Ultra-Low Power Design
Embedded Multi-Core Systems
Embedded System Reliability
System-Level Design Methodology
Giga-Scale Design Methodology
Processor and Memory Designs
Defect Tolerant Architectures
Logic Synthesis, Verification and Physical Design
Test and Reliability
Device/Circuit Simulation and Modeling
CAD/EDA Methodologies for Nanotechnology
Smart Grid Green Technologies
Post-CMOS Devices

Communication

Communication Theory & Systems
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Important Dates

Abstract Submission: Sept. 12, 2013

Paper Submission: Sept. 15, 2013

Notification of Acceptance: Nov. 07, 2013

Camera Ready Paper Submission: Nov. 25, 2013

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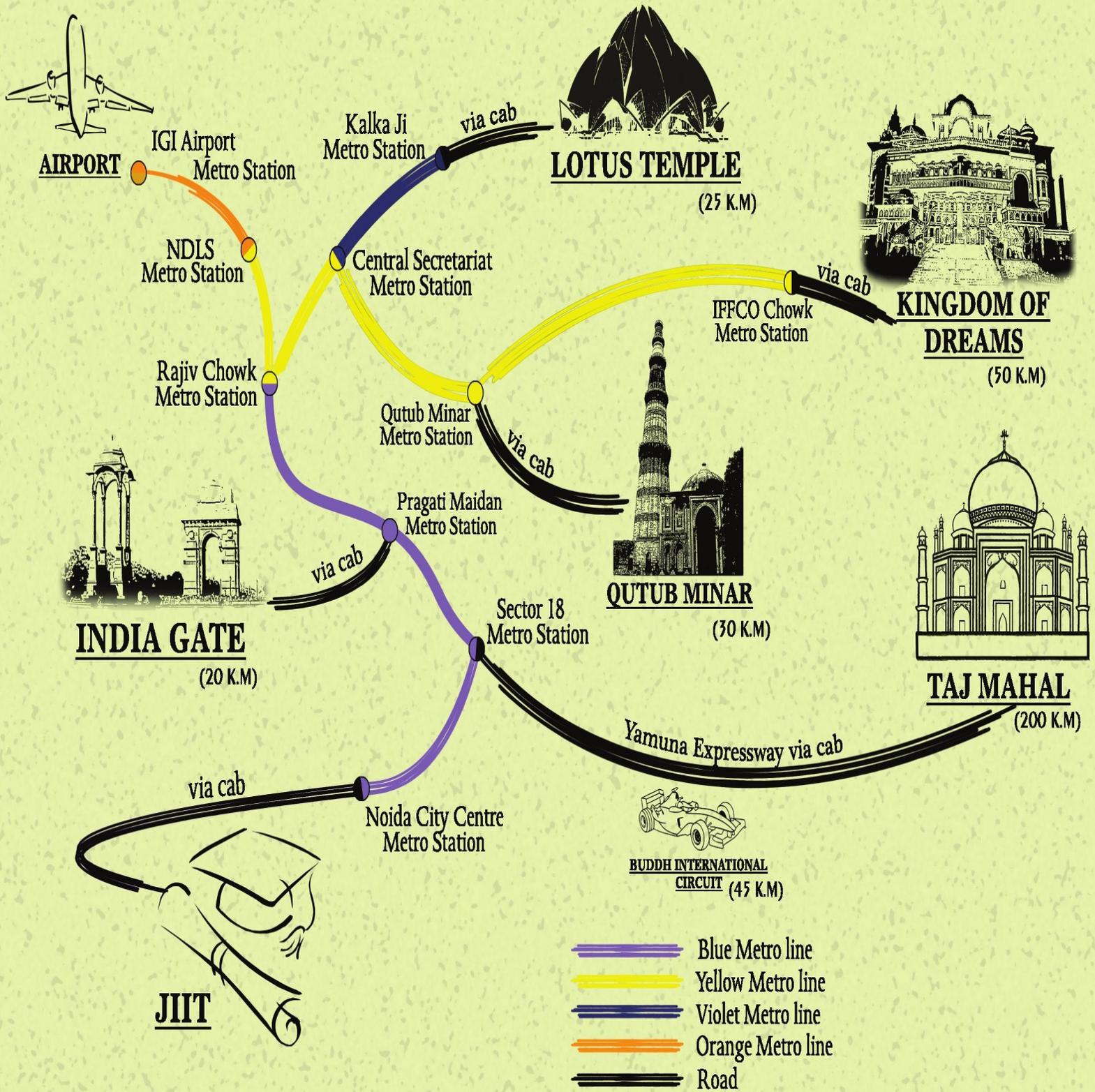
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