Track ID: UPUNGN11440   **Volume-4**

EVALUATIVE REPORT

Department of Electronics and Communication Engineering

for

ASSESSMENT AND ACCREDITATION

Submitted to

NATIONAL ASSESSMENT AND ACCREDITATION COUNCIL

BANGALORE

JAYPEE INSTITUTE OF INFORMATION TECHNOLOGY
NOIDA

17 September, 2015

Evaluative Report of the Department

1. **Name of the Department:** Electronics and Communication Engineering

2. **Year of establishment:** 2001

3. **Is the Department part of a School/Faculty of the university?**

   JIIT is a unitary University. It has departments that include Electronics and Communication Engineering Department and Jaypee Business School.

4. **Names of programmes offered (UG, PG, M. Phil., Ph. D., Integrated Masters; Integrated Ph. D., D. Sc., D. Litt, etc.)**

   a) Ph. D.
   b) M. Tech. (Electronics and Communication Engineering with specialisation in Communication Systems)
   c) M. Tech. (Electronics and Communication Engineering with specialisation in Microelectronics Systems and Embedded Technology)
   d) Dual Degree B. Tech.-M. Tech. (Electronics and Communication Engineering) [After UGC circular, July 2014, regarding standardisation of nomenclature of degrees, this program will run as Integrated M. Tech. (Electronics and Communication Engineering) w.e.f. 2015-16]
   e) B. Tech. [Electronics and Communication Engineering (ECE)]

5. **Interdisciplinary programmes and departments involved:**

   None. However, the department program curriculum provides courses from other departments namely: Computer Science Engineering & Information Technology (CSE & IT), Mathematics, Physics and Material Science and Engineering (PMSE), Biotechnology and Humanities and Social Sciences (HSS).

6. **Courses in collaboration with other universities, industries, foreign institutions, etc.**

   None. However, the department has an academic collaboration with University of Florida (UFL) through which final year B. Tech students can go for a semester long study at UFL, Gainesville, Florida, U.S.A.

7. Details of programmes discontinued, if any, with reasons:

None in last four years

8. Examination System: Annual/Semester/Trimester/Choice Based Credit System

Semester, along with choice based credit system

9. Participation of the department in the courses offered by other departments

The department offers following courses to other departments:

Course Name	Beneficiary Department
Electrical Circuit Analysis	CSE/IT, BIOTECH
Electrical Circuit Analysis Lab	CSE/IT, BIOTECH
Basic Electronic Devices and Circuits	CSE/IT, BIOTECH
Basic Electronics lab	CSE/IT, BIOTECH
Signals And Systems	CSE/IT
Signals And Systems Lab	CSE/IT
Digital Electronics	CSE/IT
Digital Electronics Lab	CSE/IT
Communication Systems	CSE/IT
Communication Systems Lab	CSE/IT
Information Theory and Applications	CSE/IT
Mobile Communications	CSE/IT
Optical Communication	CSE/IT
Digital Hardware Design	CSE/IT

10. Number of teaching posts sanctioned, filled and actual (Professors/ Associate Professors/ Asst. Professors/ others)

Positions	Sanctioned[#]	Filled	Actual (including CAS & MPS)
Professors	13	05	05
Associate Professors	24	01	01
Asst. Professors	68	57	57
Teaching Assistants*	-	-	19*

#JIIT follows flexible cadre structure like IITs; *Full time Ph. D./ M. Tech. students.

11. Faculty Profile with name, qualification, designation, area of specialization, experience and research under guidance

Name	Qualification	Designation	Specialization	No. of Yrs of Exp.	No. of Ph. D. students guided in the last 4 years	No. of M. Tech students guided in the last 4 years
Hari Om Gupta	Ph. D., Univ. of Roorkee	Professor & Dir (Sec-128)	Power Systems & Systems Engineering	40	9 (Awarded) 7 (Ongoing)	NIL
Krishna Gopal	Ph. D., Kurushetra Univ.	Professor & Dean A&R	System Engineering	47	1 (Awarded) 4 (Ongoing)	NIL
R. C. Jain	Ph. D., Univ. of Alberta	Professor& HOD	Wireless Communication Systems	44	6 (Ongoing) 1 (Awarded)	6
A. B Bhattacharyya	Ph. D., IIT BHU Varanasi	Professor	VLSI Design	50+	2 (Ongoing)	4
Samir Dev Gupta	Ph. D., JIIT Noida	Professor	Antenna Design and Theory	35	3 (Ongoing)	12

Shweta Srivastava	Ph. D., IIT BHU Varanasi	Assoc. Professor	Microwave Engineering and Antennas	16	6 (Ongoing) 1 (Awarded)	8
Vikram Karwal	Ph. D., Univ. of North Carolina	Asstt. Professor	Signal processing	8	3 (Ongoing) 1 (Awarded)	5
Jitendra Mohan	Ph. D., UTU Dehradun	Asstt. Professor	Analog Signal Processing	9	1 (Ongoing)	NIL
Sajai Veer Singh	Ph. D., UTU Dehradun	Asstt. Professor	Analog Signal Processing	14	4 (Ongoing)	NIL
Manish Kumar	Ph. D., JIIT Noida	Asstt. Professor	VLSI	11	3 (Ongoing)	3
Vivek Kumar Dwivedi	Ph. D., JUIT Waknaghat	Asstt. Professor	Wireless Communication	10	2 (Ongoing)	4
Ashish Goel	Ph. D., JIIT Noida	Asstt. Professor	Wireless Communication	11	2 (Ongoing)	4
Vipin Balyan	Ph. D., JUIT Waknaghat	Asstt. Professor	Wireless Communication	8	1 (Ongoing)	1
Vijay Khare	Ph. D., IIT Delhi	Asstt. Professor	Biomedical Signal Processing	14	2 (Ongoing)	2
Richa Gupta	Ph. D., JIIT Noida	Asstt. Professor	Image Processing, Joint Source & Channel Coding	9.5	2 (Ongoing)	3
Megha Agarwal	Ph. D., IIT Roorkee	Asstt. Professor	Digital Image Processing	2.5	1 (Ongoing)	NIL
Vineet Khandelwal	Ph. D., JNU Delhi	Asstt. Professor	Wireless & Optical Communication, Image Processing	14	2 (Ongoing)	4
Abhinav Gupta	Ph.D., JNU Delhi	Astt. Professor	Statistical Signal Processing	11	1(Ongoing)	3
Jasmine Saini	Ph. D., JIIT Noida	Asstt. Professor	Microwave Tubes	10.5	None	2
Rajesh Kumar Dubey	Ph. D., IIT Delhi	Asstt. Professor	Digital Signal and Speech Processing	13	1 (Ongoing)	3
Alok Joshi	Ph. D., JUIT Waknaghat	Asstt. Professor	Communication Systems	13	NIL	NIL

Bhartendu Chaturvedi	Ph. D., AMU	Asstt. Professor	Analog Signal Processing	5	1 (Ongoing)	NIL
Anil Rose	Ph. D., NIT Jalandhar	Asstt. Professor	Wireless Sensor Networks, Soft Computing	10	2 (Ongoing)	6
Shamim Akhter	Ph. D., JIIT Noida	Asstt. Professor	Signal Processing for VLSI	12	NIL	4
Madhu Jain	Ph. D., IIT Delhi	Asstt. Professor	Signal Processing	11.5	NIL	1
Rahul Kaushik	M. Tech. Univ. of Allahabad	Asstt. Professor	Optical Communications	10	NIL	1
Atul Kumar Srivastava	M. Tech. Manipal Univ.	Asstt. Professor	VLSI CAD	14	NIL	7
Bhawna Gupta	M. Tech. AMU Aligarh	Asstt. Professor	Communication Systems	11	NIL	1
Smriti Bhatnagar	M. Tech. Univ. of Allahabad	Asstt. Professor	Digital Signal Processing	20	NIL	5
Pankaj Kumar Yadav	M. Tech. IT-BHU Varanasi	Asstt. Professor	Wireless Communication	14	NIL	5
Reema Buddhiraja	M. Tech. Thapar Univ., Patiala	Asstt. Professor	Microwave Tubes	10	NIL	2
Satyendra Kumar	M. Tech., IIT Roorkee	Asstt. Professor	VLSI Systems	12.5	NIL	2
Shruti Kalra	M. Tech. GGSIPU Delhi	Asstt. Professor	CMOS Digital VLSI Design	8	NIL	5
Neetu Singh	M. Tech. JIIT Noida	Asstt. Professor	Signal Processing	7	NIL	4
Pushpendra Singh	M. Tech. IIT Kanpur	Asstt. Professor	Signal Processing	11	NIL	2
Tanuj Chauhan	M. Tech. Univ. of NSW	Asstt. Professor	MEMS, VLSI	5	NIL	4
Juhi Gupta	M. Tech. MDU Rohtak	Asstt. Professor	Wireless Communication	8	NIL	3
Saurabh Chaturvedi	M. Tech. GGSIPU	Asstt. Professor	VLSI Design	8	NIL	2
Kirmender Singh	M. Tech. JIIT Noida	Asstt. Professor	Analog CMOS Design	10.5	NIL	4

Amit Singhal	M. Tech. IIT Delhi	Asstt. Professor	Communication Systems	5.5	NIL	NIL
B. Suresh	M. Tech. IIT Delhi	Asstt. Professor	Communication Systems	6	NIL	3
Kapil Dev Tyagi	M. Tech. IIT Delhi	Asstt. Professor	Signal Processing	8	NIL	NIL
Rakhi Tomar	M. Tech. IIIT Allahabad	Asstt. Professor	VLSI Design	4.5	NIL	NIL
Monika	M. Tech. NSIT Delhi	Asstt. Professor	Meta-Materials, Antenna	4	NIL	4
Raaziyah Shamim	M. Tech. AMU	Asstt. Professor	Communication Systems	3 .5	NIL	NIL
Vinay Anand Tikkiwal	M.S. NTU Singapore	Asstt. Professor	Microelectronics	7	NIL	NIL
Priyanka Kwatra	M. Tech. IIT Delhi	Asstt. Professor	Wireless Sensor Network	2.5	NIL	NIL
Vishal Narain Saxena	M. Tech. IIT Roorkee	Asstt. Professor	Microwave Engineering	3.5	NIL	2
Shraddha Saxena	M. Tech. IIT Roorkee	Asstt. Professor	VLSI	5.5	NIL	3
Ruby Beniwal	M. Tech. IIT Roorkee	Asstt. Professor	Control System	5	NIL	NIL
Abhishek Khanna	M.E. BITS Pilani	Asstt Professor	Communication Systems	2	NIL	NIL
Gaurav Verma	M. Tech. IIT Kh.	Asstt. Professor	Embedded Systems	10	NIL	4
Ankur Bhardwaj	M. Tech. NSIT Delhi	Asstt. Professor	VLSI and Embedded Systems	1.5	NIL	NIL
Abhishek Kashyap	M. Tech. IIT Delhi	Asstt. Professor	Communication Systems	3.5	NIL	NIL
Sumegha Yadav	M. Tech. NSIT Delhi	Asstt. Professor	Signal Processing	1.5	NIL	NIL
Anand Kumar	M. Tech. IITDM Jabalpur	Asstt. Professor	Microwaves and Antenna	1.5	NIL	NIL
Shivaji Tyagi	M. Tech. JIIT Noida	Asstt. Professor	Microelectronics, CMOS-MEMS	2.5	NIL	NIL
Sidhartha Sankar Rout	M. Tech. IIIT Delhi	Asstt. Professor	VLSI Design & Embedded Systems	3	NIL	NIL

Rajesh Singh Parmar	M. Tech. IIT Bombay	Asstt. Professor	Communication Systems	4	NIL	NIL
Jyoti Vyas	M. Tech. MNIT Allahabad	Asstt. Professor	Communication systems	3.5	NIL	NIL
Deeksha Chandola	M. Tech. JIIT Noida	Asstt. Professor	Microelectronics & Embedded Technology	1	NIL	NIL
Ritesh Sharma	M. Tech. IIT Bombay	Asstt. Professor	Control System	1	NIL	NIL
Baljit Kaur	M. Tech. NIT Kurukshetra	Asstt. Professor	VLSI Design	1	NIL	NIL

12. **List of senior Visiting Fellows, Adjunct faculty, Emeritus Professors:**

A B Bhattacharyya, Emeritus Professor

13. **Percentage of classes taken by temporary faculty programme - wise information:**

NIL

14. **Programme - wise Student Teacher Ratio**

UG: 22:1,   PG: 12:1

15. **Number of academic support staff (technical) and administrative staff: sanctioned, filled and actual: Technical Staff: Technicians for Lab**

	Sanctioned	Filled	Actual
Technical	15	15	15
Administrative	Centrally managed; shared by all departments		

16. **Research thrust areas as recognized by major funding agencies**

Following three thrust areas have been recognised:
(i)   Emerging Communication Technologies,
(ii)   Signal Processing, and
(iii) Digital Systems and VLSI.

(i) **Emerging Communication Technologies:**
  • There are four major components of a wireless communication

system: transmitter, receiver, communication channel and antenna. Work is going on in the following areas. One of the key technologies to meet the requirements of future 5G communication systems are expected to be met by Orthogonal Frequency Division Multiplexing (OFDM) and Multiple-Input–Multiple-Output (MIMO) systems.

- Improved algorithms for transmitters and receivers proposed to be used in 4G and 5G systems have been developed. Main thrust of the above work has been improving Transmitter and Receiver designs so that less power is required to be transmitted. This will reduce RFI to other users and thus also improve compatibility with other systems, and reduce biological hazard.

- Improved antenna designs for air-borne applications and antennas based on metamaterials are given so that they can give required performance even with lesser received signal strength. Improved channel models are given. This also helps in design of optimum signal processing and modulation techniques at the transmitter. Theoretical work has also been done to improve efficiency and bandwidth of Travelling Wave Tube (TWT) and Gyro-TWTs used in satellite communication and electronic warfare. Microwave Integrated Circuit (MIC) design using Substrate Integrated Waveguide (SIW) technology is carried out. This technique provides low loss signal transmission. Emphasis is also put on smart antenna technology.

- Wireless Sensor network design for different topologies and their optimization using numerical techniques is also carried out. Integration of WSN and UWB technology for remote sensing is also going on.

(ii) **Signal Processing**:
- Signal processing is a highly dynamic and all pervading field. Work is in progress in the areas: Brain-computer interface, Digital image processing, speech processing and Video signal processing. In the Brain-computer interface developed for disabled, a signal from EEG is analysed and used to provide an alternative channel for disabled for communicating with the external world.

- Speech processing algorithms for 4G and next generation mobile communication and voice over internet protocol (VOIP) services.

- Fast algorithms for real time signal and low order Digital IIR Filters with linear phase characteristics which will improve the quality of video communication.

- Work is also going on for designing new digital recursive filters with magnitude characteristics closed to ideal ones and linear phase response. The linear phase response makes them useful in various signal processing applications such as control, radar, sonar, communication, and medical.

(iii) Digital Systems and VLSI:
- In the area of Digital Systems, our approach has been more development oriented using commercial software. We have software suites like Mentor Graphics IC station, Cadence, Xilinx ISE 10.1, ATLAS, and Coventor. Our students get hands-on training on the above.

- The VLSI activities in the department are primarily in the area of designing standard digital blocks and analog blocks in deep submicron technology node. The standard digital cells designing activities are FPGA based real time system design, HDL implementation of DSP algorithms, Digital System Verification using System Verilog and analog blocks. This is primarily focused towards developing design methodology for standard cells of band gap reference, operational trans-conductance amplifier, temperature sensor. Various Biquad circuits are being made by using various active elements (e.g., OTA, DDCC, DVCC and CCCCTA).

- The department also have microelectronics devices activities in the area of characterization and modelling of conventional and unconventional MOSFET structure, organic thin film transistor. The bulk MOS in 22nm technology and below it throw considerable challenge to modelling community to include all short channel effect and still qualify as compact model. The VLSI group is augmented with standard VLSI tools of Full custom IC design Synopsis tools and Mentor Graphics HEP1.

- The research activities in the VLSI Group focus on Nano-scale Device Modelling: SRAM design, Standard cell library characterization for CMOS based combinational circuits, and novel devices based on multilayer thin film structures.

- Some of the work being undertaken includes: Study of influence of the dielectric host on the dielectric functions of Si nano-particles and charge trapping, finding out the suitability of silicon carbide in solar cell applications. The other activity is study of Fin-FET structure with the devices scaled to 18nm range. Nano-wire Fin-FET structures offer a promising future to MOSFETs. These can be scaled down to smaller dimensions without much increase in short channel effects.

17. **Number of faculty with ongoing projects from**

   a) National funding agencies: **Ongoing:** NIL, **Completed:** 03
   b) International funding agencies: None
   c) Total grants received: 17.37 Lacs

   Completed Projects: Total Grants: 17.37 Lacs

S. No.	Granting Agency	Project Title	Time	Grant Amount (Rs in Lac)	PI
1	AICTE	RPS Project- Development of IP Core for Real Time Audio and Video Surveillance System	2009-12	5	R. C. Jain
2	ISRO	Development of algorithms for Narrow Band Interference reduction in IRNSS received signal	2011-12	5.77	R. C. Jain
3	Mentor Graphics, India.	A Low-Voltage CMOS Test Chip for Thermal Sensor and RF Application on AMS 0.35 µm technology node using Mentor Graphics PDK	2012-14	6.6	A.B. Bhattacharyya

18. **Inter-institutional collaborative projects and associated grants received.**
   The department has so far not received any grant for collaborative research. However, a good number of faculty members of other institutes as well as some industry professionals have worked with different faculty members of department for supervising doctoral research. This collaboration has resulted in many Ph. D. theses at JIIT and other universities, as given below:

S. No.	JIIT Faculty	Collaborating Faculty	Subject Area	Outcome (Research Pub.)
1.	R C Jain	Dr Omar Farooq, AMU, Aligarh	Digital Image Processing	NIL
2.	Vikram Karwal	Prof. J. P. Gupta, Lingaya's University, Faridabad	Communication Engineering	NIL
3.	H. O. Gupta	Dr. Kaushik Saha, Samsung R&D, Noida	VLSI System	NIL
4.	Manish Kumar	Dr. P. R. Sharma, YMCA Univ. of Sc. & Tech, Faridabad	Analog Signal Processing	NIL
5.	Anil Rose	Dr. Arun Khosla, Dr. B. R. Ambedkar NIT, Jalandhar	Soft Computing	NIL
6.	S.V. Singh	Ms. Gunjan Gupta, JUIT, Waknaghat	Analog Signal Processing	NIL
		Mr. R.S. Tomar, Uttarakhand Tech. Univ.	Analog Signal Processing	01
7.	Shweta Srivastava	Ms. Sonali Kumari, AIT, New Delhi	Communication Engineering	NIL
		Mr. Arnab Chakraborty, KIT, Ghaziabad	Communication Engineering	NIL
		Ms. Sheelu Kumari, CIT, Ranchi	Communication Engineering	NIL
		Ms. Shatabdi Chakraborty, BIT, Mesra	Communication Engineering	NIL
		Dr. Megha Dadel, BIT, Patna	Communication Engineering	1
		Mr. Wriddhi Bhowmik BIT, Mesra	Communication Engineering	1

Details of publications are given at Annexure-I/ECE.

19. **Departmental projects funded by DST-FIST; UGC-SAP/CAS, DPE; DBT, ICSSR, AICTE, etc.; Total grants received. – 10.00 lakh**

S.No.	Granting Agency	Duration	Scheme	Grants received	PI
1.	AICTE	2009-12	MODROBS	10 Lac	R C Jain

20. **Research facility /centre with**
    - **State Recognition:** None
    - **National Recognition:** MEMS Design Centre, JIIT MEMS Design Centre (supported by NPMASS Program, Govt. of India).

    The centre for MEMS design was set-up in the year 2009 as a part of Institute's response to launch MEMS activity supported under National Program on Microelectronics and Smart Systems (NPMASS), Govt. of India. A core course, Microelectronics and MEMS Technology has been incorporated in the M.Tech (Microelectronics Systems and Embedded Technology) programme.
    Research work carried out:
    1. Design, Characterization and Modeling of On-Chip Inductors on Silicon Substrate - A test chip with inductor structures is prototyped using industry standard CAD design environment at AMS, Austria and characterization for the same is going on presently at CARE, IITD.
    2. SAW Oscillator for Temperature/Gas Sensor
    3. Design of Cantilever Beam based Resonators for low-frequency filtering applications.
    - **International Recognition:** None

21. **Special research laboratories sponsored by / created by industry or corporate bodies: Industry/ corporate sponsored:**
    NIL

22. **Publications:**

	Total Number of Publications: Peer Reviewed	129
1.	International Journals	127

	National Journals	2
2.	Monographs	1
3.	Chapter in Books	1
4.	Edited Books	0
5.	Books	4
6.	Books with ISBN with details of publishers	4
7.	Number listed in International Databases	293
8.	**Citation Index (Google)**	
	Range	1-38
	Average	4.3
9.	**Citation Index (Scopus)**	
	Range	1-35
	Average	4.016
10.	**SNIP**	76.495
11.	**SJR**	38.772
12.	**Impact Factor (Scopus)**	
	Range	0 –5.298
	Average	1.019
13.	**H-index (Scopus) average**	27.06

Publication details are attached as **Annexure-II/ ECE**

23. **Details of patents and income generated:** NIL

24. **Areas of consultancy and income generated**

H. O. Gupta, "Design of 5000KVA 11/.69 KV Transformers (Completed on Jan. 2015)"Area of Consultancy: Transformers and Electromagnetic Devices.
Income generated: Rs. 35,000/-

25. Faculty selected nationally / internationally to visit other laboratories / institutions / industries in India and abroad

The following faculty visited and delivered lecture in various University/Institute as given below:

<table>
<tr><td>
A. B. Bhattacharyya

"Microsystem Design on Silicon", Jan 28, 2014, Devi Ahilya University, Indore
"MOSFET Modeling for VLSI Design", Jan 29, 2014, Devi Ahilya University, Indore.
"Acoustic Wave Sensors", May 13, 2013, Jadavpur University, Kolkata.
COMPACT MODEL: BACKGROUND MATERIAL", December 03, 2012, Delhi University, Delhi.
"Revolutionary Ideas in Microelectronics and their Social Impact", October 12, 2012, Delhi University, Delhi.
"MOSFET and Interconnect Inductor Modeling for VLSI Design", October 16, 2012, Jadavpur University, Kolkata.
"Micro and Nano Electronics Charitmanas – Where small is not so small-", April 27, 2012, under INSPIRE PROGRAM Initiative, Meerut.

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<tr><td>
Gaurav Verma:

"Low Power Techniques on Embedded System Design", May 5, 2014, Shivalik College of Engineering, Dehradun.

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<tr><td>
Tanuj Chauhan:

MEMS Technology, June 3, 2014, Cadence Design Systems, NOIDA.

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26. Faculty serving in
  a) National committees
  b) International committees
  c) Editorial Boards
  d) any other (please specify)

(a)   National Committees

S. No.	Name of Faculty	Details
1	A B Bhattacharyya	- Member - MOS-AK International Compact Model Group - Chairman BOG, NIT Durgapur

3	R C Jain	▪ Member, Program Committee, International Conference on VLSI System and Applications (VLSI-SATA - 2015), at Amrita University, Bangalore ▪ Member, Program Committee, ICIIS -14, Dec, 2014, IIIT, Gwalior.
4	Richa Gupta	▪ Secretary, Board of Trustees of FIM (Forum of Interdisciplinary Mathematics). Meerut, U.P.

(b) International Committees

Name of Faculty	Details
A.B Bhattacharyya	• International Advisor - Taoyaka Program for Graduate Studies, Hiroshima University, Japan (March 2014-ongoing)

(c) Editorial Committees

Name of Faculty	Details
Jitendra Mohan	Journal of Advanced Electrical and Computer Engineering

27. Faculty recharging strategies (UGC, ASC, Refresher / orientation programs, workshops, training programs and similar programs).

To recharge the faculty, Department organizes conferences, workshops, seminars, expert talks, refresher courses, faculty development programs, etc., at JIIT. In addition, faculty members also participate in these activities outside JIIT.

The academic activities organized at the Institute are listed below:

(i) Visitors/ Guest Speakers: 9
1. Sumantra Dutta Roy, IIT, Delhi, Nov 5, 2014- Empirical Mode Decomposition based Techniques.
2. Sh. R. K. Singh, Consultant (Former Engineer-in-Chief, Doordarshan), New Delhi, April 18, 2014 - Recent Trends in broadcasting in India.
3. N. N. Sharma, Coordinator- Nanomaterials and National MEMS Design Centre, BITS, Pilani, Feb. 22, 2014 - MEMS Design: A paradigm shift in design thought process.
4. R K Shevgaonkar, IIT Delhi, Dec 12, 2013- Photonic Crystals.
5. Surendra Prasad (Ex-Director, IIT Delhi), Dec 12, 2013- Broadband Access Network Technologies.
6. Jaromir Pistora, Technical University of Ostrava, Czech Republic,

Dec 13, 2013- Nonlinear Optical Devices.

7. Banmali Singh Rawat, Univ. of Nevada, Reno, USA, Dec 14, 2013 - Recycling the Light.

8. Sneh Anand, IIT, Delhi, Dec. 14, 2013- Intelligent Biomedical Systems.

9. Sunil Kumar, San Diego State University, San Diego, Aug. 3, 2013- Video Compression Techniques and Standards.

(ii) Workshops/ Conferences/ FDP:

1. IEEE International Conference on Signal Processing and Communication (ICSC-2015), March 16-18, 2015.

2. IEEE International Symposium on Signal processing and Information Technology, Dec 15 – 17, 2014.

3. Two weeks ISTE Workshop on Control Systems, Dec. 2 -12, 2014.

4. Two days workshop on Low Voltage and Low Power VLSI Design, Aug. 22 - 23, 2014.

5. Two weeks ISTE Workshop on "Signals and Systems", Jan. 2-12, 2014.

6. IEEE International Conference on Signal Processing and Communication (ICSC-2013), Dec. 12-14, 2013.

7. Workshop on "COMSOL Multiphysics for MEMS Modeling", Nov. 15, 2013.

8. Workshop on "Semiconductor Device Modeling using TCAD Tools", Oct. 4, 2013.

9. A 5-days workshop on "Design for Testability", K Chakraborty, FIEEE, Duke Univ., USA, July 23-27, 2013.

10. A 5-days IUCEE workshop on "Embedded Systems", by Vijaynarayanan, FIEEE, Penn State Univ., USA, July 1-5, 2013.

11. CMOS Technology and Spice Model: MOS-AK India, Tutorial, March 18, 2012.

12. A 2-daysworkshop on "Device Modeling for Microsystems", March 16-17, 2012.

13. A 1-day workshop on "Wavelets & its Applications", April 20, 2011.

14. A 1-day workshop on "Microwave Tubes", Nov 27, 2010.

15. FDP on Signal Processing & Communications, 21st -26 th July, 2014.

28. Student projects

Percentage of students who have done in-house projects including inter-departmental projects

100%

Percentage of students doing projects in collaboration with other universities

/ industry / institute
0%

29. Awards / recognitions received at the national and international level by Faculty:

S. No.	Name of Faculty	Detail of Research Award
1	Hari Om Gupta	1. Commendation Certificate from University of Roorkee, "Novel Techniques for electronic circuit analysis" Year: 1980
		2. Certificate Merit, Institution of Engineers (India)"An algorithm for solving nonlinear electronic circuit equations: Year: 1985 With: Dr. J.D. Sharma)
		3. Silver Medal and Cash Prize of Rs.500/- Khosla Annual Research Prize-1989 "New One-Cycle static blocking scheme for inrush in differential relay" Year: 1989 With: (Dr. B.T. Desai & Prof. M.K. Vasantha)
		4. Best Research Paper Award by "Hari Ohm Ashram Preit Shri Bhaikaka Inter University Smark Trust" Sardar Patel University. "New One-Cycle static blocking scheme for inrush in differential relay" in the Year: 1991 With:(Dr. B.T. Desai & Prof. M.K. Vasantha)
		5. CBIP Best Research Award "A Novel approach to computation of energy of non-linear loads" Year: 1992-93 With: (Dr. (Ms.) Indra Gupta, Mr. S.K. Joshi and others)
		6. Certificate of Merit from the Instiution of Engineers (India)"Investigation of Power System Voltage Security Analysis Under Critical Loading Conditions" Year:1993-94 With:(Dr. V. Bhanot and Prof. J.D. Sharma)
		7. Certificate of Merit from the Institution of Engineers (India) "Adaptive Coordination of Directional Over Current Relays of Electrical Power Systems Using Network Decomposition Approach" Year :1994-95 With: (Dr. N.A. Laway)
		8. Khosla Award Cash Prize of Rs. 1500/- and a Medal, IIT, Roorkee. "New Reduction Technique for Distribution Feeders" Year: 2001-2002 With: (Dr. (Ms.) Indra Gupta and Prof. M.K. Vasantha
		9. Union Ministry of Energy- Deptt. of Power Prize- from the Institution of Engineers (India) "A New Evolutionary

		Programming Based Approach for Optimal Selection of Wheeling Options" Year: 2003-2004 With: (Dr. N.P. Padhy and Dr. Y. R. Sood) 10. Best paper award in National Conference on Control and Instrumentation, held at Department of Electrical Engineering, NIT, Kurukshetra , Dec.29-30,2007 on research paper "Biased Reduced Order Models using Dominant Pole Retention and Genetic Algorithm" With: (Mrs Nidhi Singh and Rajendra Prasad 11. Mathur Vaishya Ratna (Highest Community award on social work) 2012.
2	A.B. Bhattacharyya	1. Hari Om Vikram Sarabhai Award (1980) 2. Bhasin Award (1984) 3. Khosla National Award (1986) 4. S. N. Mitra INAE Award (2005) 5. Indian Semiconductor Association (ISA) Techno Visionary Award (2009) 6. Fellow (The Founder Member) - Indian National Academy of Engineering(INAE)
3	R. C. Jain	1. Canadian Commonwealth Scholarship and Fellowship 1983. 2. CDIL Award of the Institution of Electronics and Telecommunication Engineers (IETE), India in the year 1994 for the best paper in Industrial Applications category. 3. K S Krishnan Memorial Award of the IETE, India in the year 1999 for a paper published in the IETE Journal of Research. 4. IETE-Students' Journal Award, 2006 for a paper published in IETE Journal of Education. 5. Foreign IT Professor, GIST, South Korea under the Govt. of South Korea Scheme "Brain Korea- 21": a. July 2002 to June, 2004, and b. July 2006 to June, 2007

Doctoral / post doctoral fellows: NIL

Students:

1. Delhi Technological University (DTU): INNOVA 2011

Name Of Event & Date	Position/ Prize	Team Members- Branch/Year

Life on the edge (22-02-2011)	1st Prize	Prateek Agnihotri, Laxmikant Ratnawat -2nd yr ECE, Swadeep Chaturvedi-1st yr ECE, Avnessh Singh-1st yr CSE
Robo Rush	Certificate	Abhishek Chauhan - 2nd yr ECE
Aqua Rush	Certificate (4th)	Abhishek Chauhan-2nd yr ECE
Robo combat	3rd Prize	Akash Shukla, Rituraj Singh, Rahul Sinha, Akash Chaudhry-2nd yr ECE
Robo Rush	Certificate	Akash Shukla, Rituraj Singh, Rahul Sinha, Akash Chaudhry-2nd yr ECE
Robo Rush/ Robo combat	Certificate	Ravi Jain- 1st yr ECE
Boat Rush	1st Prize	Ritu Raj Singh, Akash Shukla, Rahul Sinha-2nd yr ECE

2. IIT Kharagpur-Techfest-2011

Name Of Event	Position/ Prize	Team Members
RAFT	Finalists (Certificate)	Shivam Mittal-2nd yr ECE Rishabh Shukla, Prerit Jain, Arpit Gupta-2nd yr CSE
RAFT	Certificate	Ravi Jain, Sarthak Bhatnagar, Rachit Jain--1st yr ECE

3. Gnix- 2011-Galgotia University

Name Of Event	Position/ Prize	Team Members
Line-X	Certificate	Rituraj Singh-2nd yr Akash Shukla, Rahul Sinha, Akash Chaudhary-2nd yr

4. Amity Youth Fest 2012, Feb 09-12, 2012

Name Of Event	Position/ Prize	Team Members
Robo war	1st Prize	Rituraj Singh, Akash Shukla, Rahul Sinha, Mayank Juneja-3rd yr ECE

Soccer Mania	1st Prize	Rituraj Singh, Akash Shukla, Rahul Sinha, Mayank Juneja-3rd yr ECE
F-1	1st Prize	Rituraj Singh, Akash Shukla, Rahul Sinha-3rd yr ECE
Roborace	2nd Prize	Rituraj Singh, Akash Shukla, Rahul Sinha-3rd yr ECE

5. IIT Kharagpur-Techfest-2012

Name Of Event	Position/ Prize	Team Members
Inferno	Finalists	Ravi Jain, Abhinav Gupta, Piyush Agarwal-2nd yr ECE, Rishi Golyan-2nd yr CSE
Inferno	Finalists (Certificate)	Shivam Sharma, Amulya -1st yr ECE Rajan Middha-1st yr CSE
Stasis	Finalists (Certificate)	Abhinav Gupta, Piyush Agarwal, Rishi Golyan -2nd yr CSE, Ravi Jain-2nd yr ECE

6. DTU Techfest 2012

Name Of Event	Position/ Prize	Team Members
Stasis	1st Prize	Ravi Jain-2nd yr ECE
		Abhinav Gupta-2nd yr CSE
Robo Soccer	2nd Prize	Ravi Jain, Anshumaan Singh-1st yr ECE
Robo Race	Finalists (Certificate)	Ravi Jain, Anshumaan Singh-1st yr ECE
Robo War	Finalists (Certificate)	Ravi Jain, Anshumaan Singh-1st yr ECE

List of awards and achievements in events other than Robotics for students

S.No.	Name of	Detail of the Conference /	Nature of the

	Students	Technical Event / Technical competition	award
1	Manoj Makhija, Kapil Goyal	Switching Application using Hand Gesture Recognition" in ICRAIE, Rajkot.	Published a paper
2	Kapil Verma	Inter College Robotics events organized at Amity Youth festival, 2013, Delhi	1st position
3	Kapil Verma	Inter College HTML reverse engg competition organized at GDG Bharti Vidyapeeth	2nd position
4	Anubhav Gupta	Inter College Robotics events organized at Amity Youth festival, 2013.	2nd position
5	Abhinav Gupta	Inter college Robotics organized at DTU, Delhi	1st position
6	Abhinav Gupta	For developing website for International SFIA conference organized by AKS Services UK Ltd in 2013	Selected
7	Harshit Jain	To develop medical adherence applications in 2013.	Research assistantship from (University of Notre dame, USA)

30. Seminars/ Conferences/Workshops organized and the source of funding (national/ international) with details of outstanding participants, if any.

S.No.	Name of Seminars/ Conferences/Workshops	Source of funding	Outstanding participants
1	**IEEE International Conference on Signal Processing and Communication (ICSC-2015), March 16-18, 2015.** **Funding Agencies:** JIIT, Noida. **Outstanding Participants:** **Prof. U. B. Desai,** IIT Hyderabad, **Prof. V. K. Jain,** IIT Delhi, **Prof. Banmali S. Rawat,** University of Nevada, USA, **Dr. Kiyotoshi Yasumoto,** Kyushu University, Japan, **Prof. Jaromir Pistora ,** VSB-Technical University of Ostrava, **Prof. Vinod Kumar,** IIT Roorkee.		

2	**IEEE International Symposium on Signal Processing and Information Technology (ISSPIT-2014), Dec. 15-17, 2014.** **Funding Agencies:** JIIT, Noida and IEEE, USA **Organized jointly with University of Florida, USA** **Outstanding participants:** **Prof. Sartaj Sahni,** University of Floridas,USA**, Prof. Mohamed H. Ahmed**, Memorial U of Newfoundland, Canada, **Prof. Reda Ammar**, University of Connecticut, USA, **Prof. Adel Elmaghraby**, University of Louisville, USA, **Prof. Sanguthevar Rajasekaran**, Univ. of Connecticut , USA.
3	**Ten days workshop on Control Systems, Dec. 2-12, 2014.** **Funding Agencies:** NMEICT (National Mission on Education through ICT) funded by MHRD, GOI and organized in conjunction with IIT, Kharagpur. **Outstanding participants:** **Siddhartha Mukhopadhyay**, Department of Electrical Engineering, IIT Kharagpur, **Tapan Kumar Ghoshal**, Department of Electrical Engineering, Jadavpur University, Kolkata
4	**Two days workshop on "Low Voltage and Low Power VLSI Design", Aug. 22 - 23, 2014** **Funding Agencies:** JIIT, Noida. **Outstanding Participants:** **A. B. Bhattacharya**, JIIT Noida, **G. S. Visweswaran** (Professor, Department of Electrical Engineering, IIT Delhi), **Kaushik Saha** (Director, Systems R&D, Samsung India), **Shouribrata Chatterjee** (Associate Prof., Department of Electrical Engineering, IIT Delhi)
5	**Two week ISTE Workshop on "Signals and Systems", Jan. 2-12, 2014.** **Funding Agencies:** IIT, Kharagpur under MHRD programme, NMEICT held at ECE Deptt., JIIT, Noida **Outstanding Participants:** **Vikram M. Gadre**, IIT Bombay, **Somnath Sengupta**, IIT, Kharagpur
6	**IEEE International Conference on Signal Processing and Communication (ICSC-2013), Dec. 12-14, 2013.** **Funding Agencies:** JIIT, Noida. **Outstanding Participants:** **R K Shevgaonkar**, IIT Delhi, **Surendra Prasad**, Ex-Director, IIT Delhi, **Kiseon Kim**, GIST, South Korea, **Jaromir Pistora**, Tech. Univ. of Ostrava, Czech Republic, **Banmali S. Rawat**, Univ. of Reno, USA
7	**Workshop on "COMSOL Multiphysics for MEMS modeling", Nov. 15, 2013.**

	Funding Agenciy: JIIT, Noida **Outstanding participants:** **Hemant Kumar** and **Technical Team**, Comsol, India
8	**Workshop on "Semiconductor Device Modeling using TCAD Tools", Oct. 4, 2013.** **Funding Agency:** JIIT, Noida **Outstanding participants:** **Amit Saini**, and **Technical Team**, Cadence Design System, Noida
9	**Workshop on "Design for Testability", July 23-27, 2013.** **Funding Agency:** JIIT, Noida **Outstanding Participants:** **Krishnendu Chakrabarty**, Professor, Duke University, USA
10	**Workshop on "Embedded Systems", July 1-5, 2013.** **Funding Agency:** JIIT, Noida **Outstanding Participants:** **Vijaykrishnan Narayanan**, Professor, Pennsylvania State University, USA
11	**International workshop MOS-AK/GSA India 2012 on "Device Modeling for Microsystems", March 16-18, 2012.** **Funding Agencies:** JIIT, co-organized by Indian National Academy of Engineering (INAE), supported by DIT, GOI and CSIR. **Outstanding Participants:** **Mitiko Miura,** Mattausch Hiroshima University, Japan, **Narain D. Arora,** Silterra, Malaysia, **Yogesh Chauhan,** UC Berkeley, **Xing Zhou,** NTU, Singapore, **Ane Juge** STMicroelectronics, France, **Ehrenfied Seebacher,** AMS Austria, **Vaidyanathan Subramanian,** IBM, India, **Samar Saha,** SuVolta, USA, **Waldek Grabinski,** MOS-AK/GSA, EU, **Mike Brinson**, London Metropolitan University, London, **Thomas Gneting,** ADMOS, Germany, **V Ramagopal Rao**, IIT Mumbai, **A B Bhattacharyya**, JIIT, Noida, **Shantanu Mahapatra**, IISc, **Anitava Dasgupta**, IIT, Chennai, **M Jagdesh Kumar**, IIT, Delhi, **Navakanta Bhat**, IISc., **Soumya Pandit**, IPRE, Kolkata
12	**Workshop on "Wavelets & its Applications", April 20, 2011.** **Funding Agencies:** JIIT **Organized jointly with Mathematics Department JIIT,Noida** **Outstanding Participants:** **S. D. Joshi,** IIT Delhi, **Mani Mehra,** IIT Delhi, **Pradeep Sarkar,** IIT Kanpur, **Umar Farooq,** AMU, Aligarh, **Bani Singh,** JIIT Noida
13	**Workshop on 'Microwave Tubes' Nov. 27, 2010** **Funding Agencies**: JIIT **Outstanding Participants:** **B. N. Basu,** BHU Varanasi**, Lalit Kumar,** MTRDC Bangalore**, R. S.**

		Raju, CEERI**,** Pilani**.**

31. Code of ethics for research followed by the departments

Department follows the code of ethics defined by the Institute. The ethics lays strong emphasis that all research work/theses must be original and work by others is duly acknowledged. To enhance quality of research anti-plagiarism software is used. Research scholars and students before submitting their Ph. D. theses, dissertations, Project reports and research papers for award/publications check manuscripts for plagiarism.

32. Student profile programme-wise:

Name of the Programme (refer to question no. 4)	Applications received*	Selected		Pass Percentage	
		Male	Female	Male	Female
B. Tech. (ECE)					
2007-11	22629	146	27	143 (97.95)	27 (100)
2008-12	32517	122	51	117 (95.90)	50 (98.04)
2009-13	31710	215	58	209 (97.21)	58 (100)
2010-14	26650	314	79	289 (92.04)	79 (100)
DUALM-ECE					
2006-11	19372	18	8	18 (100)	8 (100)
2007-12	22229	7	0	5 (71.43)	0 (NA)
2008-13	32017	19	24	18 (94.74)	23 (95.83)
2009-14	31210	22	6	19 (86.36)	6 (100)
Dual Degree B.Tech- M. Tech. (ECE)					
2006-11	19872	NA	NA	NA	
2007-12	22629	NA	NA	NA	
2008-13	32517	NA	NA	NA	
2009-14	31710	NA	NA	NA	
M. Tech. (ECE)					
2009-11	445	6	8	5 (83.33)	8 (100)
2010-12	1310	19	9	18 (94.74)	9 (100)
2011-13	1943	7	13	6 (85.71)	13 (100)
2012-14	1600	7	10	7 (100)	10 (100)
M. Tech. (MET)					
2009-11	445	11	2	10 (91.91)	1 (50)
2010-12	1310	12	2	12 (100)	2 (100)
2011-13	1943	10	2	10 (100)	2 (100)

2012-14	1600	14	6	12	6 (100)
Ph. D.					
Upto 2014	**432**	**45**	**20**	**Passed:** **Continuing:** **Discontinued:**	9(5M, 4F) 35 (22M, 13F) 21 (18M, 3F)

*Applications for the UG programs are received without any choice of program. The choice of program is exercise by the candidate during counselling. Hence individual program based applications is not possible to be indicated.

#The application for PG programs are received under two categories (i) GATE & (ii) PGET. Each candidate is allowed to exercise multiple choices of programs where he/she is eligible. Therefore individual program based applications is not possible to be indicated.

33.   Diversity of students Data

Name of the Programme (refer to question no. 4)	% of students from the same university	% of students from other universities within the State	% of students from universities outside the State	% of students from other countries
B.Tech. (ECE)				
2011	NIL	2.63	97.13	0.24
2012	NIL	4.61	94.93	0.46
2013	NIL	5.34	94.66	NIL
2014	NIL	6.78	93.22	NIL
DUALM-ECE				
2011	NA	NA	NA	NA
2012	NA	NA	NA	NA
2013	NA	NA	NA	NA
2014	NA	NA	NA	NA
Dual Degree B.Tech- M. Tech. (ECE)				
2011	NA	NA	NA	NA
2012	NIL	3.85	96.15	NIl
2013	NIL	4.35	95.65	NIL
2014	NIL	NIL	100	NIL
M. Tech. (ECE)				
2009-11	7.69	53.85	38.46	NIL

2010-12	NIL	35.71	64.29	NIL
2011-13	NIL	50.00	50.00	NIL
2012-14	NIL	52.94	47.06	NIL
2013-15	3.45	51.72	44.83	NIL
2014-16	NIL	42.86	57.14	NIL
M. Tech. (MET)				
2009-11	NIL	46.15	53.85	NIL
2010-12	NIL	71.43	28.57	NIL
2011-13	NIL	66.67	33.33	NIL
2012-14	NIL	60.00	40.00	NIL
2013-15	NIL	75.00	25.00	NIL
2014-16	NIL	53.57	46.43	NIL
Ph. D. (ECE)				
2011	NIL	NIL	100	NIL
2012	10.00	20.00	60.00	10.00
2013	16.67	33.33	33.33	16.67
2014	16.67	16.67	66.66	NIL

34. **How many students have cleared Civil Services and Defence Services examinations, NET, SET, GATE and other competitive examinations? Give details category-wise.**

   GRE: **90+ students**
   GATE: **120+ students**
   CAT/MAT: **80+ students**

35.   **Student progression**

   The Department does not have a systematic data on the progression of students after graduating like proceeding for higher studies or joining the companies after placements, etc. However, the details of alumni available indicate following trends:

Student progression	Percentage against enrolled
UG to PG	15-20 %
PG to M.Phil.	NA
PG to Ph. D.	~20%
Ph. D. to Post-Doctoral	NIL
Employed • Campus selection	~85%

• Other than campus recruitment	~10%
Entrepreneurs	0.5%

36. **Diversity of staff**

Percentage of faculty who are graduates	PG	Ph. D.
of the same university	8	23
from other universities within the State	16	14
from universities from other States	71	54
universities outside the country	5	9

For Ph. D., percentage is calculated out of total Ph. D. degree holders and **Percentage of PG is calculated out of total strength of the Department.**

37. **Number of faculty who were awarded M.Phil., Ph. D., D.Sc. and D.Litt. during the assessment period**

Total no. = 17

The following faculty have acquired Ph. D. Degree within assessment period:
Samir Dev Gupta, Manish Kumar, Richa Gupta, Ashish Goel, Jasmine Saini, Shamim Akhter, Abhinav Gupta, Vineet Khendelwal, Vijay Khare, Rajesh Kumar Dubey, Jitendra Mohan, Sajai Vir Singh, Vipin Balyan, Megha Agrawal, Bhartendu Chaturvedi, Alok Joshi, and Madhu Jain.

38. **Present details of departmental infrastructural facilities with regard to**

a) **Library:** Department uses the Central Learning Resource (Central Library)

b) **Internet facilities for staff and students:**

   i.   All the faculty members are provided with desktop computer and internet connections. All faculty have access to Institute webkiosk and webmail.

   ii. Students are provided with internet connections with Wi-Fi access. They are also provided access to the webkiosk with personal login

   iii. Students can access internet from anywhere within the campus as the entire Institutional campus area is Wi-Fi enabled.

iv.   Class rooms, lecture theatres, and labs are also equipped with internet facilities for teaching and demonstration.

c)   **Total number of class room:** 49 Lecture Theatres/ Class Rooms and 42 Tutorial Rooms of the Institute are shared with other departments

d)   **Class rooms with ICT facility:** 40 Lecutre Theatres/ Class Rooms.

e)   **Student's Laboratories**

Laboratory	Location	LAN	PC	Power backup	Capacity	Display Board	White Board
Sector-62							
Basic Electronics Lab -1	ABB-I	N	N	Y	32	Y	Y
Basic Electronics Lab -2	ABB-I	N	N	Y	32	Y	Y
Electronic Devices Lab	ABB-I	N	N	Y	32	Y	Y
Electrical Machines Lab	ABB-I	N	N	Y	32	Y	Y
Signal Processing Lab	ABB-I	Y	Y	Y	32	Y	Y
Communication Systems Lab	ABB-I	N	N	Y	32	Y	Y
Advance Communication Systems Lab	ABB-I	Y	Y	Y	32	Y	Y
VLSI / VHDL Lab	ABB-I	Y	Y	Y	32	Y	Y
B Tech Project Lab – 1	ABB-2	Y	Y	Y	40	Y	Y
Embedded Systems Lab	Under Development						
Sector-128							
Basic Electronics Lab	Block A	N	N	Y	30	Y	Y
Communication Systems Lab	Block B	N	N	Y	30	Y	Y
VLSI Lab	Block B	Y	Y	Y	37	Y	Y
DSP Lab	Block B	Y	Y	Y	30	Y	Y
Electromagnetics Lab	Block B	Y	Y	Y	30	Y	Y
Analog & Digital Electronics Lab	Block A	N	N	Y	30	Y	Y
Creativity and Innovation Lab	Block B	N	N	Y	20	Y	Y
Electrical Machines and Instrument Lab.	Block A	N	N	Y	30	Y	Y

f) Research Laboratories

Laboratory	Location	L A N	P C	Power backup	Capacity	Display Board	White Board
MEMS Lab	ABB-2	N	N	Y	13	Y	Y
B Tech Project Lab – 2	Under Development						
M Tech Project Lab	Under Development						

39.   List of doctoral, post-doctoral students and Research Associates

a) From the host institution/university
Awarded: 7
Pursuing: 20

S. No	SCHOLAR	SUPERVISOR	STATUS
1	Samir Dev Gupta	M. C. Srivastava	Completed
2	Manish Kumar	M. C. Srivastava	Completed
3	Ashish Goel	Prerana Gupta	Completed
4	Richa Gupta	N. Kalyanasundaram, B. D. Sharma	Completed
5	Jasmine Saini	N. Kalyanasundaram	Completed
6	Shamim Akhter	Vikram Karwal, R.C. Jain	Completed
7	Pankaj Kumar Yadav	Vikram Karwal, J.P.Gupta	Ongoing
8	Smriti Bhatnagar	R.C. Jain	Ongoing
9	Bhawna Gupta	R.C. Jain, Omar Farooq	Ongoing
10	Reema Budhiraja	R. C. Jain, Manish Kumar	Ongoing
11	Juhi Gupta	Vikram Karwal, Vivek Dwivedi	Ongoing
12	Neetu Singh	R.C. Jain, Abhinav Gupta	Ongoing
13	Rahul Kaushik	R.C. Jain, Vineet Khandelwal	Ongoing
14	Tanuj Chauhan	Vikram Karwal	Ongoing
15	Vinay A. Tikkiwal	Hari Om Gupta, S. V. Singh	Ongoing
16	Ankur Bhardwaj	Manish Kumar	Ongoing
17	Shruti Kalra	A. B. Bhattacharyya	Ongoing
18	Kirminder Singh	A. B. Bhattacharyya	Ongoing
19	Manu Prakash	Vikram Karwal	Ongoing
20	Manish Agrawal	Vijay Khare	Ongoing

21	Raazia Shamim	R. C. Jain, Shweta Srivastava	Ongoing
22	Gaurav Verma	Manish Kumar, Vijay Khare	Ongoing
23	Anand Kumar	Hariom Gupta , Jitendra Mohan	Ongoing
24	Abhishek Kashyap	Megha Agarwal, Hari Om Gupta	Ongoing
25	Shivaji Tyagi	R. C. Jain	Ongoing

b) **From other Institutions/Universities**

S. NO	SCHOLAR	SUPERVISOR	STATUS
1	Amita Agnihotri	N. Kalyanasundaram	Completed
2	Tarun Kumar	R.C. Jain	Ongoing
3	Amit Garg	A. K. Singhal	Ongoing
4	Naveen Jaglan	Samir Dev Gupta	Ongoing
5	Prasanna Kumar Singh	Samir Dev Gupta	Ongoing
6	Chanadra Shankar Porwal	S. V. Singh	Ongoing
7	Aashima Garg	Ashish Goel	Ongoing
8	Manoj Kumar Garg	S. D. Gupta	Ongoing
9	Tanvi Agrawal	Vipin Balyan	Ongoing
10	Suman Bharti	Richa Gupta	Ongoing
11	Abhishek Jain	Richa Gupta	Ongoing
12	Atul Kumar	Bhartendu Chaturvedi	Ongoing
13	Shilpi Shukla	Rajesh Dubey	Ongoing
14	Saruti Gupta	Ashish Goel	Ongoing

40. **Number of post graduate students getting financial assistance from the university**

Year	M.Tech	Ph. D.
2011-12	10	0
2012-13	10	3
2013-14	13	2
2014-15	43	3

41. **Was any need assessment exercise undertaken before the development of new programme(s)? If so, highlight the methodology.**

The   emerging   trends   in   industry,   research,   government   policies,

recommendations of the professional bodies, and JIIT management's directives motivate and guide the department to design new programs. International and nationally renowned academicians from many top rated institutes and industry professionals including our alumni are consulted while conceptualising and designing new programs or revising the curricula of existing degree programs. All the faculty members are requested to give their suggestions and finally the meeting of the BOS including external experts is held for reviewing the curriculum.

42. **Does the department obtain feedback from**

    i. **Faculty on curriculum as well as teaching-learning-evaluation? If yes, how does the department utilize the feedback?**

       Yes. Feedback from faculty is obtained during departmental meetings.

    ii. **Students on staff, curriculum and teaching-learning-evaluation and how does the department utilize the feedback?**

       Yes. At the end of every semester, students' feedback is collected for teaching faculty. The average weighted calculated score is communicated to concerned faculty for remedial measures.

    iii. **Alumni and employers on the programmes offered and how does the department utilize the feedback?**

       Yes. Feedback from alumni is collected through email and the same is documented at departmental level. Important points from feedback are discussed intra-departmentally.
       Online feedback collection mechanism has been operationalized from Academic Session 2014-15 through Institute Quality Assurance Cell (IQAC).

43. **List the distinguished alumni of the department (maximum 10)**

S.No	Name, Year of Graduation from JIIT, Achievements and current affiliation
1	Nishant Mittal, 2009 Director, Sybly Industries Ltd.
2	Anurag Gupta, 2011 Director, Nova Biological Systems Pvt. Ltd.
3	Jaspreet Singh Makkar, 2013 Founder, Meissa Technologies Pvt. Ltd.
4	Siddhant Gupta, 2013

	Director, ABC Technology Ltd.
5	Chetan Chaturvedi, 2006 Vice President Sales, 6D Telecom Solutions Pvt. Ltd.
6	Mayank Agarwal, 2014 Founder, ANDROBO
7	Karan Kumar, 2009 Senior Business Specialist, Cigna Health Care

44. **Give details of student enrichment programmes (special lectures/ workshops / seminar) involving external experts.**

1. IEEE International Symposium on Signal Processing and Information Technology (ISSPIT-2014), Dec. 15-17, 2014.
2. ISTE Workshop on "Control Systems" by Siddhartha Mukhopadhyay, IIT Kharagpur, Tapan Kumar Ghoshal, Jadavpur University, Kolkata on Dec. 2-12, 2014.
3. Workshop on "Low Voltage and Low Power VLSI Design" by G.S. Visweswaran, IIT Delhi, Kaushik Saha, Samsung India, Shouribrata Chatterjee, IIT Delhi on Aug. 22-23, 2014.
4. Workshop on "Signals and Systems" by Vikram M.Gadre, IIT Bombay, Somnath Sengupta, IIT, Kharagpur on Jan. 2-12, 2014.
5. IEEE International Conference on Signal Processing and Communication (ICSC-2013) by R K Shevgaonkar, IIT Delhi, Surendra Prasad, Ex-Director, IIT Delhi, Kiseon Kim, SIST, South Korea, Jaromir Pistora, Tech. Univ. of Ostrava, Banmali Singh Rawat, Univ. of Reno, USA on Dec. 12-14, 2013.
6. Workshop on "COMSOL Multiphysics for MEMS modeling" by Hemant Kumar and Technical Team, Comsol India on Nov. 15, 2013.
7. Workshop on "Semiconductor Device Modeling using TCAD Tools" by Amit Saini, Cadence Design System, Technical Team, Cadence Design System on Oct. 4, 2013.
8. A 5-day workshop on "Design for Testability" by Krishnendu Chakrabarty, Duke University, USA on July 23-27, 2013.
9. International workshop MOS-AK/GSA India 2012 on "Device Modeling for Microsystems" by Mitiko Miura, Mattausch Hiroshima University, Japan, Narain D. Arora, Silterra, Malaysia, Yogesh Chauhan, UC Berkeley, Xing Zhou, NTU, Singapore, Ane Juge STMicroelectronics, France, Ehrenfied Seebacher, AMS Austria, Vaidyanathan Subramanian, IBM, India, Samar Saha, SuVolta, USA, Waldek Grabinski, MOS-AK/GSA, EU, Mike Brinson, London Metropolitan University, London, Thomas Gneting, ADMOS,

Germany, V Ramagopal Rao, IIT Mumbai, A B Bhattacharyya, JIIT, Shantanu Mahapatra, IISc, Anitava Dasgupta, IIT, Chennai, M Jagdesh Kumar, IIT, Delhi, Navakanta Bhat, IISc., Soumya Pandit, IPRE, Kolkata on March 16-18, 2012.

10. Workshop on "Wavelets & Its Applications" in Signal Processing by S. D. Joshi, IIT Delhi, Mani Mehra, IIT Delhi, Pradeep Sarkar, IIT Kanpur, Omar Farooq, AMU Aligarh on April 20, 2011.
11. Workshop on "Microwave Tubes" by Prof. B.N. Basu, BHU Varanasi, Lalit Kumar, MTRDC Bangalore, R.S. Raju, CEERI Pilani on Nov. 27 2010.

45. List the teaching methods adopted by the faculty for different programmes.

1. Regular chalk/Marker and board
2. Exercises in tutorial classes.
3. Use of audio visual aids
4. Project based learning (Seminars, Minor and Major Projects)
5. Case study / peer-reviewed publication based learning
6. Technical Papers from IEEE, IET, Springer etc. Journals.

46. How does the department ensure that programme objectives are constantly met and learning outcomes are monitored?

The department monitors the learning outcomes through student's progressions (placement/ entrepreneurship/higher education). Institute Quality Assurance Cell (IQAC) is responsible for the day to day administration of quality assurance at JIIT.
Academic Council, Board of studies and Institute IQAC meet regularly to evaluate and provide course correction whenever required. This improves the quality in academics as well as in research processes. The feed backs are collected from faculty and course syllabi developed subsequently taking into consideration recent developments in the subject areas. The feedback of the outgoing and ex-students and their expectations from the program/course are reviewed regularly in department, faculty meetings. This aids in continuously developing the department's objectives.

47. Highlight the participation of students and faculty in extension activities.

Faculty and students of the department are actively involved in various

extension activities organized by JIIT. Details are given in Section 3.6 of Self Study Report of the Institute.

48. **Give details of "beyond syllabus scholarly activities" of the department.**

    Beyond syllabus scholarly activities
    (i)   Expert Lectures
    (ii)   Seminars, Workshops and Conferences
    (iii) Techno-cultural fests inter- and intra-university participation

49. **State whether the programme/ department is accredited/ graded by other agencies? If yes, give details.**
    Yes, the Department of ECE was NBA accredited for 5 years w.e.f. 22.01.2008

50. **Briefly highlight the contributions of the department in generating new knowledge, basic or applied.**

    Ph. D. Scholars, students' projects under the guidance of supervisors are involved in generation of new knowledge and contributions through publications as below:

S.No.	Paper Type	Total
1	International Journals	108
2	National Journals	2
3	International Conferences	120
4	National Conferences	7
5	Book Publication	1
6	Chapter Publication	0
Total		**238**

Some of the major contributions of the department are as under:
Dr. Manish Kumar: Designing of improved on-chip tunable bi-quad filter.
Dr. Rajesh K. Dubey: Weight dependent frames in multiple time-scale speech quality assessment.
Dr. Shamim Akhter: Development of update algorithms for DCT

computation.

Dr. Vipin Balyan: Assignment schemes for OVSF codes.

Dr. Jasmine Saini: Improved design for a TWT

Dr Richa Gupta: Improved RVLCs

Dr Ashsish Goel: Improvement in 4G cellular network technology

51. **Detail five major Strengths, Weaknesses, Opportunities and Challenges (SWOC) of the department.**

Strengths
i. Well qualified and experienced faculty
ii. Good infrastructure (Audio visual facility in all classrooms, good laboratory facility, library equipped with reference books and research journals)
iii. Flexibility in updating curriculum
iv. Strategic NCR location enabling collaborations with Institutions.

Weakness
i. Ability to attract very good employers in core areas of the ECE students
ii. High end Research Facilities
iii. Extension activity

Opportunities
i. Present national focus on research, development and manufacturing
ii. Multidisciplinary collaborative research with other departments of the Institute
iii. Institute location in National Capital Region (NCR).

Challenges
i. Quality High-end Research work
ii. Quality of M.Tech/ Ph. D. students
iii. Sponsored projects from Govt. and other institutions

52. **Future plans of the department.**
i. To secure national recognition in the area of Communication Engineering.
ii. Getting research projects from national and international agencies.
iii. To publish high quality research papers with journals having high metrics.
iv. To start M. Tech course in emerging area.

Publication published through collaborations

1. Tomar R.S.**, Singh S.V.**, Chauhan D.S., "Cascadable low voltage operated current-mode universal biquad filter," WSEAS Transaction on Signal Processing, vol. 10, pp. 345-353, 2014. **[H index 9].**

1. Publications in International Journals

2015

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1. **Gupta, J.**, **Karwal**, **V.**, and **Dwivedi, V. K.**, "Joint Overlay-Underlay Optimal Power Allocation in Cognitive radio", Wireless Personal Communication, Springer, Vol. 81, No. 4, March 2015. **[H index 29 Impact factor: 0.458]**
2. Jaglan, N. and **Gupta, S. D.**, "Design and Analysis of Performance Enhanced Microstrip Patch Antenna with EBG Substrate" International Journal of Microwave and Optical Technology (IJMOT), Volume 10, Number 2, pp 79-88, March 2015. **[H index 3]**
3. Shankar, C., Singh**, S. V.**, "A new trans-admittance-mode biquad filter using MO-VDTA," WSEAS Transaction on Circuits and Systems, vol. 14, pp. 8-18, 2015. EISSN: 2224-266X. **[H Index 10]**
4. **Chaturvedi, B.**, and Maheshwari, S., "Single Active Element Based Cascadable Band-Pass Filters for Low-Q Applications." Journal of Circuits, Systems and Computers, Volume 24, Issue 05, June 2015. ISSN: 0218-1266. **[H Index 19 Impact factor: 0.24]**

Others
5. **Chandola, D.**, and Garg, G., "Pull-in Voltage Analysis of Fixed Free Cantilever Beam", International Journal of Electrical Electronics & Computer Science Engineering Volume 1, Issue 1 (February 2014), ISSN : 2348 2273.
6. Gupta, G., **Singh, S. V.**, and Bhooshan S. V., "VDTA Based Electronically Tunable Voltage Mode and Trans-Admittance Biquad Filter." Circuits and Systems, Vol.6 No.3, March 2015. DOI: 10.4236/cs.2015.63010.

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7. **Kumar, A.**, Kumar, D., **Mohan, J.**, **Gupta, H. O.,** "Investigation of grid metamaterial and EBG structures and its application to patch antenna" International Journal of Microwave and Wireless Technologies, First View Articles, pp 1-8 doi: 10.1017/S1759078714000944, Published online by Cambridge University Press, 30 Jul 2014. **[H index 8 Impact factor: 0.327].**

8. **Goel, A**., Gupta Poddar, P., and Agrawal, M., "Generalized M-2M Mapping Scheme for SLM and PTS Based Methods for PAPR Reduction in OFDM Systems without Side-Information," Wireless Personal Communications, Springer, vol. 74, no. 2, pp. 285-305, Jan. 2014. **[H index 29 Impact factor: 0.458]**

9. Saini, D. S., and **Balyan, V**., "Top Down Code Search to Locate An Optimum Code and Reduction in Code Blocking for CDMA Networks, " Wireless Personal Communication, Springer, vol. 75, no. 1, pp. 465-482, 2014. DOI 10.1007/s11277-013-1372-9. **[H index 29 Impact factor: 0.458]**

10. Kinhekar, N., Padhy, N. P., and **Gupta, H. O.,** "Multiobjective demand side management solutions for utilities with peak demand deficit," International Journal of Electrical Power and Energy Systems, vol. 55, pp. 612-619, 2014. **[H index 54 Impact factor: 3.111]**

11. Mehta, A., **Kumar, S**., and Kumar, R., "A Novel Architecture and PVT Analysis of 4-Bit Manchester Carry Chain Block," International Journal of Applied Engineering Research, ISSN 0973-4562 Volume 9, Number 20, pp. 6581-6590, 2014. **[H index 3 Impact factor: 7.827]**

12. Kumar, T**., Kalyanasundarama, N**., Lande, B. K., "Analysis of the generalized case of scattering from a ferromagnetic microwire grid", Progress In Electromagnetics Research M, vol. 35, pp. 1-10, 2014. **[H index 12]**

13. **Dwivedi, V. K**., Singh, G., "Moment Generating Function Based Performance Analysis of Maximal-Ratio Combining Diversity Receivers in the Generalized-K Fading Channels", Wireless Personal Communications, pp. 1-17, 2014. **[H index 29 Impact factor: 0.458]**

14. **Kalyanasundaram, N.**, Agnihotri, A., "Large-signal field analysis of a linear beam travelling wave amplifier for a sheath-helix model of the slow-wave structure supported by dielectric rods. Part 1: Theory", Progress InElectromagnetics Research B, vol. 57, pp. 87-104, 2014. **[H index 21 Impact factor: 4.735]**

15. **Kalyanasundaram, N**., Agnihotri, A., "Large signal field analysis of a

linear beam traveling wave amplifier for a sheath-helix model of the slow-wave structure supported by dielectric rods. Part 2: Numerical results", Progress In Electromagnetics Research B, vol. 57, pp. 105-114, 2014. **[H index 21 Impact factor: 4.735]**

16. **Mohan, J.**, Maheshwari, S., "Additional high-input low-output impedance voltage-mode all-pass sections" Journal of Circuits, Systems and Computers, vol.23, 2014. DOI: 10.1142/S0218126614500777. **[H index 19 Impact factor: 0.33]**

17. Tomar, R. S., **Singh**, **S. V.,** Chauhan D. S., "Cascadable low voltage operated current-mode universal biquad filter," WSEAS Transaction on Signal Processing, vol. 10, pp. 345-353, 2014. **[H index 9]**

Others

18. Mishra, P. K., Sachdeva, V., and **Gupta, S. D.,** "Fork shaped antenna for Bluetooth application" 2014 International Conference on Medical Imaging, m-Health and Emerging Communication Systems (MedCom), pp. 338-341, Nov. 2014.

19. Mishra, H. O. S., and **Bhatnagar, S**., "Survey on Different Image Fusion Techniques," International Journal of Scientific and engineering Research (IJSER) (ISSN 2229-5518), vol. 5, no. 2, February 2014.

20. Mishra, H. O. S., and **Bhatnagar, S.**, "MRI and CT Image Fusion Based on Wavelet Transform", International Journal of Information and Computation Technology, ISSN 0974-2239, vol. 4, no. 1, pp. 47-52, 2014.

21. Goel, R., and **Gupta, R.**, "Redesigning of the construction of symmetric RVLCs based on graph model," International Journal of Information and Computation Technology, vol. 4, no. 11, pp. 1063-1068, 2014.

22. **J. Mohan, B. Chaturvedi**, S. Maheshwari "Single Active Element Based Voltage-Mode Multifunction Filter" Advances in Electrical Engineering, vol. 2014, Article ID 514019, 7 pages, 2014. doi:10.1155/2014/514019.

23. **Akhter, S., Karwal, V.,** and **Jain, R. C.,** "Fast Windowed Update Algorithm for ODCT Computation," International Journal of Electronics Letters, Taylor & Francis, doi:10.1080/21681724.2014.880989, vol. 2, no. 2, 2014.

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24. **Khandelwal, V.**, and Karmeshu, "A New Approximation for Average Symbol Error Probability over Log-Normal Channels," IEEE Wireless Communications Letters, Volume PP, Issue 99, pp.1-4, Nov.2013 **[H index 9]**

25. Yadav, V. K., Padhy, N. P., and **Gupta, H. O.,** "The Evaluation of the Efficacy of an ongoing Reform Initiative of an Indian Electric Utility," Energy Sources, Part B: Economics, Planning, and Policy," pp. 291-300, Oct. 2013. [**H index 15**]

26. **Goel, A.,** Gupta, P., and Agrawal, M., "SER Analysis of PTS Based Techniques for PAPR Reduction in OFDM Systems," Digital Signal Processing, Elsevier, Vol. 23, No.1, pp. 302-313, Jan. 2013. [**H Index 37 Impact factor: 1.495**]

27. **Dubey, R. K.,** and Kumar, A., "Non-intrusive speech quality assessment using several combinations of auditory features," International Journal of Speech Technology, Springer, vol.16, issue 1, pp.89-101, 2013 [**H Index 14**]

28. **Joshi, A.**, and Saini, D. S., "Peak-to-Average Power Ratio Reduction of OFDM signals Using Improved PTS Scheme with Low Computational Complexity", in WSEAS Trans. on Communication, 2013 [**H index 11 Impact factor: 0.924**]

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42. **Balyan, V.,** Saini, D. S. and Gupta, G., "OVSF based Fair and Multiplexed Priority Calls Assignment CDMA Networks", WSEAS Transactions on Communication, no. 1, vol. 12, Jan. 2013. **[H index 11]**

43. **Singh, S. V.,** Gupta, G., Chabra, R., Nagpal, K., and Devansh, "Electronically Tunable voltage-mode biquad filter filter/oscillator based on CCCCTAs," Int'l J. Computer Science and Information Security, vol. 11, no. 5, pp. 59-63, 2013.

44. **Singh, S. V., Tomar, R. S**., and Chauhan, D. S., "Single CCTA based four input single output voltage-mode universal biquad filter," Int'l J. Computer Science and Information Security, vol. 11, no. 3, pp. 115-119, 2013.

45. **Tomar, R. S.**, **Singh, S. V.,** and Chauhan, D. S., "Current-mode active-C biquad filter using single MO-CCCFTA," Int'l J. Advancements in Electronics and Electrical Engineering(IJAEEE), vol. 2, no. 2, pp. 145-148,

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47. **Yadav, P.**, **Dwivedi, V., Karwal, V.,** and Gupta, J. P., "A new windowing function to reduce ICI in OFDM systems", International Journal of Electronics Letters, Taylor & Francis, 2013, doi: 10.1080/21681724.2013.854155.

48. **Mohan, J.,** "Single Active Element based Current-Mode All-Pass Filter." International Journal of Computer Applications, vol. 82, no. 1, pp. 23-27, 2013. DOI: 10.5120/14080-2074.

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53. **Dwivedi, V. K.**, and Singh, G., "Marginal moment generating function based analysis of channel capacity over correlated Nakagami-m fading with maximal-ratio combining diversity," Progress In Electromagnetic Research B**,** vol. 41, pp. 333-356, June 2012. **[H Index 29 Impact factor: 3.763]**

54. Saini, D. S. and **Balyan, V.,** "OVSF code slots sharing and reduction in call blocking for 3G and beyond WCDMA networks", WSEAS Transactions of communication, vol. 11, no.4, pp. 135-146, April 2012. **[H Index 11 Impact factor 0.924]**

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Vipin Balyan
1. IEEE International Conference on Signal Processing and Integrated Network (SPIN-2014), Amity University, Noida-India (20-21 Feb. 2014).
2. IEEE International Conference on Software Telecommunication and Computer Networks, SoftCom 2011,Sep 15-17, 2011, Croatia.

Sajai Vir Singh
1. IEEE International Conference on Signal Processing and Integrated Network (SPIN-2014), Amity University, Noida-India (20-21 Feb. 2014).
2. IEEE International Conference on Multimedia, Signal Processing and communication Technologies (IMPACT-2013) on 23-25 Nov. 2013 at Aligarh (U.P.)
3. IEEE International Conference on Multimedia, Signal Processing and communication Technologies (IMPACT-2012) on 17-19 Dec., 2011 at Aligarh (U.P.)

Alok Joshi
1. Attended and presented paper in International Conference (ARTCOM 2011), Bangalore.

Bhartendu Chaturvedi
1. IEEE International Conference on Multimedia, Signal Processing and Communication Technologies (IMPACT-2013), Aligarh, India, November 2013.

Satyendra Kumar
1. IEEE SSCS Delhi Chapter "Distinguished Lecture Colloquia" on 19 Dec. 2014, STMicroelectronics Greater Noida, India.
2. Synopsys University Symposium 2014 on "Custom IC Design & Device Modelling", on 7th March 2014, Synopsys and EIGEN Technologies Pvt. Ltd.,New Delhi

Vinay Tikkiwal
1. IEEE SSCS Delhi Chapter "Distinguished Lecture Colloquia" on 19 Dec. 2014, STMicroelectronics Greater Noida, India.
2. Synopsys University Symposium 2014 on "Custom IC Design & Device Modelling", on 7th March 2014, Synopsys and EIGEN Technologies Pvt. Ltd.,New Delhi.

Raaziyah Shamim
1. 9th International Conference on Heterogeneous Networking for Quality, Reliability, Security and Robustness, GBU, Greater Noida (11-12 Jan. 2013).

Shruti Kalra
1. Workshop on "Enhancing the outreach of Electronic System Design through e-learning" held at CDAC Noida 0n 20th Nov 2014.
2. Distinguished Lecture Colloquia on Analog/Digital Circuits at STMicroelectronics, Greater NOIDA, 23-24 July, 2012.
3. One day workshop on "COMSOL Multiphysics simulations" organized by COMSOL, Delhi on 28th April, 2011.
4. "University day" organized by STMicroelectronics and Synopsys, Noida in which Chip Designing and EDA software was discussed held on16[th] November, 2010.

Ashish Goel
1. Nineteenth National Conference on Communication (NCC)-2013 at IIT New Delhi, Feb. 18-19, 2013.
2. Ninth IEEE International Conference on Wireless and Optical Communications Networks (WOCN)-2012 at IIT Indore, Sept. 20-22, 2012.

Richa Gupta
1. IEEE International Conference on Recent Advances and Innovations in Engineering (ICRAIE 2014), 9-11 May 2014, Jaipur.
2. Twenty First International Conference of Forum for Interdisciplinary Mathematics, Statistics and Computational Techniques, IMSCT 2012- FIM XXI, Chandigarh, Dec. 15-17, 2012.

Manish Kumar
1. IEEE SSCS Delhi Chapter seminar at STMicroelectronics, Greater NOIDA on June 21, 2013.

Abhishek Srivastava
1. Workshop "TECHWEEK 2011", University Day Agenda, at ST Microelectronics, 11[th]March 2011.

2. "University day" organized by STMicroelectronics and Synopsys, Noida in which Chip Designing and EDA software was discussed held on16th November, 2010.

Kirmender Singh
1. Workshop "TECHWEEK 2011", University Day Agenda, at ST Microelectronics, 11thMarch 2011.
2. "University day" organized by STMicroelectronics and Synopsys, Noida in which Chip Designing and EDA software was discussed held on16th November, 2010

Bhawna Gupta
1. Workshop on Medical Imaging: Techniques & Clinical Applications organized by Centre for Biomedical Engineering Indian Institute of Technology Delhi, 3rd & 4th April, 2015.
2. "Confluence-2011", International Conference on Information Technology, New Delhi, from 27th Jan.-28th Jan., 2011.
3. "INDIA TELECOM-2010", International Conference on Telecom, New Delhi, 9th Dec.-11th Dec., 2010.

Smriti Bhatnagar
1. Workshop on Medical Imaging: Techniques & Clinical Applications organized by Centre for Biomedical Engineering Indian Institute of Technology Delhi, 3rd & 4th April, 2015.
2. "Confluence-2011", International Conference on Information Technology, New Delhi, from 27th Jan.-28th Jan., 2011.
3. "INDIA TELECOM-2010", International Conference on Telecom, New Delhi, 9th Dec.-11th Dec., 2010

Vikram Karwal
1. Attended SystemC training at Cadence Design Systems, September 2014.
2. Workshop on "Advanced DSP Design Techniques" at IIT Kharagpur, 11-15 July, 2011.
3. Workshop on "VLSI Chip Design, Verification and Registration Issues" at CDAC NOIDA, 20th June, 2011.

2. "University day" organized by STMicroelectronics and Synopsys, Noida in which Chip Designing and EDA software was discussed held on16th November, 2010.

Kirmender Singh
1. Workshop "TECHWEEK 2011", University Day Agenda, at ST Microelectronics, 11thMarch 2011.
2. "University day" organized by STMicroelectronics and Synopsys, Noida in which Chip Designing and EDA software was discussed held on16th November, 2010

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Smriti Bhatnagar
1. Workshop on Medical Imaging: Techniques & Clinical Applications organized by Centre for Biomedical Engineering Indian Institute of Technology Delhi, 3rd & 4th April, 2015.
2. "Confluence-2011", International Conference on Information Technology, New Delhi, from 27th Jan.-28th Jan., 2011.
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2. Workshop on "Advanced DSP Design Techniques" at IIT Kharagpur, 11-15 July, 2011.
3. Workshop on "VLSI Chip Design, Verification and Registration Issues" at CDAC NOIDA, 20th June, 2011.

4. Workshop on "Fundamentals of Automatic Speech Recognition" at CDAC Noida, 7th -8th March, 2011.
5. Workshop on "Altium Seminar Series - Modern FPGA and PCB design processes" at New Delhi, 9th Feb., 2011.
6. Session Chair and TPC member in "International Conference on Signal Processing and Integrated Networks (SPIN 2014)" technically co-sponsored by IEEE, Feb 2014.

Shamim Akhter
1. Workshop on "VLSI Chip Design, Verification and Registration Issues" at CDAC NOIDA, 20th June, 2011.
2. Workshop on "Fundamentals of Automatic Speech Recognition" at CDAC Noida,7th-8thMarch, 2011.
3. Workshop "TECHWEEK 2011", University Day Agenda, at ST Microelectronics, 11thMarch 2011.
4. Workshop on "Altium Seminar Series - Modern FPGA and PCB design processes" at New Delhi, 9th Feb., 2011.

Gaurav Verma
1. Faculty Development Program on "Low-Power VLSI Design" at ABES Engineering College, Ghaziabad, February 20-22, 2014

Rajesh Kumar Dubey
1. Attended 6 days International Conference, Acoustics 2013 New Delhi, organized by the Acoustical Society of India (ASI) and the French Acoustical Society (SFA) under the aegis of the CSIR-National Physical Laboratory, New Delhi during 10th-15th Nov. 2013.
2. Workshop on "Fundamentals of Automatic Speech Recognition" at CDAC Noida,7th -8th March, 2011.

Pankaj Kumar Yadav
1. Short Term Course on "Next Generation Networks" at IIT Delhi, 9th-10th Jan, 2010.

Rahul Kaushik

1. Five Day ICT based Faculty Development Programme on Optical Fiber Communication from 22nd to 26th September, 2014 at ABES Engineering College, organized by NITTTR, CHANDIGARH under NMICT, MHRD, Govt. of India.
2. Short Term Course on "Next Generation Networks" at IIT Delhi, 9th-10th Jan, 2010.

Juhi Gupta

1. Short Term Course on "Next Generation Networks" at IIT Delhi, 9th-10th Jan, 2010.

Sandeep Joshi

1. Short Term Course on "Next Generation Networks" at IIT Delhi, 9th-10th Jan, 2010.

Saurabh Chaturvedi

1. Saurabh Chaturvedi attended and presented papers in "International Conference on Signal Processing and Integrated Networks (SPIN-2014)", Amity University, NOIDA, India during Feb. 20-21, 2014.

Shradha Saxena

1. Workshop on, "Recent Trends in Semiconductor Devices and Technology" at Deen Dayal Dayal Upadhyaya College, Karampura, New Delhi, 13-14 Sept, 2014.

Shivaji Tyagi

1. IEEE CAS Workshop on, "Advanced Topics in VLSI Circuit Design" at IIT, Roorkee, 18-19th Oct, 2014.
2. Synopsys University Symposium on "SoC Architecture Design & Embedded Software Development with Virtual Prototyping", 30th Apr, 2015
3. IEEE EDS Workshop on, "GaN Devices Fabrication, Device Modeling and Power Amplifiers" at IIT, Roorkee, 10th June, 2015.