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Volume-4

EVALUATIVE REPORT

**Department of Electronics and Communication
Engineering**

for

ASSESSMENT AND ACCREDITATION

Submitted to

NATIONAL ASSESSMENT AND ACCREDITATION COUNCIL

BANGALORE



**JAYPEE INSTITUTE OF INFORMATION TECHNOLOGY
NOIDA**

17 September, 2015

Evaluative Report of the Department

1. **Name of the Department:** Electronics and Communication Engineering

2. **Year of establishment:** 2001

3. **Is the Department part of a School/Faculty of the university?**

JIIT is a unitary University. It has departments that include Electronics and Communication Engineering Department and Jaypee Business School.

4. **Names of programmes offered (UG, PG, M. Phil., Ph. D., Integrated Masters; Integrated Ph. D., D. Sc., D. Litt, etc.)**

- a) Ph. D.
- b) M. Tech. (Electronics and Communication Engineering with specialisation in Communication Systems)
- c) M. Tech. (Electronics and Communication Engineering with specialisation in Microelectronics Systems and Embedded Technology)
- d) Dual Degree B. Tech.-M. Tech. (Electronics and Communication Engineering) [After UGC circular, July 2014, regarding standardisation of nomenclature of degrees, this program will run as Integrated M. Tech. (Electronics and Communication Engineering) w.e.f. 2015-16]
- e) B. Tech. [Electronics and Communication Engineering (ECE)]

5. **Interdisciplinary programmes and departments involved:**

None. However, the department program curriculum provides courses from other departments namely: Computer Science Engineering & Information Technology (CSE & IT), Mathematics, Physics and Material Science and Engineering (PMSE), Biotechnology and Humanities and Social Sciences (HSS).

6. **Courses in collaboration with other universities, industries, foreign institutions, etc.**

None. However, the department has an academic collaboration with University of Florida (UFL) through which final year B. Tech students can go for a semester long study at UFL, Gainesville, Florida, U.S.A.

7. Details of programmes discontinued, if any, with reasons:

None in last four years

8. Examination System: Annual/Semester/Trimester/Choice Based Credit System

Semester, along with choice based credit system

9. Participation of the department in the courses offered by other departments

The department offers following courses to other departments:

Course Name	Beneficiary Department
Electrical Circuit Analysis	CSE/IT, BIOTECH
Electrical Circuit Analysis Lab	CSE/IT, BIOTECH
Basic Electronic Devices and Circuits	CSE/IT, BIOTECH
Basic Electronics lab	CSE/IT, BIOTECH
Signals And Systems	CSE/IT
Signals And Systems Lab	CSE/IT
Digital Electronics	CSE/IT
Digital Electronics Lab	CSE/IT
Communication Systems	CSE/IT
Communication Systems Lab	CSE/IT
Information Theory and Applications	CSE/IT
Mobile Communications	CSE/IT
Optical Communication	CSE/IT
Digital Hardware Design	CSE/IT

10. Number of teaching posts sanctioned, filled and actual (Professors/ Associate Professors/ Asst. Professors/ others)

Positions	Sanctioned [#]	Filled	Actual (including CAS & MPS)
Professors	13	05	05
Associate Professors	24	01	01
Asst. Professors	68	57	57
Teaching Assistants*	-	-	19*

#JIIT follows flexible cadre structure like IITs; *Full time Ph. D./ M. Tech. students.

11. Faculty Profile with name, qualification, designation, area of specialization, experience and research under guidance

Name	Qualification	Designation	Specialization	No. of Yrs of Exp.	No. of Ph. D. students guided in the last 4 years	No. of M. Tech students guided in the last 4 years
Hari Om Gupta	Ph. D., Univ. of Roorkee	Professor & Dir (Sec-128)	Power Systems & Systems Engineering	40	9 (Awarded) 7 (Ongoing)	NIL
Krishna Gopal	Ph. D., Kurushetra Univ.	Professor & Dean A&R	System Engineering	47	1 (Awarded) 4 (Ongoing)	NIL
R. C. Jain	Ph. D., Univ. of Alberta	Professor & HOD	Wireless Communication Systems	44	6 (Ongoing) 1 (Awarded)	6
A. B Bhattacharyya	Ph. D., IIT BHU Varanasi	Professor	VLSI Design	50+	2 (Ongoing)	4
Samir Dev Gupta	Ph. D., JIIT Noida	Professor	Antenna Design and Theory	35	3 (Ongoing)	12

Shweta Srivastava	Ph. D., IIT BHU Varanasi	Assoc. Professor	Microwave Engineering and Antennas	16	6 (Ongoing) 1 (Awarded)	8
Vikram Karwal	Ph. D., Univ. of North Carolina	Asstt. Professor	Signal processing	8	3 (Ongoing) 1 (Awarded)	5
Jitendra Mohan	Ph. D., UTU Dehradun	Asstt. Professor	Analog Signal Processing	9	1 (Ongoing)	NIL
Sajai Veer Singh	Ph. D., UTU Dehradun	Asstt. Professor	Analog Signal Processing	14	4 (Ongoing)	NIL
Manish Kumar	Ph. D., JIIT Noida	Asstt. Professor	VLSI	11	3 (Ongoing)	3
Vivek Kumar Dwivedi	Ph. D., JUIT Waknaghat	Asstt. Professor	Wireless Communication	10	2 (Ongoing)	4
Ashish Goel	Ph. D., JIIT Noida	Asstt. Professor	Wireless Communication	11	2 (Ongoing)	4
Vipin Balyan	Ph. D., JUIT Waknaghat	Asstt. Professor	Wireless Communication	8	1 (Ongoing)	1
Vijay Khare	Ph. D., IIT Delhi	Asstt. Professor	Biomedical Signal Processing	14	2 (Ongoing)	2
Richa Gupta	Ph. D., JIIT Noida	Asstt. Professor	Image Processing, Joint Source & Channel Coding	9.5	2 (Ongoing)	3
Megha Agarwal	Ph. D., IIT Roorkee	Asstt. Professor	Digital Image Processing	2.5	1 (Ongoing)	NIL
Vineet Khandelwal	Ph. D., JNU Delhi	Asstt. Professor	Wireless & Optical Communication, Image Processing	14	2 (Ongoing)	4
Abhinav Gupta	Ph.D., JNU Delhi	Astt. Professor	Statistical Signal Processing	11	1(Ongoing)	3
Jasmine Saini	Ph. D., JIIT Noida	Asstt. Professor	Microwave Tubes	10.5	None	2
Rajesh Kumar Dubey	Ph. D., IIT Delhi	Asstt. Professor	Digital Signal and Speech Processing	13	1 (Ongoing)	3
Alok Joshi	Ph. D., JUIT Waknaghat	Asstt. Professor	Communication Systems	13	NIL	NIL

Bhartendu Chaturvedi	Ph. D., AMU	Asstt. Professor	Analog Signal Processing	5	1 (Ongoing)	NIL
Anil Rose	Ph. D., NIT Jalandhar	Asstt. Professor	Wireless Sensor Networks, Soft Computing	10	2 (Ongoing)	6
Shamim Akhter	Ph. D., JIIT Noida	Asstt. Professor	Signal Processing for VLSI	12	NIL	4
Madhu Jain	Ph. D., IIT Delhi	Asstt. Professor	Signal Processing	11.5	NIL	1
Rahul Kaushik	M. Tech. Univ. of Allahabad	Asstt. Professor	Optical Communications	10	NIL	1
Atul Kumar Srivastava	M. Tech. Manipal Univ.	Asstt. Professor	VLSI CAD	14	NIL	7
Bhawna Gupta	M. Tech. AMU Aligarh	Asstt. Professor	Communication Systems	11	NIL	1
Smriti Bhatnagar	M. Tech. Univ. of Allahabad	Asstt. Professor	Digital Signal Processing	20	NIL	5
Pankaj Kumar Yadav	M. Tech. IT-BHU Varanasi	Asstt. Professor	Wireless Communication	14	NIL	5
Reema Buddhiraja	M. Tech. Thapar Univ., Patiala	Asstt. Professor	Microwave Tubes	10	NIL	2
Satyendra Kumar	M. Tech., IIT Roorkee	Asstt. Professor	VLSI Systems	12.5	NIL	2
Shruti Kalra	M. Tech. GGSIPIU Delhi	Asstt. Professor	CMOS Digital VLSI Design	8	NIL	5
Neetu Singh	M. Tech. JIIT Noida	Asstt. Professor	Signal Processing	7	NIL	4
Pushpendra Singh	M. Tech. IIT Kanpur	Asstt. Professor	Signal Processing	11	NIL	2
Tanuj Chauhan	M. Tech. Univ. of NSW	Asstt. Professor	MEMS, VLSI	5	NIL	4
Juhi Gupta	M. Tech. MDU Rohtak	Asstt. Professor	Wireless Communication	8	NIL	3
Saurabh Chaturvedi	M. Tech. GGSIPIU	Asstt. Professor	VLSI Design	8	NIL	2
Kirmender Singh	M. Tech. IIIT Noida	Asstt. Professor	Analog CMOS Design	10.5	NIL	4

Amit Singhal	M. Tech. IIT Delhi	Asstt. Professor	Communication Systems	5.5	NIL	NIL
B. Suresh	M. Tech. IIT Delhi	Asstt. Professor	Communication Systems	6	NIL	3
Kapil Dev Tyagi	M. Tech. IIT Delhi	Asstt. Professor	Signal Processing	8	NIL	NIL
Rakhi Tomar	M. Tech. IIIT Allahabad	Asstt. Professor	VLSI Design	4.5	NIL	NIL
Monika	M. Tech. NSIT Delhi	Asstt. Professor	Meta-Materials, Antenna	4	NIL	4
Raaziyah Shamim	M. Tech. AMU	Asstt. Professor	Communication Systems	3.5	NIL	NIL
Vinay Anand Tikkiwal	M.S. NTU Singapore	Asstt. Professor	Microelectronics	7	NIL	NIL
Priyanka Kwatra	M. Tech. IIT Delhi	Asstt. Professor	Wireless Sensor Network	2.5	NIL	NIL
Vishal Narain Saxena	M. Tech. IIT Roorkee	Asstt. Professor	Microwave Engineering	3.5	NIL	2
Shraddha Saxena	M. Tech. IIT Roorkee	Asstt. Professor	VLSI	5.5	NIL	3
Ruby Beniwal	M. Tech. IIT Roorkee	Asstt. Professor	Control System	5	NIL	NIL
Abhishek Khanna	M.E. BITS Pilani	Asstt Professor	Communication Systems	2	NIL	NIL
Gaurav Verma	M. Tech. IIT Kh.	Asstt. Professor	Embedded Systems	10	NIL	4
Ankur Bhardwaj	M. Tech. NSIT Delhi	Asstt. Professor	VLSI and Embedded Systems	1.5	NIL	NIL
Abhishek Kashyap	M. Tech. IIT Delhi	Asstt. Professor	Communication Systems	3.5	NIL	NIL
Sumegha Yadav	M. Tech. NSIT Delhi	Asstt. Professor	Signal Processing	1.5	NIL	NIL
Anand Kumar	M. Tech. IITDM Jabalpur	Asstt. Professor	Microwaves and Antenna	1.5	NIL	NIL
Shivaji Tyagi	M. Tech. IIIT Noida	Asstt. Professor	Microelectronics, CMOS-MEMS	2.5	NIL	NIL
Sidhartha Sankar Rout	M. Tech. IIIT Delhi	Asstt. Professor	VLSI Design & Embedded Systems	3	NIL	NIL

Rajesh Singh Parmar	M. Tech. IIT Bombay	Asstt. Professor	Communication Systems	4	NIL	NIL
Jyoti Vyas	M. Tech. MNIT Allahabad	Asstt. Professor	Communication systems	3.5	NIL	NIL
Deeksha Chandola	M. Tech. IIIT Noida	Asstt. Professor	Microelectronics & Embedded Technology	1	NIL	NIL
Ritesh Sharma	M. Tech. IIT Bombay	Asstt. Professor	Control System	1	NIL	NIL
Baljit Kaur	M. Tech. NIT Kurukshetra	Asstt. Professor	VLSI Design	1	NIL	NIL

12. List of senior Visiting Fellows, Adjunct faculty, Emeritus Professors:

A B Bhattacharyya, Emeritus Professor

13. Percentage of classes taken by temporary faculty programme - wise information:

NIL

14. Programme - wise Student Teacher Ratio

UG: 22:1, PG: 12:1

15. Number of academic support staff (technical) and administrative staff: sanctioned, filled and actual: Technical Staff: Technicians for Lab

	Sanctioned	Filled	Actual
Technical	15	15	15
Administrative	Centrally managed; shared by all departments		

16. Research thrust areas as recognized by major funding agencies

Following three thrust areas have been recognised:

- (i) Emerging Communication Technologies,
- (ii) Signal Processing, and
- (iii) Digital Systems and VLSI.

(i) Emerging Communication Technologies:

- There are four major components of a wireless communication

system: transmitter, receiver, communication channel and antenna. Work is going on in the following areas. One of the key technologies to meet the requirements of future 5G communication systems are expected to be met by Orthogonal Frequency Division Multiplexing (OFDM) and Multiple-Input–Multiple-Output (MIMO) systems.

- Improved algorithms for transmitters and receivers proposed to be used in 4G and 5G systems have been developed. Main thrust of the above work has been improving Transmitter and Receiver designs so that less power is required to be transmitted. This will reduce RFI to other users and thus also improve compatibility with other systems, and reduce biological hazard.
 - Improved antenna designs for air-borne applications and antennas based on metamaterials are given so that they can give required performance even with lesser received signal strength. Improved channel models are given. This also helps in design of optimum signal processing and modulation techniques at the transmitter. Theoretical work has also been done to improve efficiency and bandwidth of Travelling Wave Tube (TWT) and Gyro-TWTs used in satellite communication and electronic warfare. Microwave Integrated Circuit (MIC) design using Substrate Integrated Waveguide (SIW) technology is carried out. This technique provides low loss signal transmission. Emphasis is also put on smart antenna technology.
 - Wireless Sensor network design for different topologies and their optimization using numerical techniques is also carried out. Integration of WSN and UWB technology for remote sensing is also going on.
- (ii) **Signal Processing:**
- Signal processing is a highly dynamic and all pervading field. Work is in progress in the areas: Brain-computer interface, Digital image processing, speech processing and Video signal processing. In the Brain-computer interface developed for disabled, a signal from EEG is analysed and used to provide an alternative channel for disabled for communicating with the external world.
 - Speech processing algorithms for 4G and next generation mobile communication and voice over internet protocol (VOIP) services.

- Fast algorithms for real time signal and low order Digital IIR Filters with linear phase characteristics which will improve the quality of video communication.
- Work is also going on for designing new digital recursive filters with magnitude characteristics closed to ideal ones and linear phase response. The linear phase response makes them useful in various signal processing applications such as control, radar, sonar, communication, and medical.

(iii) Digital Systems and VLSI:

- In the area of Digital Systems, our approach has been more development oriented using commercial software. We have software suites like Mentor Graphics IC station, Cadence, Xilinx ISE 10.1, ATLAS, and Coventor. Our students get hands-on training on the above.
- The VLSI activities in the department are primarily in the area of designing standard digital blocks and analog blocks in deep submicron technology node. The standard digital cells designing activities are FPGA based real time system design, HDL implementation of DSP algorithms, Digital System Verification using System Verilog and analog blocks. This is primarily focused towards developing design methodology for standard cells of band gap reference, operational trans-conductance amplifier, temperature sensor. Various Biquad circuits are being made by using various active elements (e.g., OTA, DDCC, DVCC and CCCCTA).
- The department also have microelectronics devices activities in the area of characterization and modelling of conventional and unconventional MOSFET structure, organic thin film transistor. The bulk MOS in 22nm technology and below it throw considerable challenge to modelling community to include all short channel effect and still qualify as compact model. The VLSI group is augmented with standard VLSI tools of Full custom IC design Synopsis tools and Mentor Graphics HEP1.
- The research activities in the VLSI Group focus on Nano-scale Device Modelling: SRAM design, Standard cell library characterization for CMOS based combinational circuits, and novel devices based on multilayer thin film structures.

- Some of the work being undertaken includes: Study of influence of the dielectric host on the dielectric functions of Si nano-particles and charge trapping, finding out the suitability of silicon carbide in solar cell applications. The other activity is study of Fin-FET structure with the devices scaled to 18nm range. Nano-wire Fin-FET structures offer a promising future to MOSFETs. These can be scaled down to smaller dimensions without much increase in short channel effects.

17. Number of faculty with ongoing projects from

- National funding agencies: **Ongoing: NIL, Completed: 03**
- International funding agencies: None
- Total grants received: 17.37 Lacs

Completed Projects: Total Grants: 17.37 Lacs

S. No.	Granting Agency	Project Title	Time	Grant Amount (Rs in Lac)	PI
1	AICTE	RPS Project- Development of IP Core for Real Time Audio and Video Surveillance System	2009-12	5	R. C. Jain
2	ISRO	Development of algorithms for Narrow Band Interference reduction in IRNSS received signal	2011-12	5.77	R. C. Jain
3	Mentor Graphics, India.	A Low-Voltage CMOS Test Chip for Thermal Sensor and RF Application on AMS 0.35 μ m technology node using Mentor Graphics PDK	2012-14	6.6	A.B. Bhattacharyya

18. Inter-institutional collaborative projects and associated grants received.

The department has so far not received any grant for collaborative research. However, a good number of faculty members of other institutes as well as some industry professionals have worked with different faculty members of department for supervising doctoral research. This collaboration has resulted in many Ph. D. theses at IIIT and other universities, as given below:

S. No.	JIIT Faculty	Collaborating Faculty	Subject Area	Outcome (Research Pub.)
1.	R C Jain	Dr Omar Farooq, AMU, Aligarh	Digital Image Processing	NIL
2.	Vikram Karwal	Prof. J. P. Gupta, Lingaya's University, Faridabad	Communication Engineering	NIL
3.	H. O. Gupta	Dr. Kaushik Saha, Samsung R&D, Noida	VLSI System	NIL
4.	Manish Kumar	Dr. P. R. Sharma, YMCA Univ. of Sc. & Tech, Faridabad	Analog Signal Processing	NIL
5.	Anil Rose	Dr. Arun Khosla, Dr. B. R. Ambedkar NIT, Jalandhar	Soft Computing	NIL
6.	S.V. Singh	Ms. Gunjan Gupta, JUIT, Wagnaghat	Analog Signal Processing	NIL
		Mr. R.S. Tomar, Uttarakhand Tech. Univ.	Analog Signal Processing	01
7.	Shweta Srivastava	Ms. Sonali Kumari, AIT, New Delhi	Communication Engineering	NIL
		Mr. Arnab Chakraborty, KIT, Ghaziabad	Communication Engineering	NIL
		Ms. Sheelu Kumari, CIT, Ranchi	Communication Engineering	NIL
		Ms. Shatabdi Chakraborty, BIT, Mesra	Communication Engineering	NIL
		Dr. Megha Dadel, BIT, Patna	Communication Engineering	1
		Mr. Wriddhi Bhowmik BIT, Mesra	Communication Engineering	1

Details of publications are given at Annexure-I/ECE.

19. Departmental projects funded by DST-FIST; UGC-SAP/CAS, DPE; DBT, ICSSR, AICTE, etc.; Total grants received. – 10.00 lakh

S.No.	Granting Agency	Duration	Scheme	Grants received	PI
1.	AICTE	2009-12	MODROBS	10 Lac	R C Jain

20. Research facility /centre with

- **State Recognition:** None
- **National Recognition:** MEMS Design Centre, IIIT MEMS Design Centre (supported by NPMASS Program, Govt. of India).

The centre for MEMS design was set-up in the year 2009 as a part of Institute's response to launch MEMS activity supported under National Program on Microelectronics and Smart Systems (NPMASS), Govt. of India. A core course, Microelectronics and MEMS Technology has been incorporated in the M.Tech (Microelectronics Systems and Embedded Technology) programme.

Research work carried out:

1. Design, Characterization and Modeling of On-Chip Inductors on Silicon Substrate - A test chip with inductor structures is prototyped using industry standard CAD design environment at AMS, Austria and characterization for the same is going on presently at CARE, IITD.
2. SAW Oscillator for Temperature/Gas Sensor
3. Design of Cantilever Beam based Resonators for low-frequency filtering applications.

- **International Recognition:** None

21. Special research laboratories sponsored by / created by industry or corporate bodies: Industry/ corporate sponsored: NIL

22. Publications:

1.	Total Number of Publications: Peer Reviewed	129
	International Journals	127

	National Journals	2
2.	Monographs	1
3.	Chapter in Books	1
4.	Edited Books	0
5.	Books	4
6.	Books with ISBN with details of publishers	4
7.	Number listed in International Databases	293
8.	Citation Index (Google)	
	Range	1-38
	Average	4.3
9.	Citation Index (Scopus)	
	Range	1-35
	Average	4.016
10.	SNIP	76.495
11.	SJR	38.772
12.	Impact Factor (Scopus)	
	Range	0 –5.298
	Average	1.019
13.	H-index (Scopus) average	27.06

Publication details are attached as **Annexure-II/ ECE**

23. Details of patents and income generated: NIL

24. Areas of consultancy and income generated

H. O. Gupta, “Design of 5000KVA 11/.69 KV Transformers (Completed on Jan. 2015)”Area of Consultancy: Transformers and Electromagnetic Devices.

Income generated: Rs. 35,000/-

25. Faculty selected nationally / internationally to visit other laboratories / institutions / industries in India and abroad

The following faculty visited and delivered lecture in various University/Institute as given below:

<p>A. B. Bhattacharyya</p> <ul style="list-style-type: none"> • "Microsystem Design on Silicon", Jan 28, 2014, Devi Ahilya University, Indore • "MOSFET Modeling for VLSI Design", Jan 29, 2014, Devi Ahilya University, Indore. • "Acoustic Wave Sensors", May 13, 2013, Jadavpur University, Kolkata. • COMPACT MODEL: BACKGROUND MATERIAL", December 03, 2012, Delhi University, Delhi. • "Revolutionary Ideas in Microelectronics and their Social Impact", October 12, 2012, Delhi University, Delhi. • "MOSFET and Interconnect Inductor Modeling for VLSI Design", October 16, 2012, Jadavpur University, Kolkata. • "Micro and Nano Electronics Charitmanas – Where small is not so small-", April 27, 2012, under INSPIRE PROGRAM Initiative, Meerut.
<p>Gaurav Verma:</p> <ul style="list-style-type: none"> • "Low Power Techniques on Embedded System Design", May 5, 2014, Shivalik College of Engineering, Dehradun.
<p>Tanuj Chauhan:</p> <ul style="list-style-type: none"> • MEMS Technology, June 3, 2014, Cadence Design Systems, NOIDA.

- 26. Faculty serving in**
- National committees
 - International committees
 - Editorial Boards
 - any other (please specify)

(a) National Committees

S. No.	Name of Faculty	Details
1	A B Bhattacharyya	<ul style="list-style-type: none"> ▪ Member - MOS-AK International Compact Model Group ▪ Chairman BOG, NIT Durgapur

3	R C Jain	<ul style="list-style-type: none"> ▪ Member, Program Committee, International Conference on VLSI System and Applications (VLSI-SATA - 2015), at Amrita University, Bangalore ▪ Member, Program Committee, ICIIS -14, Dec, 2014, IIIT, Gwalior.
4	Richa Gupta	<ul style="list-style-type: none"> ▪ Secretary, Board of Trustees of FIM (Forum of Interdisciplinary Mathematics). Meerut, U.P.

(b) International Committees

Name of Faculty	Details
A.B Bhattacharyya	<ul style="list-style-type: none"> • International Advisor - Taoyaka Program for Graduate Studies, Hiroshima University, Japan (March 2014-ongoing)

(c) Editorial Committees

Name of Faculty	Details
Jitendra Mohan	Journal of Advanced Electrical and Computer Engineering

27. Faculty recharging strategies (UGC, ASC, Refresher / orientation programs, workshops, training programs and similar programs).

To recharge the faculty, Department organizes conferences, workshops, seminars, expert talks, refresher courses, faculty development programs, etc., at IIIT. In addition, faculty members also participate in these activities outside IIIT.

The academic activities organized at the Institute are listed below:

(i) Visitors/ Guest Speakers: 9

1. Sumantra Dutta Roy, IIT, Delhi, Nov 5, 2014- Empirical Mode Decomposition based Techniques.
2. Sh. R. K. Singh, Consultant (Former Engineer-in-Chief, Doordarshan), New Delhi, April 18, 2014 - Recent Trends in broadcasting in India.
3. N. N. Sharma, Coordinator- Nanomaterials and National MEMS Design Centre, BITS, Pilani, Feb. 22, 2014 - MEMS Design: A paradigm shift in design thought process.
4. R K Shevgaonkar, IIT Delhi, Dec 12, 2013- Photonic Crystals.
5. Surendra Prasad (Ex-Director, IIT Delhi), Dec 12, 2013- Broadband Access Network Technologies.
6. Jaromir Pistora, Technical University of Ostrava, Czech Republic,

- Dec 13, 2013- Nonlinear Optical Devices.
7. Banmali Singh Rawat, Univ. of Nevada, Reno, USA, Dec 14, 2013 - Recycling the Light.
 8. Sneh Anand, IIT, Delhi, Dec. 14, 2013- Intelligent Biomedical Systems.
 9. Sunil Kumar, San Diego State University, San Diego, Aug. 3, 2013- Video Compression Techniques and Standards.

(ii) Workshops/ Conferences/ FDP:

1. IEEE International Conference on Signal Processing and Communication (ICSC-2015), March 16-18, 2015.
2. IEEE International Symposium on Signal processing and Information Technology, Dec 15 – 17, 2014.
3. Two weeks ISTE Workshop on Control Systems, Dec. 2 -12, 2014.
4. Two days workshop on Low Voltage and Low Power VLSI Design, Aug. 22 - 23, 2014.
5. Two weeks ISTE Workshop on “Signals and Systems”, Jan. 2-12, 2014.
6. IEEE International Conference on Signal Processing and Communication (ICSC-2013), Dec. 12-14, 2013.
7. Workshop on “COMSOL Multiphysics for MEMS Modeling”, Nov. 15, 2013.
8. Workshop on “Semiconductor Device Modeling using TCAD Tools”, Oct. 4, 2013.
9. A 5-days workshop on “Design for Testability”, K Chakraborty, FIEEE, Duke Univ., USA, July 23-27, 2013.
10. A 5-days IUCEE workshop on “Embedded Systems”, by Vijaynarayanan, FIEEE, Penn State Univ., USA, July 1-5, 2013.
11. CMOS Technology and Spice Model: MOS-AK India, Tutorial, March 18, 2012.
12. A 2-daysworkshop on “Device Modeling for Microsystems”, March 16-17, 2012.
13. A 1-day workshop on “Wavelets & its Applications”, April 20, 2011.
14. A 1-day workshop on “Microwave Tubes”, Nov 27, 2010.
15. FDP on Signal Processing & Communications, 21st -26th July, 2014.

28. Student projects

Percentage of students who have done in-house projects including inter-departmental projects

100%

Percentage of students doing projects in collaboration with other universities

/ industry / institute
0%

29. Awards / recognitions received at the national and international level by Faculty:

S. No.	Name of Faculty	Detail of Research Award
1	Hari Om Gupta	<ol style="list-style-type: none"> 1. Commendation Certificate from University of Roorkee, “Novel Techniques for electronic circuit analysis” Year: 1980 2. Certificate Merit, Institution of Engineers (India) “An algorithm for solving nonlinear electronic circuit equations: Year: 1985 With: Dr. J.D. Sharma) 3. Silver Medal and Cash Prize of Rs.500/- Khosla Annual Research Prize-1989 “New One-Cycle static blocking scheme for inrush in differential relay” Year: 1989 With: (Dr. B.T. Desai & Prof. M.K. Vasantha) 4. Best Research Paper Award by “Hari Ohm Ashram Preit Shri Bhaikaka Inter University Smark Trust” Sardar Patel University. “New One-Cycle static blocking scheme for inrush in differential relay” in the Year: 1991 With:(Dr. B.T. Desai & Prof. M.K. Vasantha) 5. CBIP Best Research Award “A Novel approach to computation of energy of non-linear loads” Year: 1992-93 With: (Dr. (Ms.) Indra Gupta, Mr. S.K. Joshi and others) 6. Certificate of Merit from the Instiution of Engineers (India) “Investigation of Power System Voltage Security Analysis Under Critical Loading Conditions” Year:1993-94 With:(Dr. V. Bhanot and Prof. J.D. Sharma) 7. Certificate of Merit from the Institution of Engineers (India) “Adaptive Coordination of Directional Over Current Relays of Electrical Power Systems Using Network Decomposition Approach” Year :1994-95 With: (Dr. N.A. Laway) 8. Khosla Award Cash Prize of Rs. 1500/- and a Medal, IIT, Roorkee. “New Reduction Technique for Distribution Feeders” Year: 2001-2002 With: (Dr. (Ms.) Indra Gupta and Prof. M.K. Vasantha) 9. Union Ministry of Energy- Deptt. of Power Prize- from the Institution of Engineers (India) “A New Evolutionary

		<p>Programming Based Approach for Optimal Selection of Wheeling Options” Year: 2003-2004 With: (Dr. N.P. Padhy and Dr. Y. R. Sood)</p> <p>10. Best paper award in National Conference on Control and Instrumentation, held at Department of Electrical Engineering, NIT, Kurukshetra , Dec.29-30,2007 on research paper “Biased Reduced Order Models using Dominant Pole Retention and Genetic Algorithm” With: (Mrs Nidhi Singh and Rajendra Prasad</p> <p>11. Mathur Vaishya Ratna (Highest Community award on social work) 2012.</p>
2	A.B. Bhattacharyya	<p>1. Hari Om Vikram Sarabhai Award (1980)</p> <p>2. Bhasin Award (1984)</p> <p>3. Khosla National Award (1986)</p> <p>4. S. N. Mitra INAE Award (2005)</p> <p>5. Indian Semiconductor Association (ISA) Techno Visionary Award (2009)</p> <p>6. Fellow (The Founder Member) - Indian National Academy of Engineering(INAE)</p>
3	R. C. Jain	<p>1. Canadian Commonwealth Scholarship and Fellowship 1983.</p> <p>2. CDIL Award of the Institution of Electronics and Telecommunication Engineers (IETE), India in the year 1994 for the best paper in Industrial Applications category.</p> <p>3. K S Krishnan Memorial Award of the IETE, India in the year 1999 for a paper published in the IETE Journal of Research.</p> <p>4. IETE-Students’ Journal Award, 2006 for a paper published in IETE Journal of Education.</p> <p>5. Foreign IT Professor, GIST, South Korea under the Govt. of South Korea Scheme “Brain Korea- 21”:</p> <p>a. July 2002 to June, 2004, and</p> <p>b. July 2006 to June, 2007</p>

Doctoral / post doctoral fellows: NIL

Students:

1. Delhi Technological University (DTU): INNOVA 2011

Name Of Event & Date	Position/ Prize	Team Members- Branch/Year
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Life on the edge (22-02-2011)	1 st Prize	Prateek Agnihotri, Laxmikant Ratnawat -2 nd yr ECE, Swadeep Chaturvedi-1 st yr ECE, Avnessh Singh-1 st yr CSE
Robo Rush	Certificate	Abhishek Chauhan - 2 nd yr ECE
Aqua Rush	Certificate (4 th)	Abhishek Chauhan-2 nd yr ECE
Robo combat	3 rd Prize	Akash Shukla, Rituraj Singh, Rahul Sinha, Akash Chaudhry-2 nd yr ECE
Robo Rush	Certificate	Akash Shukla, Rituraj Singh, Rahul Sinha, Akash Chaudhry-2 nd yr ECE
Robo Rush/ Robo combat	Certificate	Ravi Jain- 1 st yr ECE
Boat Rush	1 st Prize	Ritu Raj Singh, Akash Shukla, Rahul Sinha-2 nd yr ECE

2. IIT Kharagpur-Techfest-2011

Name Of Event	Position/ Prize	Team Members
RAFT	Finalists (Certificate)	Shivam Mittal-2 nd yr ECE Rishabh Shukla, Prerit Jain, Arpit Gupta-2 nd yr CSE
RAFT	Certificate	Ravi Jain, Sarthak Bhatnagar, Rachit Jain--1 st yr ECE

3. Gnix- 2011-Galgotia University

Name Of Event	Position/ Prize	Team Members
Line-X	Certificate	Rituraj Singh-2 nd yr Akash Shukla, Rahul Sinha, Akash Chaudhary-2 nd yr

4. Amity Youth Fest 2012, Feb 09-12, 2012

Name Of Event	Position/ Prize	Team Members
Robo war	1 st Prize	Rituraj Singh, Akash Shukla, Rahul Sinha, Mayank Juneja-3 rd yr ECE

Soccer Mania	1 st Prize	Rituraj Singh, Akash Shukla, Rahul Sinha, Mayank Juneja-3 rd yr ECE
F-1	1 st Prize	Rituraj Singh, Akash Shukla, Rahul Sinha-3 rd yr ECE
Roborace	2 nd Prize	Rituraj Singh, Akash Shukla, Rahul Sinha-3 rd yr ECE

5. IIT Kharagpur-Techfest-2012

Name Of Event	Position/ Prize	Team Members
Inferno	Finalists	Ravi Jain, Abhinav Gupta, Piyush Agarwal-2 nd yr ECE, Rishi Golyan-2 nd yr CSE
Inferno	Finalists (Certificate)	Shivam Sharma, Amulya -1 st yr ECE Rajan Middha-1 st yr CSE
Stasis	Finalists (Certificate)	Abhinav Gupta, Piyush Agarwal, Rishi Golyan -2 nd yr CSE, Ravi Jain-2 nd yr ECE

6. DTU Techfest 2012

Name Of Event	Position/ Prize	Team Members
Stasis	1 st Prize	Ravi Jain-2 nd yr ECE
		Abhinav Gupta-2 nd yr CSE
Robo Soccer	2 nd Prize	Ravi Jain, Anshumaan Singh-1 st yr ECE
Robo Race	Finalists (Certificate)	Ravi Jain, Anshumaan Singh-1 st yr ECE
Robo War	Finalists (Certificate)	Ravi Jain, Anshumaan Singh-1 st yr ECE

List of awards and achievements in events other than Robotics for students

S.No.	Name of	Detail of the Conference /	Nature of the
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	Students	Technical Event / Technical competition	award
1	Manoj Makhija, Kapil Goyal	Switching Application using Hand Gesture Recognition” in ICRAIE, Rajkot.	Published a paper
2	Kapil Verma	Inter College Robotics events organized at Amity Youth festival, 2013, Delhi	1st position
3	Kapil Verma	Inter College HTML reverse engg competition organized at GDG Bharti Vidyapeeth	2nd position
4	Anubhav Gupta	Inter College Robotics events organized at Amity Youth festival, 2013.	2nd position
5	Abhinav Gupta	Inter college Robotics organized at DTU, Delhi	1st position
6	Abhinav Gupta	For developing website for International SFIA conference organized by AKS Services UK Ltd in 2013	Selected
7	Harshit Jain	To develop medical adherence applications in 2013.	Research assistantship from (University of Notre dame, USA)

30. Seminars/ Conferences/Workshops organized and the source of funding (national/ international) with details of outstanding participants, if any.

S.No.	Name of Seminars/ Conferences/Workshops	Source of funding	Outstanding participants
1	IEEE International Conference on Signal Processing and Communication (ICSC-2015), March 16-18, 2015. Funding Agencies: IIIT, Noida. Outstanding Participants: Prof. U. B. Desai , IIT Hyderabad, Prof. V. K. Jain , IIT Delhi, Prof. Banmali S. Rawat , University of Nevada, USA, Dr. Kiyotoshi Yasumoto , Kyushu University, Japan, Prof. Jaromir Pistora , VSB-Technical University of Ostrava, Prof. Vinod Kumar , IIT Roorkee.		

2	<p>IEEE International Symposium on Signal Processing and Information Technology (ISSPIT-2014), Dec. 15-17, 2014. Funding Agencies: IIIT, Noida and IEEE, USA Organized jointly with University of Florida, USA Outstanding participants: Prof. Sartaj Sahni, University of Florida, USA, Prof. Mohamed H. Ahmed, Memorial U of Newfoundland, Canada, Prof. Reda Ammar, University of Connecticut, USA, Prof. Adel Elmaghraby, University of Louisville, USA, Prof. Sanguthevar Rajasekaran, Univ. of Connecticut, USA.</p>
3	<p>Ten days workshop on Control Systems, Dec. 2-12, 2014. Funding Agencies: NMEICT (National Mission on Education through ICT) funded by MHRD, GOI and organized in conjunction with IIT, Kharagpur. Outstanding participants: Siddhartha Mukhopadhyay, Department of Electrical Engineering, IIT Kharagpur, Tapan Kumar Ghoshal, Department of Electrical Engineering, Jadavpur University, Kolkata</p>
4	<p>Two days workshop on “Low Voltage and Low Power VLSI Design”, Aug. 22 - 23, 2014 Funding Agencies: IIIT, Noida. Outstanding Participants: A. B. Bhattacharya, IIIT Noida, G. S. Visweswaran (Professor, Department of Electrical Engineering, IIT Delhi), Kaushik Saha (Director, Systems R&D, Samsung India), Shouribrata Chatterjee (Associate Prof., Department of Electrical Engineering, IIT Delhi)</p>
5	<p>Two week ISTE Workshop on “Signals and Systems”, Jan. 2-12, 2014. Funding Agencies: IIT, Kharagpur under MHRD programme, NMEICT held at ECE Deptt., IIIT, Noida Outstanding Participants: Vikram M. Gadre, IIT Bombay, Somnath Sengupta, IIT, Kharagpur</p>
6	<p>IEEE International Conference on Signal Processing and Communication (ICSC-2013), Dec. 12-14, 2013. Funding Agencies: IIIT, Noida. Outstanding Participants: R K Shevgaonkar, IIT Delhi, Surendra Prasad, Ex-Director, IIT Delhi, Kiseon Kim, GIST, South Korea, Jaromir Pistora, Tech. Univ. of Ostrava, Czech Republic, Banmali S. Rawat, Univ. of Reno, USA</p>
7	<p>Workshop on “COMSOL Multiphysics for MEMS modeling”, Nov. 15, 2013.</p>

	<p>Funding Agency: JIIT, Noida Outstanding participants: Hemant Kumar and Technical Team, Comsol, India</p>
8	<p>Workshop on “Semiconductor Device Modeling using TCAD Tools”, Oct. 4, 2013. Funding Agency: JIIT, Noida Outstanding participants: Amit Saini, and Technical Team, Cadence Design System, Noida</p>
9	<p>Workshop on “Design for Testability”, July 23-27, 2013. Funding Agency: JIIT, Noida Outstanding Participants: Krishnendu Chakrabarty, Professor, Duke University, USA</p>
10	<p>Workshop on “Embedded Systems”, July 1-5, 2013. Funding Agency: JIIT, Noida Outstanding Participants: Vijaykrishnan Narayanan, Professor, Pennsylvania State University, USA</p>
11	<p>International workshop MOS-AK/GSA India 2012 on “Device Modeling for Microsystems”, March 16-18, 2012. Funding Agencies: JIIT, co-organized by Indian National Academy of Engineering (INAE), supported by DIT, GOI and CSIR. Outstanding Participants: Mitiko Miura, Mattauch Hiroshima University, Japan, Narain D. Arora, Silterra, Malaysia, Yogesh Chauhan, UC Berkeley, Xing Zhou, NTU, Singapore, Ane Juge STMicroelectronics, France, Ehrenfried Seebacher, AMS Austria, Vaidyanathan Subramanian, IBM, India, Samar Saha, SuVolta, USA, Waldek Grabinski, MOS-AK/GSA, EU, Mike Brinson, London Metropolitan University, London, Thomas Gneting, ADMOS, Germany, V Ramagopal Rao, IIT Mumbai, A B Bhattacharyya, JIIT, Noida, Shantanu Mahapatra, IISc, Anitava Dasgupta, IIT, Chennai, M Jagdesh Kumar, IIT, Delhi, Navakanta Bhat, IISc., Soumya Pandit, IPRE, Kolkata</p>
12	<p>Workshop on “Wavelets & its Applications”, April 20, 2011. Funding Agencies: JIIT Organized jointly with Mathematics Department JIIT,Noida Outstanding Participants: S. D. Joshi, IIT Delhi, Mani Mehra, IIT Delhi, Pradeep Sarkar, IIT Kanpur, Umar Farooq, AMU, Aligarh, Bani Singh, JIIT Noida</p>
13	<p>Workshop on ‘Microwave Tubes’ Nov. 27, 2010 Funding Agencies: JIIT Outstanding Participants: B. N. Basu, BHU Varanasi, Lalit Kumar, MTRDC Bangalore, R. S.</p>

31. Code of ethics for research followed by the departments

Department follows the code of ethics defined by the Institute. The ethics lays strong emphasis that all research work/theses must be original and work by others is duly acknowledged. To enhance quality of research anti-plagiarism software is used. Research scholars and students before submitting their Ph. D. theses, dissertations, Project reports and research papers for award/publications check manuscripts for plagiarism.

32. Student profile programme-wise:

Name of the Programme (refer to question no. 4)	Applications received*	Selected		Pass Percentage	
		Male	Female	Male	Female
B. Tech. (ECE)					
2007-11	22629	146	27	143 (97.95)	27 (100)
2008-12	32517	122	51	117 (95.90)	50 (98.04)
2009-13	31710	215	58	209 (97.21)	58 (100)
2010-14	26650	314	79	289 (92.04)	79 (100)
DUALM-ECE					
2006-11	19372	18	8	18 (100)	8 (100)
2007-12	22229	7	0	5 (71.43)	0 (NA)
2008-13	32017	19	24	18 (94.74)	23 (95.83)
2009-14	31210	22	6	19 (86.36)	6 (100)
Dual Degree B.Tech- M. Tech. (ECE)					
2006-11	19872	NA	NA	NA	
2007-12	22629	NA	NA	NA	
2008-13	32517	NA	NA	NA	
2009-14	31710	NA	NA	NA	
M. Tech. (ECE)					
2009-11	445	6	8	5 (83.33)	8 (100)
2010-12	1310	19	9	18 (94.74)	9 (100)
2011-13	1943	7	13	6 (85.71)	13 (100)
2012-14	1600	7	10	7 (100)	10 (100)
M. Tech. (MET)					
2009-11	445	11	2	10 (91.91)	1 (50)
2010-12	1310	12	2	12 (100)	2 (100)
2011-13	1943	10	2	10 (100)	2 (100)

2012-14	1600	14	6	12	6 (100)
Ph. D.					
Upto 2014	432	45	20	Passed: Continuing: Discontinued:	9(5M, 4F) 35 (22M, 13F) 21 (18M, 3F)

*Applications for the UG programs are received without any choice of program. The choice of program is exercised by the candidate during counselling. Hence individual program based applications is not possible to be indicated.

#The application for PG programs are received under two categories (i) GATE & (ii) PGET. Each candidate is allowed to exercise multiple choices of programs where he/she is eligible. Therefore individual program based applications is not possible to be indicated.

33. Diversity of students Data

Name of the Programme (refer to question no. 4)	% of students from the same university	% of students from other universities within the State	% of students from universities outside the State	% of students from other countries
B.Tech. (ECE)				
2011	NIL	2.63	97.13	0.24
2012	NIL	4.61	94.93	0.46
2013	NIL	5.34	94.66	NIL
2014	NIL	6.78	93.22	NIL
DUALM-ECE				
2011	NA	NA	NA	NA
2012	NA	NA	NA	NA
2013	NA	NA	NA	NA
2014	NA	NA	NA	NA
Dual Degree B.Tech- M. Tech. (ECE)				
2011	NA	NA	NA	NA
2012	NIL	3.85	96.15	NIL
2013	NIL	4.35	95.65	NIL
2014	NIL	NIL	100	NIL
M. Tech. (ECE)				
2009-11	7.69	53.85	38.46	NIL

2010-12	NIL	35.71	64.29	NIL
2011-13	NIL	50.00	50.00	NIL
2012-14	NIL	52.94	47.06	NIL
2013-15	3.45	51.72	44.83	NIL
2014-16	NIL	42.86	57.14	NIL
M. Tech. (MET)				
2009-11	NIL	46.15	53.85	NIL
2010-12	NIL	71.43	28.57	NIL
2011-13	NIL	66.67	33.33	NIL
2012-14	NIL	60.00	40.00	NIL
2013-15	NIL	75.00	25.00	NIL
2014-16	NIL	53.57	46.43	NIL
Ph. D. (ECE)				
2011	NIL	NIL	100	NIL
2012	10.00	20.00	60.00	10.00
2013	16.67	33.33	33.33	16.67
2014	16.67	16.67	66.66	NIL

34. How many students have cleared Civil Services and Defence Services examinations, NET, SET, GATE and other competitive examinations? Give details category-wise.

GRE: 90+ students
GATE: 120+ students
CAT/MAT: 80+ students

35. Student progression

The Department does not have a systematic data on the progression of students after graduating like proceeding for higher studies or joining the companies after placements, etc. However, the details of alumni available indicate following trends:

Student progression	Percentage against enrolled
UG to PG	15-20 %
PG to M.Phil.	NA
PG to Ph. D.	~20%
Ph. D. to Post-Doctoral	NIL
Employed	
• Campus selection	~85%

• Other than campus recruitment	~10%
Entrepreneurs	0.5%

36. Diversity of staff

Percentage of faculty who are graduates	PG	Ph. D.
of the same university	8	23
from other universities within the State	16	14
from universities from other States	71	54
universities outside the country	5	9

For Ph. D., percentage is calculated out of total Ph. D. degree holders and **Percentage of PG is calculated out of total strength of the Department.**

37. Number of faculty who were awarded M.Phil., Ph. D., D.Sc. and D.Litt. during the assessment period

Total no. = 17

The following faculty have acquired Ph. D. Degree within assessment period:

Samir Dev Gupta, Manish Kumar, Richa Gupta, Ashish Goel, Jasmine Saini, Shamim Akhter, Abhinav Gupta, Vineet Khendelwal, Vijay Khare, Rajesh Kumar Dubey, Jitendra Mohan, Sajai Vir Singh, Vipin Balyan, Megha Agrawal, Bhartendu Chaturvedi, Alok Joshi, and Madhu Jain.

38. Present details of departmental infrastructural facilities with regard to

a) **Library:** Department uses the Central Learning Resource (Central Library)

b) **Internet facilities for staff and students:**

- i. All the faculty members are provided with desktop computer and internet connections. All faculty have access to Institute webkiosk and webmail.
- ii. Students are provided with internet connections with Wi-Fi access. They are also provided access to the webkiosk with personal login
- iii. Students can access internet from anywhere within the campus as the entire Institutional campus area is Wi-Fi enabled.

iv. Class rooms, lecture theatres, and labs are also equipped with internet facilities for teaching and demonstration.

c) **Total number of class room:** 49 Lecture Theatres/ Class Rooms and 42 Tutorial Rooms of the Institute are shared with other departments

d) **Class rooms with ICT facility:** 40 Lecutre Theatres/ Class Rooms.

e) **Student's Laboratories**

Laboratory	Location	L A N	P C	Power backup	Capacity	Display Board	White Board
Sector-62							
Basic Electronics Lab -1	ABB-I	N	N	Y	32	Y	Y
Basic Electronics Lab -2	ABB-I	N	N	Y	32	Y	Y
Electronic Devices Lab	ABB-I	N	N	Y	32	Y	Y
Electrical Machines Lab	ABB-I	N	N	Y	32	Y	Y
Signal Processing Lab	ABB-I	Y	Y	Y	32	Y	Y
Communication Systems Lab	ABB-I	N	N	Y	32	Y	Y
Advance Communication Systems Lab	ABB-I	Y	Y	Y	32	Y	Y
VLSI / VHDL Lab	ABB-I	Y	Y	Y	32	Y	Y
B Tech Project Lab – 1	ABB-2	Y	Y	Y	40	Y	Y
Embedded Systems Lab	Under Development						
Sector-128							
Basic Electronics Lab	Block A	N	N	Y	30	Y	Y
Communication Systems Lab	Block B	N	N	Y	30	Y	Y
VLSI Lab	Block B	Y	Y	Y	37	Y	Y
DSP Lab	Block B	Y	Y	Y	30	Y	Y
Electromagnetics Lab	Block B	Y	Y	Y	30	Y	Y
Analog & Digital Electronics Lab	Block A	N	N	Y	30	Y	Y
Creativity and Innovation Lab	Block B	N	N	Y	20	Y	Y
Electrical Machines and Instrument Lab.	Block A	N	N	Y	30	Y	Y

f) Research Laboratories

Laboratory	Location	L A N	P C	Power backup	Capacity	Display Board	White Board
MEMS Lab	ABB-2	N	N	Y	13	Y	Y
B Tech Project Lab – 2	Under Development						
M Tech Project Lab	Under Development						

39. List of doctoral, post-doctoral students and Research Associates

a) From the host institution/university

Awarded: 7

Pursuing: 20

S. No	SCHOLAR	SUPERVISOR	STATUS
1	Samir Dev Gupta	M. C. Srivastava	Completed
2	Manish Kumar	M. C. Srivastava	Completed
3	Ashish Goel	Prerana Gupta	Completed
4	Richa Gupta	N. Kalyanasundaram, B. D. Sharma	Completed
5	Jasmine Saini	N. Kalyanasundaram	Completed
6	Shamim Akhter	Vikram Karwal, R.C. Jain	Completed
7	Pankaj Kumar Yadav	Vikram Karwal, J.P.Gupta	Ongoing
8	Smriti Bhatnagar	R.C. Jain	Ongoing
9	Bhawna Gupta	R.C. Jain, Omar Farooq	Ongoing
10	Reema Budhiraja	R. C. Jain, Manish Kumar	Ongoing
11	Juhi Gupta	Vikram Karwal, Vivek Dwivedi	Ongoing
12	Neetu Singh	R.C. Jain, Abhinav Gupta	Ongoing
13	Rahul Kaushik	R.C. Jain, Vineet Khandelwal	Ongoing
14	Tanuj Chauhan	Vikram Karwal	Ongoing
15	Vinay A. Tikkiwal	Hari Om Gupta, S. V. Singh	Ongoing
16	Ankur Bhardwaj	Manish Kumar	Ongoing
17	Shruti Kalra	A. B. Bhattacharyya	Ongoing
18	Kirminder Singh	A. B. Bhattacharyya	Ongoing
19	Manu Prakash	Vikram Karwal	Ongoing
20	Manish Agrawal	Vijay Khare	Ongoing

21	Raazia Shamim	R. C. Jain, Shweta Srivastava	Ongoing
22	Gaurav Verma	Manish Kumar, Vijay Khare	Ongoing
23	Anand Kumar	Hariom Gupta , Jitendra Mohan	Ongoing
24	Abhishek Kashyap	Megha Agarwal, Hari Om Gupta	Ongoing
25	Shivaji Tyagi	R. C. Jain	Ongoing

b) From other Institutions/Universities

S. NO	SCHOLAR	SUPERVISOR	STATUS
1	Amita Agnihotri	N. Kalyanasundaram	Completed
2	Tarun Kumar	R.C. Jain	Ongoing
3	Amit Garg	A. K. Singhal	Ongoing
4	Naveen Jaglan	Samir Dev Gupta	Ongoing
5	Prasanna Kumar Singh	Samir Dev Gupta	Ongoing
6	Chanadra Shankar Porwal	S. V. Singh	Ongoing
7	Aashima Garg	Ashish Goel	Ongoing
8	Manoj Kumar Garg	S. D. Gupta	Ongoing
9	Tanvi Agrawal	Vipin Balyan	Ongoing
10	Suman Bharti	Richa Gupta	Ongoing
11	Abhishek Jain	Richa Gupta	Ongoing
12	Atul Kumar	Bhartendu Chaturvedi	Ongoing
13	Shilpi Shukla	Rajesh Dubey	Ongoing
14	Saruti Gupta	Ashish Goel	Ongoing

40. Number of post graduate students getting financial assistance from the university

Year	M.Tech	Ph. D.
2011-12	10	0
2012-13	10	3
2013-14	13	2
2014-15	43	3

41. Was any need assessment exercise undertaken before the development of new programme(s)? If so, highlight the methodology.

The emerging trends in industry, research, government policies,

recommendations of the professional bodies, and JIIT management's directives motivate and guide the department to design new programs. International and nationally renowned academicians from many top rated institutes and industry professionals including our alumni are consulted while conceptualising and designing new programs or revising the curricula of existing degree programs. All the faculty members are requested to give their suggestions and finally the meeting of the BOS including external experts is held for reviewing the curriculum.

42. Does the department obtain feedback from

i. Faculty on curriculum as well as teaching-learning-evaluation? If yes, how does the department utilize the feedback?

Yes. Feedback from faculty is obtained during departmental meetings.

ii. Students on staff, curriculum and teaching-learning-evaluation and how does the department utilize the feedback?

Yes. At the end of every semester, students' feedback is collected for teaching faculty. The average weighted calculated score is communicated to concerned faculty for remedial measures.

iii. Alumni and employers on the programmes offered and how does the department utilize the feedback?

Yes. Feedback from alumni is collected through email and the same is documented at departmental level. Important points from feedback are discussed intra-departmentally.

Online feedback collection mechanism has been operationalized from Academic Session 2014-15 through Institute Quality Assurance Cell (IQAC).

43. List the distinguished alumni of the department (maximum 10)

S.No	Name, Year of Graduation from JIIT, Achievements and current affiliation
1	Nishant Mittal, 2009 Director, Sybly Industries Ltd.
2	Anurag Gupta, 2011 Director, Nova Biological Systems Pvt. Ltd.
3	Jaspreet Singh Makkar, 2013 Founder, Meissa Technologies Pvt. Ltd.
4	Siddhant Gupta, 2013

	Director, ABC Technology Ltd.
5	Chetan Chaturvedi, 2006 Vice President Sales, 6D Telecom Solutions Pvt. Ltd.
6	Mayank Agarwal, 2014 Founder, ANDROBO
7	Karan Kumar, 2009 Senior Business Specialist, Cigna Health Care

44. Give details of student enrichment programmes (special lectures/ workshops / seminar) involving external experts.

1. IEEE International Symposium on Signal Processing and Information Technology (ISSPIT-2014), Dec. 15-17, 2014.
2. ISTE Workshop on “Control Systems” by Siddhartha Mukhopadhyay, IIT Kharagpur, Tapan Kumar Ghoshal, Jadavpur University, Kolkata on Dec. 2-12, 2014.
3. Workshop on "Low Voltage and Low Power VLSI Design" by G.S. Visweswaran, IIT Delhi, Kaushik Saha, Samsung India, Shouribrata Chatterjee, IIT Delhi on Aug. 22-23, 2014.
4. Workshop on “Signals and Systems” by Vikram M.Gadre, IIT Bombay, Somnath Sengupta, IIT, Kharagpur on Jan. 2-12, 2014.
5. IEEE International Conference on Signal Processing and Communication (ICSC-2013) by R K Shevgaonkar, IIT Delhi, Surendra Prasad, Ex-Director, IIT Delhi, Kiseon Kim, SIST, South Korea, Jaromir Pistora, Tech. Univ. of Ostrava, Banmali Singh Rawat, Univ. of Reno, USA on Dec. 12-14, 2013.
6. Workshop on “COMSOL Multiphysics for MEMS modeling” by Hemant Kumar and Technical Team, Comsol India on Nov. 15, 2013.
7. Workshop on “Semiconductor Device Modeling using TCAD Tools” by Amit Saini, Cadence Design System, Technical Team, Cadence Design System on Oct. 4, 2013.
8. A 5-day workshop on “Design for Testability” by Krishnendu Chakrabarty, Duke University, USA on July 23-27, 2013.
9. International workshop MOS-AK/GSA India 2012 on “Device Modeling for Microsystems” by Mitiko Miura, Mattausch Hiroshima University, Japan, Narain D. Arora, Silterra, Malaysia, Yogesh Chauhan, UC Berkeley, Xing Zhou, NTU, Singapore, Ane Juge STMicroelectronics, France, Ehrenfried Seebacher, AMS Austria, Vaidyanathan Subramanian, IBM, India, Samar Saha, SuVolta, USA, Waldek Grabinski, MOS-AK/GSA, EU, Mike Brinson, London Metropolitan University, London, Thomas Gneting, ADMOS,

Germany, V Ramagopal Rao, IIT Mumbai, A B Bhattacharyya, IIIT, Shantanu Mahapatra, IISc, Anitava Dasgupta, IIT, Chennai, M Jagdish Kumar, IIT, Delhi, Navakanta Bhat, IISc., Soumya Pandit, IPRE, Kolkata on March 16-18, 2012.

10. Workshop on “Wavelets & Its Applications” in Signal Processing by S. D. Joshi, IIT Delhi, Mani Mehra, IIT Delhi, Pradeep Sarkar, IIT Kanpur, Omar Farooq, AMU Aligarh on April 20, 2011.
11. Workshop on “Microwave Tubes” by Prof. B.N. Basu, BHU Varanasi, Lalit Kumar, MTRDC Bangalore, R.S. Raju, CEERI Pilani on Nov. 27 2010.

45. List the teaching methods adopted by the faculty for different programmes.

1. Regular chalk/Marker and board
2. Exercises in tutorial classes.
3. Use of audio visual aids
4. Project based learning (Seminars, Minor and Major Projects)
5. Case study / peer-reviewed publication based learning
6. Technical Papers from IEEE, IET, Springer etc. Journals.

46. How does the department ensure that programme objectives are constantly met and learning outcomes are monitored?

The department monitors the learning outcomes through student’s progressions (placement/ entrepreneurship/higher education). Institute Quality Assurance Cell (IQAC) is responsible for the day to day administration of quality assurance at IIIT.

Academic Council, Board of studies and Institute IQAC meet regularly to evaluate and provide course correction whenever required. This improves the quality in academics as well as in research processes. The feed backs are collected from faculty and course syllabi developed subsequently taking into consideration recent developments in the subject areas. The feedback of the outgoing and ex-students and their expectations from the program/course are reviewed regularly in department, faculty meetings. This aids in continuously developing the department’s objectives.

47. Highlight the participation of students and faculty in extension activities.

Faculty and students of the department are actively involved in various

extension activities organized by JIIT. Details are given in Section 3.6 of Self Study Report of the Institute.

48. Give details of “beyond syllabus scholarly activities” of the department.

Beyond syllabus scholarly activities

- (i) Expert Lectures
- (ii) Seminars, Workshops and Conferences
- (iii) Techno-cultural fests inter- and intra-university participation

49. State whether the programme/ department is accredited/ graded by other agencies? If yes, give details.

Yes, the Department of ECE was NBA accredited for 5 years w.e.f. 22.01.2008

50. Briefly highlight the contributions of the department in generating new knowledge, basic or applied.

Ph. D. Scholars, students’ projects under the guidance of supervisors are involved in generation of new knowledge and contributions through publications as below:

S.No.	Paper Type	Total
1	International Journals	108
2	National Journals	2
3	International Conferences	120
4	National Conferences	7
5	Book Publication	1
6	Chapter Publication	0
Total		238

Some of the major contributions of the department are as under:

Dr. Manish Kumar: Designing of improved on-chip tunable bi-quad filter.

Dr. Rajesh K. Dubey: Weight dependent frames in multiple time-scale speech quality assessment.

Dr. Shamim Akhter: Development of update algorithms for DCT

computation.

Dr. Vipin Balyan: Assignment schemes for OVVSF codes.

Dr. Jasmine Saini: Improved design for a TWT

Dr Richa Gupta: Improved RVLCs

Dr Ashsish Goel: Improvement in 4G cellular network technology

51. Detail five major Strengths, Weaknesses, Opportunities and Challenges (SWOC) of the department.

Strengths

- i. Well qualified and experienced faculty
- ii. Good infrastructure (Audio visual facility in all classrooms, good laboratory facility, library equipped with reference books and research journals)
- iii. Flexibility in updating curriculum
- iv. Strategic NCR location enabling collaborations with Institutions.

Weakness

- i. Ability to attract very good employers in core areas of the ECE students
- ii. High end Research Facilities
- iii. Extension activity

Opportunities

- i. Present national focus on research, development and manufacturing
- ii. Multidisciplinary collaborative research with other departments of the Institute
- iii. Institute location in National Capital Region (NCR).

Challenges

- i. Quality High-end Research work
- ii. Quality of M.Tech/ Ph. D. students
- iii. Sponsored projects from Govt. and other institutions

52. Future plans of the department.

- i. To secure national recognition in the area of Communication Engineering.
- ii. Getting research projects from national and international agencies.
- iii. To publish high quality research papers with journals having high metrics.
- iv. To start M. Tech course in emerging area.

Publication published through collaborations

1. Tomar R.S., **Singh S.V.**, Chauhan D.S., “Cascadable low voltage operated current-mode universal biquad filter,” WSEAS Transaction on Signal Processing, vol. 10, pp. 345-353, 2014. [**H index 9**].

1. Publications in International Journals

2015

Indexed in SCOPUS

1. **Gupta, J., Karwal, V., and Dwivedi, V. K.**, "Joint Overlay-Underlay Optimal Power Allocation in Cognitive radio", Wireless Personal Communication, Springer, Vol. 81, No. 4, March 2015. [**H index 29 Impact factor: 0.458**]
2. Jaglan, N. and **Gupta, S. D.**, “Design and Analysis of Performance Enhanced Microstrip Patch Antenna with EBG Substrate” International Journal of Microwave and Optical Technology (IJMOT), Volume 10, Number 2, pp 79-88, March 2015. [**H index 3**]
3. Shankar, C., Singh, **S. V.**, “A new trans-admittance-mode biquad filter using MO-VDTA,” WSEAS Transaction on Circuits and Systems, vol. 14, pp. 8-18, 2015. EISSN: 2224-266X. [**H Index 10**]
4. **Chaturvedi, B.**, and Maheshwari, S., "Single Active Element Based Cascadable Band-Pass Filters for Low-Q Applications." Journal of Circuits, Systems and Computers, Volume 24, Issue 05, June 2015. ISSN: 0218-1266. [**H Index 19 Impact factor: 0.24**]

Others

5. **Chandola, D.**, and Garg, G., “Pull-in Voltage Analysis of Fixed Free Cantilever Beam”, International Journal of Electrical Electronics & Computer Science Engineering Volume 1, Issue 1 (February 2014), ISSN : 2348 2273.
6. Gupta, G., **Singh, S. V.**, and Bhooshan S. V., "VDTA Based Electronically Tunable Voltage Mode and Trans-Admittance Biquad Filter." Circuits and Systems, Vol.6 No.3, March 2015. DOI: 10.4236/cs.2015.63010.

2014

Indexed in SCOPUS

7. **Kumar, A.**, Kumar, D., **Mohan, J.**, **Gupta, H. O.**, “Investigation of grid metamaterial and EBG structures and its application to patch antenna” International Journal of Microwave and Wireless Technologies, First View Articles, pp 1-8 doi: 10.1017/S1759078714000944, Published online by Cambridge University Press, 30 Jul 2014. [**H index 8 Impact factor: 0.327**].
8. **Goel, A.**, Gupta Poddar, P., and Agrawal, M., “Generalized M-2M Mapping Scheme for SLM and PTS Based Methods for PAPR Reduction in OFDM Systems without Side-Information,” Wireless Personal Communications, Springer, vol. 74, no. 2, pp. 285-305, Jan. 2014. [**H index 29 Impact factor: 0.458**]
9. Saini, D. S., and **Balyan, V.**, “Top Down Code Search to Locate An Optimum Code and Reduction in Code Blocking for CDMA Networks, “ Wireless Personal Communication, Springer, vol. 75, no. 1, pp. 465-482, 2014. DOI 10.1007/s11277-013-1372-9. [**H index 29 Impact factor: 0.458**]
10. Kinhekar, N., Padhy, N. P., and **Gupta, H. O.**, “Multiobjective demand side management solutions for utilities with peak demand deficit,” International Journal of Electrical Power and Energy Systems, vol. 55, pp. 612-619, 2014. [**H index 54 Impact factor: 3.111**]
11. Mehta, A., **Kumar, S.**, and Kumar, R., “A Novel Architecture and PVT Analysis of 4-Bit Manchester Carry Chain Block,” International Journal of Applied Engineering Research, ISSN 0973-4562 Volume 9, Number 20, pp. 6581-6590, 2014. [**H index 3 Impact factor: 7.827**]
12. Kumar, T., **Kalyanasundaram, N.**, Lande, B. K., “Analysis of the generalized case of scattering from a ferromagnetic microwire grid”, Progress In Electromagnetics Research M, vol. 35, pp. 1-10, 2014. [**H index 12**]
13. **Dwivedi, V. K.**, Singh, G., “Moment Generating Function Based Performance Analysis of Maximal-Ratio Combining Diversity Receivers in the Generalized-K Fading Channels”, Wireless Personal Communications, pp. 1-17, 2014. [**H index 29 Impact factor: 0.458**]
14. **Kalyanasundaram, N.**, Agnihotri, A., “Large-signal field analysis of a linear beam travelling wave amplifier for a sheath-helix model of the slow-wave structure supported by dielectric rods. Part 1: Theory”, Progress InElectromagnetics Research B, vol. 57, pp. 87-104, 2014. [**H index 21 Impact factor: 4.735**]
15. **Kalyanasundaram, N.**, Agnihotri, A., “Large signal field analysis of a

linear beam traveling wave amplifier for a sheath-helix model of the slow-wave structure supported by dielectric rods. Part 2: Numerical results”, Progress In Electromagnetics Research B, vol. 57, pp. 105-114, 2014. [**H index 21 Impact factor: 4.735**]

16. **Mohan, J.**, Maheshwari, S., “Additional high-input low-output impedance voltage-mode all-pass sections” Journal of Circuits, Systems and Computers, vol.23, 2014. DOI: 10.1142/S0218126614500777. [**H index 19 Impact factor: 0.33**]
17. Tomar, R. S., **Singh, S. V.**, Chauhan D. S., “Cascadable low voltage operated current-mode universal biquad filter,” WSEAS Transaction on Signal Processing, vol. 10, pp. 345-353, 2014. [**H index 9**]

Others

18. Mishra, P. K., Sachdeva, V., and **Gupta, S. D.**, “Fork shaped antenna for Bluetooth application” 2014 International Conference on Medical Imaging, m-Health and Emerging Communication Systems (MedCom), pp. 338-341, Nov. 2014.
19. Mishra, H. O. S., and **Bhatnagar, S.**, “Survey on Different Image Fusion Techniques,” International Journal of Scientific and engineering Research (IJSER) (ISSN 2229-5518), vol. 5, no. 2, February 2014.
20. Mishra, H. O. S., and **Bhatnagar, S.**, “MRI and CT Image Fusion Based on Wavelet Transform”, International Journal of Information and Computation Technology, ISSN 0974-2239, vol. 4, no. 1, pp. 47-52, 2014.
21. Goel, R., and **Gupta, R.**, "Redesigning of the construction of symmetric RVLCs based on graph model," International Journal of Information and Computation Technology, vol. 4, no. 11, pp. 1063-1068, 2014.
22. **J. Mohan, B. Chaturvedi**, S. Maheshwari “Single Active Element Based Voltage-Mode Multifunction Filter” Advances in Electrical Engineering, vol. 2014, Article ID 514019, 7 pages, 2014. doi:10.1155/2014/514019.
23. **Akhter, S., Karwal, V., and Jain, R. C.**, “Fast Windowed Update Algorithm for ODCT Computation,” International Journal of Electronics Letters, Taylor & Francis, doi:10.1080/21681724.2014.880989, vol. 2, no. 2, 2014.

2013

Indexed in SCOPUS

24. **Khandelwal, V.**, and Karmeshu, “A New Approximation for Average Symbol Error Probability over Log-Normal Channels,” IEEE Wireless Communications Letters, Volume PP, Issue 99, pp.1-4, Nov.2013 [**H index 9**]

25. Yadav, V. K., Padhy, N. P., and **Gupta, H. O.**, "The Evaluation of the Efficacy of an ongoing Reform Initiative of an Indian Electric Utility," Energy Sources, Part B: Economics, Planning, and Policy," pp. 291-300, Oct. 2013. [**H index 15**]
26. **Goel, A.**, Gupta, P., and Agrawal, M., "SER Analysis of PTS Based Techniques for PAPR Reduction in OFDM Systems," Digital Signal Processing, Elsevier, Vol. 23, No.1, pp. 302-313, Jan. 2013. [**H Index 37 Impact factor: 1.495**]
27. **Dubey, R. K.**, and Kumar, A., "Non-intrusive speech quality assessment using several combinations of auditory features," International Journal of Speech Technology, Springer, vol.16, issue 1, pp.89-101, 2013 [**H Index 14**]
28. **Joshi, A.**, and Saini, D. S., "Peak-to-Average Power Ratio Reduction of OFDM signals Using Improved PTS Scheme with Low Computational Complexity", in WSEAS Trans. on Communication, 2013 [**H index 11 Impact factor: 0.924**]
29. Puri, P., and **Singh, N.**, "Two-dimensional spreading scheme employing 2D orthogonal variable spreading factor codes for orthogonal frequency and code division multiplexing systems," IET Communications, vol 7.5, pp. 463-470, 2013. [**H Index 37 Impact Factor: 0.637**].
30. **Kalyanasundaram, N.**, and **Saini, J.**, "On small-signal amplification of a TM circular cylindrical wave-guide mode in a Gyro-TWT," IET Microwaves, Antennas & Propagation, Vol.7, pp. 644-655, 2013. [**H Index 40 Impact factor: 0.681**]
31. **Kalyanasundaram, N.**, and Babu, G. N., "Propagation of electromagnetic waves guided by anisotropically conducting model of a tape helix supported by dielectric rods," Progress In Electromagnetic Research, Vol. 51, pp. 81 - 99, 2013.[**H Index 56 Impact factor: 4.735**]
32. **Kalyanasundaram, N.**, **Budhiraja, R.**, Singh, S., and Gupta, K., "TE and TM modes of a vane loaded circular cylindrical waveguide for Gyro-TWT applications," Progress In Electromagnetic Research, Vol. 50, pp.295-313, 2013. [**H Index 56 Impact factor: 4.735**]
33. **Joshi, A.**, and Saini, D. S., "Performance Analysis and PAPR Reduction of Coded OFDM (with RS-CC and Turbo coding) System using Modified SLM, PTS and DHT Precoding in Fading Environments," in WSEAS Trans. on Communication. Vol. 12, Issue 1, pp.14-28, 2013. [**H index 11 Impact factor: 0.924**]
34. **Mohan, J.**, and Maheshwari S, "Cascadable Current-Mode First-Order All-Pass Filter Based on Minimal Components," The Scientific World Journal, vol. 2013, Article ID 859784, 5 pages, 2013. doi:10.1155/2013/859784. [**H index 40 Impact factor: 1.22**]

35. **Goel, A.**, Gupta, P., and Agrawal, M., “Joint ICI Cancellation and PAPR Reduction in OFDM Systems without Side-Information” *Wireless Personal Communications*, Springer, vol. 71 , no. 4, pp. 2605-2623, 2013. **[H index 29 Impact factor: 0.458]**
36. Maheshwari, S., **Mohan, J.**, Chauhan, D. S., “Novel Voltage-Mode Cascadable All-Pass Sections Employing Grounded Passive Components” *Journal of Circuits, Systems and Computers*, Vol. 22 No. 1, pp. 1-12, 2013. DOI: 10.1142/S021812661250065X. **[H index 19 Impact factor: 0.33]**
37. Yadav, V. K., Chauhan, Y. K., Padhy. N. P., and **Gupta, H.O.**, “A novel power sector restructuring model based on Data Envelopment Analysis (DEA)” *International Journal on Electrical Power and Energy Systems* (Elsevier) vol 44, pp 629-637, 2013. **[H index 54 Impact factor: 3.11]**
38. **Srivastava, A. K.**, “Novel crossover for evolvable hardware”, *World Applied Sciences Journal*, vol. 21, issue 10, pp. 1408-1414, 2013. **[H index 8 Impact factor: 0.23]**
39. Karmeshu, and **Khandelwal, V.**, “On the applicability of average channel capacity in log-normal fading environment”, *Wireless Personal Communications*, vol. 68, issue 4, pp. 1393-1402, 2013. **[H index 29 Impact factor: 0.458]**

Others

40. **Jain, M.**, Gupta, M., and Jain, N., “Analysis and Design of Digital IIR Integrators and Differentiators using Minimax and Pole, Zero & Constant Optimization Methods,” *ISRN Electronics*, Vol. 2013, pp. 1-14, 2013, doi:10.1155/2013/493973.
41. Agarwal, M., and **Jain, R. C.**, “Ensemble Empirical Mode Decomposition: An adaptive method for noise reduction,” *International Journal of IOSR-JECE*. vol. 5, issue 5, pp. 60-65, Mar-Apr, 2013.
42. **Balyan, V.**, Saini, D. S. and Gupta, G., “OVSF based Fair and Multiplexed Priority Calls Assignment CDMA Networks”, *WSEAS Transactions on Communication*, no. 1, vol. 12, Jan. 2013. **[H index 11]**
43. **Singh, S. V.**, Gupta, G., Chabra, R., Nagpal, K., and Devansh, “Electronically Tunable voltage-mode biquad filter filter/oscillator based on CCCCTAs,” *Int’l J. Computer Science and Information Security*, vol. 11, no. 5, pp. 59-63, 2013.
44. **Singh, S. V.**, **Tomar, R. S.**, and Chauhan, D. S., “Single CCTA based four input single output voltage-mode universal biquad filter,” *Int’l J. Computer Science and Information Security*, vol. 11, no. 3, pp. 115-119, 2013.
45. **Tomar, R. S.**, **Singh, S. V.**, and Chauhan, D. S., “Current-mode active-C biquad filter using single MO-CCCFTA,” *Int’l J. Advancements in Electronics and Electrical Engineering(IJAEEEE)*, vol. 2, no. 2, pp. 145-148,

- 2013.
46. **Mohan, J.,** Maheshwari, S., **Singh, S. V.,** and Chauhan, D. S., “High input impedance voltage-mode universal biquadratic filter using only plus-type DVCCs” *Journal of Active and Passive Electronic Devices*, Vol. 8, pp. 31–38, 2013.
 47. **Yadav, P., Dwivedi, V., Karwal, V.,** and Gupta, J. P., “A new windowing function to reduce ICI in OFDM systems”, *International Journal of Electronics Letters*, Taylor & Francis, 2013, doi: 10.1080/21681724.2013.854155.
 48. **Mohan, J.,** “Single Active Element based Current-Mode All-Pass Filter.” *International Journal of Computer Applications*, vol. 82, no. 1, pp. 23-27, 2013. DOI: 10.5120/14080-2074.
 49. **Mohan, J., Chaturvedi, B.,** Maheshwari, S., "Novel Current-Mode All-Pass Filter with Minimum Component Count", *International Journal of Image, Graphics and Signal Processing (IJIGSP)*, vol.5, no.12, pp.32-37, 2013.DOI: 10.5815/ijigsp.2013.12.05
 50. **Mohan, J.,** Maheshwari, S., “Two Active Elements Based All-Pass Section Suited for Current-Mode Cascading” *World Academy of Science, Engineering and Technology*, International Science Index 73, *International Journal of Electrical Engineering*, vol. 7, No. 1, pp. 1217 – 1221, 2013.

2012

Indexed in SCOPUS

51. **Gupta, S. D.,** and **Srivastava, M. C.,** “Multilayer Microstrip Antenna Quality Factor Optimization for Bandwidth Enhancement”, *Journal of Engineering Science & Technology (JESTEC)*, School Of Engineering, Taylor’s University, Malaysia, Vol. 7, Issue 6, pp. 756-773, Dec. 2012. [**H Index 6 Impact factor: 2.212**]
52. **Jain, M.,** Gupta, M., and Jain, N., “Linear Phase Second Order Recursive Digital Integrators and Differentiators,” *Radioengineering*, Vol. 21, Issue 2, pp. 712–717, June 2012. [**H index 14 Impact factor: 0.739**]
53. **Dwivedi, V. K.,** and Singh, G., “Marginal moment generating function based analysis of channel capacity over correlated Nakagami-m fading with maximal-ratio combining diversity,” *Progress In Electromagnetic Research B*, vol. 41, pp. 333-356, June 2012. [**H Index 29 Impact factor: 3.763**]
54. Saini, D. S. and **Balyan, V.,** “OVSF code slots sharing and reduction in call blocking for 3G and beyond WCDMA networks”, *WSEAS Transactions of communication*, vol. 11, no.4, pp. 135-146, April 2012. [**H Index 11 Impact factor 0.924**]

55. **Singh, S. V.**, Maheshwari, S., and Chauhan, D. S., "Current-Processing Current-Controlled Universal Biquad Filter," *Radioengineering Journal*, vol. 21, no. 1, pp. 317- 323, April 2012. [**H index 14 impact factor: 0.503**]
56. **Dwivedi, V. K.**, and Singh, G., "Repeated correlative coding scheme for mitigation of ICI in OFDM systems," *IET Communications*, vol.6, pp 599-603, April 2012. [**H Index 37 Impact factor: 0.963**]
57. Palanisamy, P., **Kalyansundaram, N.**, and Swetha, P. M., "Two-dimensional DOA estimation of coherent signals using acoustic vector sensor array", *Signal Processing*, vol.92, pp.19-28, 2012. [**H Index 77 Impact factor:1.503**]
58. **Kalyanasundaram, N.**, and **Babu, G. N.**, "Perfectly conducting tape-helix model for guided electromagnetic wave propagation," *IET Microwaves, Antennas and Propagation*, Vol.6, Issue 8, pp. 899-907, 2012. [**H Index 40 Impact factor: 0.682**]
59. **Mohan, J.**, and Maheshwari, S., "Supplementary High-Input Impedance Voltage-Mode Universal Biquadratic Filter Using DVCCs", *Modelling and Simulation in Engineering*, Vol. 2012, Article ID 184829, pp. 1-6, 2012. doi:10.1155/2012/184829.[**H Index 4**]
60. **Akhter, S.**, **Karwal, V.**, and **Jain, R. C.**, "Improved Algorithm for ODCT Computation of a Running Data Sequence," *Journal of Electrical and Computer Engineering*, vol. 2012, pp. 1-10, Article ID 879626, 2012. doi:10.1155/2012/879626. [**H Index 6 Impact factor 0.118**].
61. **Jain, M.**, Gupta, M. and Kumar, B., "Wideband digital integrator and Differentiator," *IETE Journal of Research*, Vol. 58, Issue 2, pp.166–170, March 2012. [**H Index 14 Impact factor 0.076**]

Not indexed in SCOPUS/Web of Science/ DBLP but having Impact factors

62. **Gupta, R.** and Sharma, B. D., "Reversible Variable Length Codes in Video Coding Standards ", *International Journal of Emerging trends in Engineering and Development*, ISSN No. 2249-6149, Vol. 3, Issue 2, pp. 33-43, April 2012. [**Impact factor 0.91**]
63. **Gupta, R.**, and Sharma, B. D., "Generation of Variable Length Error Correcting Codes using Constant Length Error Correcting Codes", *International Journal of Emerging Trends in Engineering and Development*, ISSN NO: 2249-6149, Issue 2, Vol. 1, pp. 269-279, January 2012. [**Impact factor: 0.91**]

Others

64. Agarwal, S., **Jain, R. C.**, and Ojha, D. B., "Broadband Interference Suppression Using Volterra Filters," *Journal of Global Research in Computer Science*, vol. 3, issue 11, pp. 27-31, Dec. 2012.

65. Singh, M. and **Dubey, R. K.**, “Non-intrusive speech quality with different time scale,” IOSR Journal of Computer Engineering (IOSRJCE), vol. 2, Issue 5, pp. 49-53 ISSN: 2278-0661, July-Aug. 2012.
66. Tyagi, V. B., **Tyagi, K. D.**, Dagar, S., **Singh, P.**, Gunjan and **Suresh, B.**, “Application of Neural Network to Overcome Blind Time Problem in CTFM Sonar,” International Journal on Computer Science and Technology, ISSN NO: 0976-8491, Vol.3 Ver. 5, April-June 2012.
67. Singh, A. P., Pandey, S. K., and **Kumar, M.**, “Operational Transconductance Amplifier for Low Frequency Application”, International Journal of Computer Technology and Application, Vol. 3, No. 3, pp. 1064-1066, May-June 2012.
68. Pandey, S. K., Singh, A. P., and **Kumar, M.**, “A Current Mode Fifth Order Elliptical Filter Employing CDTA,” International Journal of Computer Science and Communication Networks, Vol. 2, No. 2, pp. 236-239, April-May 2012.
69. Pandey, S. K., Singh, A. P., and **Kumar, M.**, “A Current Mode Second Order Filter Using Dual Output CDTA,” International Journal of Computer Science and Communication Networks, Vol.2, No.2, pp. 210-213, April-May 2012.
70. Kumar, K., Srivastava, A., **Kumar, M.**, and **Akhter, S.**, "Piece-Wise Linear Approach for Gain Adjustment of Single Stage, Single Transistor Amplifier", International Journal of Computational Engineering & Management, Vol. 15, Issue 3, pp.24-29, May 2012.
71. **Gupta, S. D.**, and Singh, A., “Design and Analysis of Multidielectric Layer Microstrip Antenna with Varying Superstrate Layer Characteristics,” International Journal of Advances in Engineering & Technology (IJ AET), vol. 3, Issue 1, pp. 55-68, March 2012.
72. Kumar, K., **Akhter, S.**, and Gupta, S., “Implementation of DLX based ALU Using Xilinx® System Generator®, “International Journal of Computer Science & Communication Networks, ISSN: 2249-5789, Vol. 2 (2), pp. 231-235, 2012. **[Impact factor: 1.503]**
73. **Mohan, J.**, Maheshwari, S., Garg, G., and Chauhan, D. S., “Two DDCC based cascadable voltage-mode first-order all-pass filters” International Journal of Advancements in Electronics and Electrical Engineering – IJAEEE, Vol. 1, Issue 2, pp. 48-52, 2012.
74. **Mohan, J.**, Garg, G., and Chauhan, D. S., “High Input Impedance Voltage-Mode Universal Biquadratic Filter with One Input and Five Outputs using plus-type DDCCs” International Journal of Advancements in Electronics and Electrical Engineering – IJAEEE, Vol. 2, Issue 3, pp. 124-129, 2012.
75. **Goel, A.**, Gupta, P. P., and Agarwal, M., “M-ary Chaotic Sequence Based SLM-OFDM System for PAPR Reduction without Side-Information”

2011

Indexed in SCOPUS

76. **Dwivedi, V. K.**, and Singh, G., “A Novel MGF-based performance analysis of maximal ratio combining diversity receivers in the generalized-K fading channels”, Progress In Electromagnetic Research C, vol. 26, pp.153-165, Dec. 2011. [**H Index 12 Impact factor: 3.763**].
77. **Dwivedi, V. K.**, and Singh, G., “A novel moment generating function based performance analysis over correlated Nakagami-m fading”, Journal of Computational Electronics, vol. 10, no. 4, pp.373-381, Dec. 2011. [**H Index 17 Impact factor: 0.896**]
78. **Dwivedi, V. K.**, and Singh, G., “Error-rate analysis of OFDM communication system in correlated Nakagami-m fading channel using maximal ratio combining diversity”, Int. Journal of Microwave and Wireless Technologies, vol. 3, no. 6, pp. 717-726, Dec. 2011. [**H index 8 Impact factor: 0.327**].
79. **Singh, S. V.**, Maheshwari, S., and Chauhan, D. S., “Novel electronically tunable mixed-mode biquad filter,” Electronics and Signal Processing, Lecture Notes in Electrical Engineering (LNEE), Springer, Berlin Heidelberg, vol. 97, pp. 735-742, July 2011, DOI: 10.1007/978-3-642-21697-8_93. [**H Index 11**]
80. Nigam, V., Luthra, S., and **Bhatnagar, S.**, “Image Denoising Using Wavelet Transform and Wavelet Transform with Enhanced Diversity,” AMR Journal, Vol. (403-408), pp. 866-870, 2011. [**H Index 19**]
81. **Kalyanasundaram, N.**, and **Saini, J.**, “On Small Signal Amplification in a Gyro-TWT”, Progress in Electromagnetic Research C, vol. 21, pp. 75-86, 2011. [**H Index 12 Impact factor 4.735**]
82. **Saxena, P. K.**, “Modeling and simulation of HgCdTe based p⁺-n-n⁺ LWIR photodetector”, Infrared Physics and Technology, 54, pp. 25–33, 2011. [**H Index 37 Impact factor 1.46**]
83. Khanna, S., Noor, A., Neeleshwar, S., and **Tyagi, M. S.**, “Effect of annealing temperature on the electrical characteristics of Platinum/4H-SiC Schottky barrier diodes”, International Journal of Electronics, vol. 98, issue 12, pp. 1733-1741, 2011. [**H Index 29 Impact factor 0.75**]
84. Palanisamy, P., and **Kalyanasundaram, N.**, “Sonar target detection by modified adaptive noise cancellation using correlating filter,” International Journal of Electronics, Vol. 98, Issue 1, pp. 41-60, 2011. [**H Index 29 Impact factor: 0.44**]
85. **Gupta, A.**, and Karmeshu, “Statistical characterisation of speckle in clinical

echocardiographic images with Pearson family of distributions”, Defence Science Journal, vol. 61, issue 5, pp. 473-478, 2011. [**H Index 13 Impact factor: 0.31**]

Others

86. **Kumar, M.**, Kumar, U. and **Srivastava, M. C.**, “A Tunable Universal Filter Using Dual-X Differential Difference Current Conveyor (DXDDCC)”, Journal of Active and Passive Electronic Devices, Vol. 6, pp. 321-326, Nov. 2011.
87. **Kumar, M.**, Bansal, P. and Maheshwari, S., “Behaviour Based Mobile Robot Navigation in a Dynamic Environment: Fuzzy Logic Approach”, Journal of Active and Passive Electronic Devices, Vol. 6, No. 3-4, pp. 293-303, Nov. 2011.
88. **Gupta, S. D.**, and Singh, A., “Design of Microstrip Planar Antenna Array and Study of Effect on Antenna Parameters due to Mutual Coupling in both E and H Planes,”International Journal of Communication Engineering Applications (IJCEA), vol. 02, Issue-06, Aug. 2011.
89. **Gupta, S. D.**, **Srivastava, M. C.**, and Singh, A., “Design and Performance Analysis of Cylindrical Microstrip Antenna and Array using Conformal Mapping Technique,” International Journal of Communication Engineering Applications (IJCEA), vol. 02, Issue 03, pp 166-180, Jul. 2011.
90. **Singh, S. V.**, Maheshwari, S., and Chauhan, D. S., “Single MO-CCCCTA-based electronically tunable current/tans-impedance-mode biquad universal filter,” International Journal of Circuits and Systems, USA, vol. 2, No. 1, pp. 1- 6, Jan. 2011, DOI:10.4236/CS.2011.21013.
91. Sharma, S., Chitranshi, G., Mahato, B., **Srivastava, A. K.**, and Singh, B. P., “Control of Home-Appliances through IR interface using web (GPRS) enabled Mobile Phones”, International Journal of Advanced Engineering Sciences and Technologies, vol. 6, issue 2, pp. 242-245, 2011.

2010

Indexed in SCOPUS

92. **Gupta, A.**, Gosain, B., and Kaushal, S., “A Comparison of Two Algorithms for Automated Stone Detection in Clinical B-Mode Ultrasound Images Of The Abdomen “, Journal of Clinical Monitoring And Computing, Springer, vol. 25(5), pp.341-362, Aug 2010. [**H Index 34 Impact factor: 0.375**]
93. **Kaur, G.**, and **Gupta, N.**, “Design and implementation of improved Superimposed Cyclic Optical Orthogonal Codes (SCOOC) based Optical Encoder/Decoder Structure for 1 Gbps Optical CDMA System”, Journal of Engineering Science and Technology, Taylor's University College, Malaysia, Vol. 5, No.4, pp. 391-399, 2010. [**H Index 6**]

94. **Kalyanasundaram, N., Babu, G. N., and Tulsian, R., "On the distribution of current on an open tape helix", Progress In Electromagnetics Research M, Vol. 12, pp. 81-93, 2010. [H index 12 Impact Factor: 4.735].**
95. **Kalyanasundaram, N., and Muthuchidambaranathan, P., "Nonlinear Pulse Propagation in a Weakly Birefringent Optical Fiber Part 1: Derivation of Coupled Nonlinear Schrödinger Equations (CNLSE)", Progress in Electromagnetic Research B, vol. 19, pp. 205-231, 2010. [H index 21 Impact factor: 4.735]**
96. **Kalyanasundaram, N., and Babu, G. N., "Dispersion of Electromagnetic Waves Guided by an Open tape Helix II", Progress in Electromagnetic Research B, vol. 19, pp. 133-150, 2010. [H index 21 Impact factor: 4.735].**
97. **Sharma, R., Chakravarty, T., Bhattacharyya, A. B., "Reduction of signal overshoots in high-speed interconnects using adjacent ground tracks", Journal of Electromagnetic Waves and Applications, Vol 24, Issue 7, pp. 941-950, 2010. [H Index 36 Impact factor: 1.40]**
98. **Saxena, P. K., "An Improved Photoinduced Model for Silicon Based Microcantilever", Journal of Nanoelectronics and Optoelectronics, Vol. 5, 1-5, 2010. [H Index 12 Impact factor: 0.48]**
99. **Kalyanasundaram, N., and Palanisamy, P., "Active Bearing Estimation from a Mobile Two-dimensional Array of Sensors", IET Signal processing, vol 4, Issue 1, pp. 55-68, 2010. [H index 17 Impact Factor: 1.28].**

Indexed in DBLP but not in SCOPUS and Web of Science

100. **Kalyanasundaram, N., and Palanisamy, P., "Active Bearing Estimation from a Mobile Two-dimensional Array of Sensors", IET Signal processing, vol 4, Issue 1, pp. 55-68, 2010. [H index 17 Impact Factor: 1.28].**
101. **Kumar, M., Srivastava, M. C., and Kumar, U., "Tunable Multifunction Filter using Current Conveyor", International Journal of Computer Science and Information Security, Vol. 8, No.1 , pp. 104-107, April 2010.[Impact factor: 0.423]**
102. **Kumar, M., Kumar, U., and Srivastava, M. C., "Current Conveyor Based Multifunction Filter", International Journal of Computer Science and Information Security, vol. 7, no. 2 , pp. 104-107, 2010.[Impact factor: 0.423].**

Not indexed in SCOPUS/Web of Science/ DBLP but having Impact factors

103. **Srivastava, S., and Srivastava, M. C., "An Evolving Order Regularized Affine Projection Algorithm, suitable for Echo Cancellation" International Journal of Computer Science and Information Security, Vol. 8, No. 3, pp. 183-187, June 2010. [Impact factor: 0.423]**

104. **Gupta, S. D., and Srivastava, M. C.**, “Design of Frequency Agile Multidielectric Microstrip Antenna for Airborne Applications” International Journal of Microwave and Optical Technology (IJMOT), Vol. 5, pp. 257-266, September 2010. [**Impact factor: 0.2334**]

Others

105. **Gupta, R.**, and Venkatesh, K. S., "Set Theory and the Composition of ProcProfessors", Journal of Combinatorics, Information & System Sciences, Vol. 35, No. 3-4, pp. 415-428, July-Dec 2010.
106. **Gupta, R.**, and Venkatesh, K. S., "Preservance Topology and Complex Set Theory", Journal of Combinatorics, Information & System Sciences, Vol. 35, No. 3-4, pp. 403-413, July-Dec 2010.
107. **Khare, V.**, Santhosh, J. , Anand, S., and Bhatia, M., “Performance Comparison of Three Artificial Neural Network Methods for Classification of Electroencephalograph Signals of Five Mental Tasks,”Journal Biomedical Science & Engineering, vol. 3, no. 2, pp 200-205, Feb. 2010.
108. **Gupta, R.**, and Sharma, B. D., “Construction of Symmetrical Reversible Variable Length Codes for a Markovian Source”, International Journal of Information Science and Computer Mathematics, vol.2, pp 75-86, 2010.

2009

Indexed in SCOPUS

109. **Dwivedi, V. K.**, and Singh, G., “A novel bit error rate analysis and improved ICI reduction method in OFDM communication systems”, Journal of Infrared, Millimeter, and Terahertz Waves, 30 (11), pp. 1170-1180, Nov 2009.[**H Index 31 Impact factor 1.89**]
110. **Kalyanasundaram, N.**, Jindal, A., and Gupta, A., “ H_{∞} Bounds for Quasi-Newton Adaptive Algorithm,” Signal Processing, vol. 89, pp. 2304-2309, Nov. 2009. [**H index 77 Impact factor:1.135**]
111. Palanisamy, P., **Kalyanasundaram, N.**, and Raghunandan, A., “A New DOA Estimation Algorithm for Wideband Signals in the Presence of Unknown Spatially Correlated Noise”, Signal Processing, vol. 89, no. 10, pp. 1921- 1931, Oct. 2009. [**H index 77 Impact Factor: 1.256**]
112. Sahu, B., Chakrabarti, S., and **Maskara, S. L.**, “An improved residual frequency offset estimation scheme for OFDM based WLAN systems “, Digital Signal Processing: A Review Journal, 2009. [**H Index 37 Impact factor: 1.50**]
113. Sharma, R., Chakravarty, T., and **Bhattacharyya, A. B.**, “Empirical expressions for characteristic impedance of modified microstrip-like interconnections “, AEU - International Journal of Electronics and Communications, Vol. 64, Issue 7, pp. 682-684, 2009. [**H Index 29 Impact**

factor: 0.75]

114. Sharma, R., Chakravarty, T., and **Bhattacharyya, A. B.**, “Analytical model for optimum signal integrity in PCB interconnects using ground tracks”, IEEE Transactions on Electromagnetic Compatibility, 51 (1), pp. 67-77, 2009. [**H Index 58 Impact factor: 1.35]**
115. Sharma, R., Chakravarty, T., and **Bhattacharyya, A. B.**, “Analytical modelling of micro strip-like interconnections in presence of ground plane aperture”, IET Microwaves, Antennas and Propagation, 3 (1), pp. 14-22, 2009. [**H Index 40 Impact factor: 0.681]**
116. **Kalyanasundaram, N.**, and **Babu, G. N.**, “Dispersion of Electromagnetic Waves Guided by an Open tape Helix I”, Progress in Electromagnetic Research B, vol. 16, pp. 311-331, 2009. [**H index 21 Impact factor: 4.735]**.
117. Sahu, B., Chakrabarti, S., and **Maskara, S. L.**, “A New Timing Synchronization Metric for OFDM Based WLAN Systems”, Wireless Personal Communications, pp. 1-12, 2009. [**H Index 29 Impact factor: 0.458]**
118. Mantri, A., Dutt, S., **Gupta, J. P.**, and Chitkara, M., “Using PBL to deliver course in digital electronics,” Advances in Engineering Education, Vol. 1, Issue 4, ISSN: 19411766, pp. 1-17, 2009. [**H Index 7]**

Others

119. Garg, P., **Gupta, A.**, **Srivastava, M. C.**, Gupta, A., and Agarawal, N., “Edge Directed Error Diffused Digital Halftoning: A Steerable Filter Approach”, International Journal of Signal Processing, Image Processing and Pattern Recognition, vol 2, No 3, pp. 43-56, Sept. 2009.
120. Mehrotra, H., Agarawal, G., and **Srivastava, M. C.**, “Automatic Lip Contour Tracking and Visual Character Recognition for Computerized Lip Reading”, International Journal of Electrical and Computer Engg., vol 4, No. 1, pp. 62-71, 2009

2008

Indexed in SCOPUS

121. **Kalyansundaram, N.**, and Palanisamy, P., “Target Detection by Adaptive Noise Cancellation”, Electronics Letters, vol. 44, pp. 1329- 1330, Oct 2008. [**H index 112 Impact Factor: 1.009]**
122. Sharma, R., Chakravarty, T., and **Bhattacharyya, A. B.**, “Signal Integrity Issues in High-Speed Interconnects over a Ground Plane Aperture”, Journal of Electromagnetic Waves and Applications, vol. 22, no. 16, pp. 2231-2240, 2008. [**H index 36 Impact Factor: 3.134]**
123. Sharma, R., Chakravarty, T., and **Bhattacharyya, A. B.**, “Transient

Analysis of Microstrip-Like Interconnections Guarded by Ground Tracks”, Progress in Electromagnetic Research, vol. PIER 82, pp.189-202, 2008. [**H index 56 Impact Factor: 4.735**]

124. Mantri, A., Dutt, S., **Gupta, J. P.**, and Chitkara, M., “Design and evaluation of a PBL-based course in analog electronics”, IEEE Transactions on Education, Vol. 51, Issue 4, pp. 432-438, 2008.[**H Index 44 Impact factor: 1.221**]

Others

125. **Gupta, S. D.**, Garg, A., and Saran, A. P., "Improvement in Accuracy for Design of Multidielectric Layers Microstrip Patch Antenna", International Journal of Microwave and Optical Technology (IJMOT), vol. 3, no. 5, pp. 498-504, Nov. 2008.
126. Gupta, A., **Khandelwal, V.**, **Srivastava, M. C.**, and **Gupta, A.**, "Image Processing Methods for the restoration of Digitized Paintings", Thamasat Int. Journal of Science & Technology, ISSN: 0859-4074, vol. 13, No.3, pp. 66-72, July 2008.

2006

Indexed in SCOPUS

127. Sharma, R., Chakravarty, T., Bhooshan, S., and **Bhattacharyya, A. B.**, “Design of a novel 3 DB microstrip backward wave coupler using defected ground structure”, Progress in Electromagnetics Research, vol. 65, pp. 261-273,2006. [**H index 56 Impact Factor :4.735**]

Publications in National Journals

2012

Indexed in SCOPUS

128. **Jain, M.**, Gupta, M., and Kumar, B., “Wideband digital integrator and Differentiator,” IETE Journal of Research, Vol. 58, Issue 2, pp.166–170, March 2012. [**Impact factor:0.076**]
129. **Bhattacharyya, A. B.**, and Grabinski, W., “Compact modeling as a bridge between scaled semiconductor technologies and advanced designs of the integrated circuits,” IETE Journal of Research, vol. 58, issue 3, pp. 179-180, 2012.

Publications in International Conferences

2015

Indexed in SCOPUS

130. Bhowmik, W., Gupta Vibha Rani, **Srivastava, S.**, and Prasad. L., "Gain enhancement of butler matrix fed antenna array system by using planar

- circular EBG units." In Signal Processing and Communication (ICSC), 2015 International Conference on, pp. 183-188. IEEE, 2015. ISBN: 978-1-4799-6760-5.
131. Kumari, Vandana, and **Shweta Srivastava**. "Design and comparison of H-plane SIW horn antenna with different flare shapes." In Signal Processing and Communication (ICSC), 2015 International Conference on, pp. 462-465. IEEE, 2015. ISBN: 978-1-4799-6760-5.
 132. Dadel, Megha, K. P. Tiwary, and **Shweta Srivastava**. "Log periodic triangular patch array antenna with gap coupled feed." In Signal Processing and Communication (ICSC), 2015 International Conference on, pp. 99-104. IEEE, 2015. ISBN: 978-1-4799-6760-5.

 133. **Singh, Neetu**, and Rishab Bansal. "Analysis of Benford's law in digital image forensics." In Signal Processing and Communication (ICSC), 2015 International Conference on, pp. 413-418. IEEE, 2015. ISBN: 978-1-4799-6760-5.
 134. Gautam, Shreya, Shreety Sriya, and **Tanuj Chauhan**. "Focal and non-focal epilepsy detection using EEG signals via empirical mode decomposition." In Signal Processing and Communication (ICSC), 2015 International Conference on, pp. 452-455. IEEE, 2015. ISBN: 978-1-4799-6760-5.
 135. **Kashyap Abhishek**, B. Suresh, Agrawal Megha, **Gupta Hariom**, Joshi Shiv Dutt. "Detection of splicing forgery using wavelet decomposition," In International Conference on Computing, Communication and Automation (ICCCA 2015) on, pp. 843-848. IEEE, 15-16 May 2015. ISBN: 978-1-4799-8889-1
 136. **Joshi, Alok**, and Davinder S. Saini. "Performance analysis and peak-to-average power ratio reduction of concatenated LDPC coded OFDM system using low complexity PTS." In Signal Processing and Communication (ICSC), 2015 International Conference on, pp. 195-200. IEEE, 2015. ISBN: 978-1-4799-6760-5.
 137. **Tyagi, Shivaji**, and **A. B. Bhattacharyya**. "Analytical modeling and simulation of low-frequency Lorentz-force transduced micromechanical cantilever resonator." In Signal Processing and Communication (ICSC), 2015 International Conference on, pp. 337-342. IEEE, 2015. ISBN: 978-1-4799-6760-5.
 138. Gautam, Akansha, and **Richa Gupta**. "Enhancement of steganography scheme based on QC-LDPC codes." In Signal Processing and Communication (ICSC), 2015 International Conference on, pp. 10-13. IEEE, 2015. ISBN: 978-1-4799-6760-5.

139. Bharti, Suman, and **Richa Gupta**. "Construction algorithms for RVLCs: A survey." In Signal Processing and Communication (ICSC), 2015 International Conference on, pp. 456-461. IEEE, 2015. ISBN: 978-1-4799-6760-5.
140. Jain, Abhishek, and **Richa Gupta**. "A survey on defect and noise detection and correction algorithms in image sensors." In Computer Engineering and Applications (ICACEA), 2015 International Conference on Advances in, pp. 754-759. IEEE, 2015. INSPEC: 15324647
141. Jain, Abhishek, and **Richa Gupta**. "Gaussian filter threshold modulation for filtering flat and texture area of an image." In Computer Engineering and Applications (ICACEA), 2015 International Conference on Advances in, pp. 760-763. IEEE, 2015. INSPEC: 15324647
142. **Singh, K.**, and **A. B. Bhattacharyya**. "Analysis of Second-Order Effect Components of Drain Conductance and Its Implication on Output Resistance of Wilson Current Mirror." In VLSI Design (VLSID), 2015 28th International Conference on, pp. 529-534. IEEE, 2015. ISSN: 1063-9667
143. **Singh, Kirmender**. "Design methodology of standard analog circuit block using EKV MOSFET model and validation using BSIM3v3 MOSFET model." In Signal Processing and Communication (ICSC), 2015 International Conference on, pp. 318-323. IEEE, 2015. ISBN: 978-1-4799-6760-5.
144. Tewari, Shikhar, and **Kirmender Singh**. "Intuitive design of PTAT and CTAT circuits for MOSFET based temperature sensor using Inversion Coefficient based approach." In VLSI Design and Test (VDAT), 2015 19th International Symposium on, pp. 1-6. IEEE, 2015. ISBN: 978-1-4799-1742-6
145. Goel, Nidhi, and Megha Agarwal. "Smart grid networks: A state of the art review." In Signal Processing and Communication (ICSC), 2015 International Conference on, pp. 122-126. IEEE, 2015. ISBN: 978-1-4799-6760-5.
146. **Singh, S. V.**, R. S. Tomar, and D. S. Chauhan. "Single MCCTA based single input three output electronically tunable current-mode Active-C biquad filter." In Signal Processing and Communication (ICSC), 2015 International Conference on, pp. 267-272. IEEE, 2015. ISBN: 978-1-4799-6760-5.
147. Shankar, Chandra, and **Sajai Vir Singh**. "A low voltage operable VDTA based biquad filter realizing band pass and high pass filtering functions in trans-admittance-mode." IEEE International Conference on Computing, Communication and Automation (ICCCA 2015), Galgotia University, G. Noida (UP)-India (15-16 May 2015), pp. 1288-1293, 2015. ISBN: 978-1-4799-8889-1

148. **Kumar, Satyendra**, Kaushik Saha, and **Hariom Gupta**. "Run time write detection in SRAM." In Signal Processing and Communication (ICSC), 2015 International Conference on, pp. 328-333. IEEE, 2015. ISBN: 978-1-4799-6760-5.
149. Sharma, Sachin, **Vishal N. Saxena**, Kumar Goodwill, Sandeep Kr Singh, and Kshma Sharma. "CPW fed rectangular slot antenna with dual H-slot on ground for wideband wireless applications." In Signal Processing and Communication (ICSC), 2015 International Conference on, pp. 439-442. IEEE, 2015. ISBN: 978-1-4799-6760-5.
150. Gyan, Deepankar Shri, Sachin Sharma, **Vishal N. Saxena**, and Kumar Goodwill. "UWB antenna modification with four notches, for filtration of WiMAX and HiperLAN applications." In Signal Processing and Communication (ICSC), 2015 International Conference on, pp. 31-34. IEEE, 2015. ISBN: 978-1-4799-6760-5.
151. Sharma, Drishti, **Vishal N. Saxena**, and Pragya Singh. "A simple plasmonic based rectangular microstrip patch antenna resonating at 5.9 GHz." In Signal Processing and Communication (ICSC), 2015 International Conference on, pp. 386-389. IEEE, 2015. ISBN: 978-1-4799-6760-5.
152. **Verma, Gaurav**, Vikas Verma, Sumit Jhambhulkar, and Himanshu Verma. "Design of a Lead-Lag Compensator for Position Loop Control of a Gimballed Payload." In International Conference on Signal Processing and Integrated Networks (SPIN-2015) February 19-20, 2015 Amity University, Noida. pp 394 – 399. ISBN: 978-1-4799-5990-7
153. **Verma, Gaurav**, and Bishwajeet Pandey. "Thermal and Power Aware Internet of Things Enable RAM Design on FPGA." In International Conference on "Computing for Sustainable Global Development", 11th – 13th March, 2015 at Bharati Vidyapeeth's Institute of Computer Applications and Management (BVICAM), New Delhi (INDIA). pp 1537 – 1540, ISBN: 978-9-3805-4415-1
154. Gupta, Tukur, and **Gaurav Verma**. "Area & Power Optimization of VPB Peripheral Memory for ARM7TDMI Based Microcontrollers." International Conference on Cognitive Computing and Information Processing (CCIP-2015) March 3-4, 2015 JSSATEN, Noida. pp 1 – 6, INSPEC: 15109732
155. Kumar, Kailash, **Rahul Kaushik**, and **Roop Chandra Jain**. "Blind channel estimation for indoor optical wireless communication systems." In Signal Processing and Communication (ICSC), 2015 International Conference on, pp. 60-64. IEEE, 2015. ISBN: 978-1-4799-6760-5.
156. **Akhter, Shamim**, **Saurabh Chaturvedi**, and Kilari Pardhasardi. "CMOS Implementation of Efficient 16-Bit Square Root Carry-Select Adder." 2nd International Conference on Signal Processing and Integrated Networks (SPIN), India, Noida, pp. 891 – 896, Feb 2015. ISBN: 978-1-4799-5990-7

157. **Kumar, Anil.** "Optimized range-free 3D node localization in wireless sensor networks using firefly algorithm." In Signal Processing and Communication (ICSC), 2015 International Conference on, pp. 14-19. IEEE, 2015. ISBN: 978-1-4799-6760-5.
158. **Saini, Jasmine,** and Brajesh Kumar Dixit. "Microwave cavity resonator based dielectric constants measurement of solid materials." In Signal Processing and Communication (ICSC), 2015 International Conference on, pp. 56-59. IEEE, 2015. ISBN: 978-1-4799-6760-5.
159. **Saini, Jasmine,** Somya Agarwal, and Aditi Kansal. "Performance, analysis and comparison of digital adders." In Computer Engineering and Applications (ICACEA), 2015 International Conference on Advances in, pp. 80-83. IEEE, 2015. INSPEC: 15324647
160. **Kumar, Anand, Jitendra Mohan,** and **Hariom Gupta.** "Surface wave suppression of microstrip antenna using different EBG designs." In Signal Processing and Communication (ICSC), 2015 International Conference on, pp. 355-359. IEEE, 2015. ISBN: 978-1-4799-6760-5.
161. Aggarwal, Manish Kr, and **Vijay Khare.** "Automatic localization and contour detection of Optic disc." In Signal Processing and Communication (ICSC), 2015 International Conference on, pp. 406-409. IEEE, 2015. ISBN: 978-1-4799-6760-5.

Others

162. Deepankar S. Gyan, Sachin Sharma, **V. N. Saxena,** Kumar Goodwill, "UWB Antenna Modification with Four Notches, for Filtration of Wi-Max and HiperLAN Applications", IEEE International Conference on Signal Processing and Communication (ICSC-2015), JIIT Noida, p.p. 31-34, 16-18 March, 2015. **[IEEE Xplore]**
163. Sharma, S., **Saxena, V. N.,** Kumar Goodwill, Sandeep Kr. Singh, Kshma Sharma, "CPW Fed Rectangular Slot Antenna with Dual H-Slot on Ground for Wideband Wireless Applications", IEEE Sponsored International Conference on Signal Processing and Communication (ICSC-2015), JIIT Noida, p.p. 435-438, 16-18 March, 2015. **[IEEE Xplore]**
164. Sharma, D., **Saxena, V. N.,** Pragya Singh, P., "A Simple Plasmonic Based Rectangular Microstrip Patch Antenna resonating at 5.9 GHz", IEEE Sponsored International Conference on Signal Processing and Communication (ICSC-2015), JIIT Noida, p.p. 382-385, 16-18 March, 2015. **[IEEE Xplore]**
165. **Dubey, R. K.** and Kumar, A., "Comparison of Subjective and Objective Speech Quality Assessment for Different Degradations/Noise Conditions", in Proceeding of IEEE International Conference on Signal

- Processing and Communication (ICSC-2015) organized by Jaypee Institute of Information Technology, Noida, India (March 2015), pp. 261-266.[**IEEE Xplore**]
166. Parmar, N. and **Dubey, R. K.**, “Comparison of Performance of the Features of Speech Signal for Non-intrusive Speech Quality Assessment”, in Proceeding of IEEE International Conference on Signal Processing and Communication(ICSC-2015) organized by Jaypee Institute of Information Technology, Noida, India (March 2015), pp. 243-248.[**IEEE Xplore**]
167. **Tyagi, S. and Bhattacharyya, A. B.**, “Analytical Modeling and Simulation of Low-Frequency Lorentz-Force Transduced Micromechanical Cantilever Resonator”, in Proceeding of IEEE International Conference on Signal Processing and Communication (ICSC-2015), at IIIT Noida, March 2015. [**IEEE Xplore**]
168. Jain A. and **R. Gupta**, “Scaling the Uvm_Reg Model towards Automation and Simplicity of Use”, Proceedings of the 28th International Conference on VLSI Design and 14th International Conference on Embedded Systems, Bangalore, pp 164-169, 2015.

2014

Indexed in SCOPUS

169. Sinha, N., **Gupta, S. D.**, Singh, S., and Tyagi, N., “Improvement in Bandwidth Using Multidielectric Patch Antenna” 2014 International Conference on Signal Propagation and Computer Technology (ICSPCT), pp. 222 – 227, July 2014.

Others

170. **Akhter, S.**, and **Chaturvedi, S.**, “HDL Based Implementation of NxN Bit-Serial Multiplier,” Proc. of IEEE International Conference on Signal Processing and Integrated Networks (SPIN-2014), pp. 470-474, Feb. 20-21, 2014, Amity University, NOIDA, India. [**IEEE Xplore**]
171. **Akhter, S.**, and **Chaturvedi, S.**, “A Novel Method for Dual Output Dynamic Logic Using SCL Topology,” Proc. of IEEE International Conference on Signal Processing and Integrated Networks (SPIN-2014), pp. 481-485, Feb. 20-21, 2014, Amity University, NOIDA, India. [**IEEE Xplore**]

2013

Indexed in SCOPUS

172. **Kaushik, R.** and **Jain, R. C.**, "Comparison of Coded-BOC and Uncoded BOC Modulation for Radionavigation", International Conference on Signal

- Processing and Communication (ICSC-2013) December 12-14, 2013, IIIT NOIDA, India. [IEEE Xplore]
173. **Yadav, P. K.**, Kumar, G., **Dwivedi, V.**, and **Karwal, V.**, "Joint Chunk, Power and Bit Allocation in OFDMA Based LTE Systems", International Conference on Signal Processing and Communication (ICSC-2013) December 12-14, 2013, pp. 104-109, IIIT NOIDA, India. [IEEE Xplore]
 174. **Gupta, S. D.**, "Effect of Mutual Coupling in E and H plane on Microstrip Antenna Array Conformal on Cylindrical Surface", International Conference on Signal Processing and Communication (ICSC-2013) December 12-14, 2013, pp. 149-154, IIIT NOIDA, India. [IEEE Xplore]
 175. Kumar, G., **Saxena, V. N.**, and Kartikeyan, M. V., "Dual band CSSRR inspired microstrip patch antenna for enhancing antenna performance and size reduction", International Conference on Signal Processing and Communication (ICSC-2013) December 12-14, 2013, pp. 495-497, IIIT NOIDA, India. [IEEE Xplore]
 176. **Singh, M.**, "Bandwidth Enhancement using Small Triangles on Sierpinski Fractal", International Conference on Signal Processing and Communication (ICSC-2013) December 12-14, 2013, pp. 86-91, IIIT NOIDA, India. [IEEE Xplore]
 177. **Chauhan, T.** and **Karwal, V.**, "DFT Implementation Aspects and Techniques suitable for VLSI Implementation: A Survey," International Conference on Signal Processing and Communication (ICSC-2013) December 12-14, 2013, pp. 330-334, IIIT NOIDA, India. [IEEE Xplore]
 178. Srivastava, A. and **Karwal, V.**, "Windowed r-point Update Algorithm for Discrete Fourier Transform," International Conference on Signal Processing and Communication (ICSC-2013) December 12-14, 2013, pp. 185-190, IIIT NOIDA, India. [IEEE Xplore]
 179. **Dubey, R. K.** and Kumar, A., "Non-intrusive Objective Speech Quality Assessment using a Combination of MFCC, PLP and LSF Features", International Conference on Signal Processing and Communication (ICSC-2013) December 12-14, 2013, pp. 297-302, IIIT NOIDA, India. [IEEE Xplore]
 180. **Bhatnagar, S.** and **Jain, R. C.**, "Different Denoising Techniques for Medical Images in Wavelet Domain", International Conference on Signal Processing and Communication (ICSC-2013) December 12-14, 2013, pp. 325-329, IIIT NOIDA, India. [IEEE Xplore]
 181. **Akhter, S.**, **Karwal, V.** and **Jain, R. C.**, "Implementation of Rectangular Windowed Odd Discrete Cosine Transform Update Algorithm Using Distributed Arithmetic Approach", International Conference on Signal Processing and Communication (ICSC-2013) December 12-14, 2013, pp. 381-386, IIIT NOIDA, India. [IEEE Xplore]

182. Kandpal, M. and **Verma, G.**, "Classification of Ground Vehicles using Signal Processing and Neural Network Classifier", International Conference on Signal Processing and Communication (ICSC-2013) December 12-14, 2013, IIIT NOIDA, India. [**IEEE Xplore**].
183. **Kaushal, G.**, Maheshwaram, S., Dasgupta, S. and Manhas, S. K., "Matching Issues in Multi Gate CMOS Inverter", International Conference on Signal Processing and Communication (ICSC-2013) December 12-14, 2013, pp. 349-354, IIIT NOIDA, India. [**IEEE Xplore**]
184. **Kalra, S.**, "Effect of temperature dependence on performance of Digital CMOS circuit technologies", International Conference on Signal Processing and Communication (ICSC-2013) December 12-14, 2013, pp. 392-395, IIIT NOIDA, India. [**IEEE Xplore**].
185. **Saxena, S.**, "Effect of Contact Thickness on Electrical Properties of Organic Thin Film Transistors", International Conference on Signal Processing and Communication (ICSC-2013) December 12-14, 2013, pp. 387-391, IIIT NOIDA, India. [**IEEE Xplore**]
186. Mishra, S. and **Verma, G.**, "Low Power and Area Efficient Implementation of BCD Adder on FPGA", International Conference on Signal Processing and Communication (ICSC-2013) December 12-14, 2013, IIIT NOIDA, India. [**IEEE Xplore**].
187. Ahmad, M., Alam, B., Jain, A., and **Khare, V.**, "A novel octuple images encryption algorithm using chaos in wavelet domain", Lecture Notes in Electrical Engineering, 131 LNEE, pp. 509-518, 2013.
188. **Balyan, V.**, Saini, S. D., S., Singh, A. K., Agarwal, P., and Agarwal, P., "Neighbour Code Capacity and Reduction in Number of Code Searches", IEEE Conference on Information & Communication Technologies (ICT), pp. 589-593, 2013.
189. Baid, A., and **Srivastava, A. K.**, "Generating test patterns for fault detection in combinational circuits using genetic algorithm", Students Conference on Engineering and Systems, SCES 2013, 2013.
190. **Gupta, A.**, and Gupta, H., "Applications of MFCC and Vector Quantization in speaker recognition", International Conference on Intelligent Systems and Signal Processing, ISSP 2013, pp. 170-173, 2013.

Others

191. **J. Mohan** "Novel All-pass/Notch filter configuration with cascadability" IEEE 2013 International Conference on Signal Processing and Communication (ICSC-2013), Noida, India, 12-14 Dec 2013

192. **Dubey, R. K.**, and Kumar, A., “Non-intrusive Objective Speech Quality Evaluation using Features at Multiple Time Scales,” in Proceeding of International Conference, Acoustics 2013 New Delhi, organized by the Acoustical Society of India (ASI) and the French Acoustical Society (SFA) under the aegis of the CSIR-National Physical Laboratory, New Delhi, India, pp-1040-1046, Nov. 2013.
193. **J. Mohan, Chaturvedi B.**, Maheshwari S., “Grounded components based voltage-mode quadrature oscillators” IEEE International Conference on Multimedia, Signal Processing and Communication Technologies (IMPACT-2013), Aligarh, India, November 2013.
194. **Srivastava, A. K.**, Madan, S., **Chaturvedi, S.**, and **Gupta, H. O.**, “Synthesis of Evolvable Hardware with Different Clustering Techniques,” Proc. of 5th International Conference on Computer Applications in Electrical Engineering- Recent Advances (CERA-13), pp. 306-310, Oct. 3-5, 2013, Indian Institute of Technology Roorkee, India.
195. **Srivastava, A. K.**, and Madan, S., and **Gupta, H. O.** “Evolvable Hardware: A Synthesis and Analysis Using Different Evolutionary Strategies”, Proc. of 3rd International Conference on Computational Intelligence and Information Technology – CIIT 2013, Oct 2013, pp. 1 – 6, Mumbai.
196. Agarwal, M., and **Jain, R. C.**, "Spectral Analysis of Fractional Gaussian Noise via Empirical Mode Decomposition" in International Conference on Various Facets of Energy Technologies and its Management for Sustainable Development, Jawahar Lal Nehru University Delhi, 16 March 2013.

2012

Indexed in SCOPUS

197. **Akhter S., Karwal V.**, and **Jain, R. C.**, “Implementation of Odd Discrete Cosine Transform (ODCT-II) using Distributed Arithmetic Approach”, in Proceeding of 3rd Nirma University International Conference on Engineering (NUiCONE), Nirma University, Ahmedabad, pp. 1-6, Dec. 2012. [**IEEE xplore**].
198. Kumar, G., **Yadav, P. K.** and **Dwivedi, V. K.**, “Joint Chunk, Power and Bit Allocation in Multicast OFDMA System with Average BER Constraint”, in Proceeding of 3rd Nirma University International Conference on Engineering (NUiCONE), Nirma University, Ahmadabad, Dec. 2012. [**IEEE xplore**].
199. **Goel, A.**, and Garg, M. K., “Comparative Performance Evaluation of Convolutionally Coded and LDPC Coded OFDM System over AWGN and SUI-5 Fading Channel”, In Proceedings of IEEE International Conference on Computer and Communication Technology (ICCCT) 2012, Allahabad, India, pp. 250-254, Nov. 2012. [**IEEE xplore**]

200. Tripathi, S., and **Bhatnagar, S.**, "Speaker Recognition," International Conference on Computer & Communication Technology, ICCCT 2012, pp-283-287, Nov. 23-25, 2012. [**IEEE xplore**].
201. **Goel, A.**, Nagpal, A., and Kaur, J., "Optimized Data Allocation & Combining Scheme for ICI self Cancellation in OFDM Systems," In Proceedings of Ninth IEEE International Conference on Wireless and Optical Communications Networks (WOCN) 2012, pp.1-4, 20-22 Sept. 2012. [**IEEE xplore**]
202. Chauhan, V., and **Gupta, J.**, "Proposed Mobile Controlled Handoff (MCHO) in 3GPP Long Term Evolution (LTE) System," 5th International Conference on Contemporary Computing IC3-2012, Noida, India, vol. 306, pp. 294-305, August 6-8, 2012, Springer.
203. Puri, P., and **Singh, N.**, "Application of 2D OVSF Codes in OFCDM for Downlink Communication in 4G Systems," 5th International Conference on Contemporary Computing, IC3-2012, Noida, India, vol. 306 pp. 502-503, August 6-8, 2012.
204. **Joshi, A.**, and Saini, D. S., "PAPR reduction in OFDM with FEC (RS-CC and Turbo coding) using DHT pre-coding," **in proceedings of IEEE ICEOE-2012), Shenyang, China, pp. 14-18, July 2012.**
205. **Mohan, J.**, and Garg, G., "Minimum grounded component based voltage-mode quature oscillator using a single plus-type DO-DDCC", IEEE International Conference on Signal Processing, Computing and Control (ISPCC), JUIT Solan, India, pp. 1-4, March 2012.
206. Jain, A., Ahmad, M., and **Khare, V.**, "A ridgelet based symmetric multiple image encryption in wavelet domain using chaotic key image," Communications in Computer and Information Science, vol. 305- CCIS, pp. 135-144, 2012.
207. **Gupta, R.**, and **Sharma, B. D.**, "An Improved Bound on Average Codeword Length of Variable Length Error Correcting Codes", refereed Conference Volume entitled "Some Topics on Current Issues in Mathematical and Statistical Methods" by World Scientific Publishing Co. Ltd, Singapore, Dec. 2012.
208. **Gupta, R.**, and **Sharma, B. D.**, "A Comparative analysis of the Variable Length Error Correcting Code Generating Algorithms" Proceedings of Twenty first international conference of Forum for interdisciplinary Mathematics on interdisciplinary Mathematics, statistics and computational techniques , IMSCT 2012- FIM XXI, Chandigarh, Dec. 15-17, 2012.
209. **Gupta, R.**, and **Sharma, B. D.**, "Parametric Effect of Embedded Zerotree of Wavelets on Image Compression with Reversible Variable Length Code as an Entropy Encoder" Proceedings of Twenty first international conference of Forum for interdisciplinary Mathematics on interdisciplinary

- Mathematics, statistics and computational techniques, IMSCT 2012- FIM XXI, Chandigarh, Dec. 15-17, 2012.
210. Jhingan, S., Agarwal, S., and **Saini, J.**, "Modified Booth Dadda Multiplier," 1st International Conference on Innovations and Advancements in Information and Communication Technology", Gautam Buddha University, Greater Noida, Vol. 3, pp. 231-239, 30-31 March 2012.
 211. Aggarwal, M., **Kumar, M.**, and **Jain, R. C.**, "Fuzzy Logic Based Audio Surveillance System," Proc. of International Conference on Innovations and Advancements in Information and Communication Technology, Vol. 2, pp. 93-96, Gautam Buddha University, Greater Noida, 30-31 March, 2012.
 212. Sinha, A., Agarwal, M., Paliwal, S. and **Kumar, M.**, "Image enhancement Using Fuzzy Logic and Bacterial Foraging Optimization A Comparative Study," International Conference On Innovations And Advancements In Information And Communication Technology (ICIAICT), Greater Noida, pp. 97-103, 30-31 March 2012.
 213. **Kapoor, S.**, Taneja, D., and Malhotra, V., "Energy Competent Building Automation and Control" Study on Architecture Disparity", In: Proc. of International Conference on Advances in Electronics, Electrical and Computer Science Engineering (EEC2012), July 7-9, 2012, Dehradun, India.
 214. **Mohan, J.**, Garg, G., and Chauhan, D. S., "High Input Impedance Voltage-Mode Universal Biquadratic Filter with One Input and Five Outputs using plus-type DDCCs" International Conference on Advances in Electronics, Electrical and Computer Science Engineering - EEC 2012, Dehradun, India, pp 295-299, July 2012. DOI: 10.3850/978-981-07-2950-9 692.
 215. **Goel, P.**, and **Bhattacharyya, A. B.**, "DC Inductance Modeling of Coplanar Meander Inductor with Grounded Guard Ring," Proceedings of International Conference on Smart Materials Structures and Systems, Bangalore, India, January 4-7, 2012.
 216. **Mohan, J.**, Maheshwari, S., Garg, G., and Chauhan, D. S., "Two DDCC based cascaded voltage-mode first-order all-pass filters" International Conference on Advances in Electronics, Electrical and Computer Science Engineering - EEC 2012, Dehradun, India, pp 290-294.

2011

Indexed in SCOPUS

217. Maheshwari, S. and **Mohan, J.**, "CMOS compatible voltage-mode all-pass filters using minimum and grounded components", IEEE International Conference on Multimedia, Signal Processing and Communication Technologies (IMPACT), Aligarh, India, pp. 156-159, 17-19 December

2011. DOI: 10.1109/MSPCT.2011.6150463.
218. **Goel, A., Gupta Poddar, P.,** and Agarwal, M., “Two New Phase Sequence Sets for PAPR Reduction in SLM-OFDM Systems Without Side Information” In Proceedings of IEEE International Conference on Wireless Technologies for Humanitarian Relief (ACWR2011), pp. 41-46, December 2011, Amritapuri, India.
 219. **Singh, S. V.,** Maheshwari, S., and Chauhan, D. S., “Current-mode electronically tunable biquad filter,” IEEE International Conference on Multimedia, Signal Processing and Communication Technologies (IMPACT-2011), AMU, Aligarh, pp. 176-179, 17-19 Dec. 2011.
 220. **Singh, S. V.,** Maheshwari, S., and Chauhan, D. S., “Single CCCCTA-based SITO biquad filter with electronic tuning,” IEEE International Conference on Multimedia, Signal Processing and Communication Technologies (IMPACT-2011), AMU, Aligarh, pp. 172-175, 17-19 Dec. 2011.
 221. **Joshi, A.,** and Saini, D. S., “PAPR Analysis of Coded- OFDM System and Mitigating its Effect with Clipping, SLM and PTS," in **proceedings of IEEE International Conference (ICIMU-2011)**, pp. 1-5, **Malaysia, Nov. 14-16, 2011. DOI:10.1109/ICIMU.2011.6122749 [IEEE xplore]**
 222. **Joshi, A.,** and Saini, D. S., "Performance Analysis of Coded-OFDM with RS-CC and Turbo codes in Various Fading Environment," **Proceedings of IEEE International Conference (ICIMU-2011)**, pp. 1-6, **Malaysia, Nov. 14-16, 2011. DOI:10.1109/ICIMU.2011.6122750.**
 223. **Joshi, A.,** and Saini, D., S., “Performance Analysis of coded- OFDM with ICI due to Frequency Offset,” IET UK/IEEE International conference on Advances in Recent Technologies in communication & Computing, **ARTCOM-2011**, pp. 47-50, **Bangalore, Sept. 14-15, 2011. DOI:10.1049/ic.2011.0049.**
 224. **Gupta, J.,** Kumar, I., and Kacholiya, A., “Game Theoretic Approach to Resolve Energy Conflicts in Ad-Hoc Networks”, Proceedings of International Conference on Advances in Computing and Communications, Vol. 193, Springer, pp. 205-210, July, 2011.
 225. **Dwivedi, V. K.,** and Singh, G., “Analysis of Channel Capacity of Generalized-K Fading with Maximal-Ratio Combining Diversity Receivers”, International Conference on Communication Systems and Network Technologies (CSNT), pp. 550 – 553, June 2011.
 226. **Kalyanasundaram, N.,** and **Babu, G. N.,** “Propagation of Electromagnetic Waves Guided by an Open Tape Helix”, IEEE International Conference on Vacuum Tubes (IVEC-2011), Bangalore, India, pp 185-186, Feb 2011.
 227. **Kalyanasundaram, N., Saini, J.,** and **Babu, G. N.,** “Small-signal Field Analysis of Gyro-TWT Amplifier”, IEEE International Conference on

- Vacuum Tubes (IVEC-2011), Bangalore, India, pp 337-338, Feb 2011.
228. Sanon, K., and **Joshi, S.**, “Preemptive mobile assisted and guard channel based handoff queuing scheme”, Proceedings - 2011 Annual IEEE India Conference: Engineering Sustainable Solutions, INDICON-2011, article no. 6139427, 2011.
 229. Karmeshu, and **Khandelwal, V.**, “MGF and high order moment of channel capacity in log-normal fading environment”, Proceedings of the International Conference on Wireless Technologies for Humanitarian Relief, pp. 99-106, 2011.
 230. **Balyan, V.**, and Saini, D. S., “Code assignment and reassignment to reduce new code blocking in WCDMA networks”, International Conference on Software, Telecommunications and Computer Networks, SoftCOM 2011, article no. 6064400, pp. 414-418, 2011.
 231. **Balyan, V.**, and Saini, D. S., “Multi code assignment with minimum number of rakes for OVSF CDMA”, International Conference on Software, Telecommunications and Computer Networks, SoftCOM 2011, article no. 6064401, pp. 419-423, 2011.
 232. Mirza, K., Wajid, M., Rahman, S. A., **Srivastava, A.**, and Khatoun, M., "Reconfigurable VLSI architecture for graph color allocation with its chromatic number," Communications in Computer and Information Science, pp. 531-534, 2011.

Others

233. Mittal, S., and **Maskara, S. L.**, “A Review of Some Bayesian Belief Network Structure Learning Algorithms”, in Proceedings of 8th International Conference on Information, Communications and Signal Processing, Singapore, Dec 2011.
234. Mittal, S., and **Maskara, S. L.**, “A Review of Bayesian Belief Network Structure Learning Algorithms”, 8th International on Information, Communications and Signal Processing, Singapore, Dec. 13-16, 2011.
235. **Gupta, R.**, and Sharma, B. D., “Improved Combinatorial Bounds on Variable Length Error Correcting Codes”, Proceedings of the International Conference Statistics 2011 Canada-IMST 2011-FIM XX, July 2011, pp 109-121.
236. Nigam, V., **Bhatnagar, S.**, and Luthra, S., “Image Denoising using Wavelet Transform and Wavelet Transform with Enhanced Diversity”, International Conference on Control, Robotics and Cybernetics, ICCRC 2011, SRM University (NCR Campus) New Delhi, India ,VI pp. 293-296, 21-23 March, 2011.

2010

Indexed in SCOPUS

237. **Goel, A., Gupta, P.,** and Aggarwal, M., “Concentric Circle Mapping Based PTS for PAPR Reduction in OFDM without Side Information”, at The Sixth IEEE Conf. on Wireless Communications & Sensor Networks (WCSN 2010), 17-19 Dec. 2010, Allahabad India. [IEEE xplore].
238. Yadav, A., Varun, and **Kaur, G.,** “Design and Implementation of artificially intelligent microcontroller based Chess opponent”, in International Conference of Information Engineering organized by World Congress on Engineering and International Association of Engineers (IAENG), pp. 410-414, 30th June -2nd July, 2010, London, UK.
239. **Kaur, G.,** and Gupta, N., “Performance Comparison between Modified Prime Sequence Codes and Superimposed Optical Cyclic Orthogonal Codes for OCDMA System at 5Gbps bit rate”, in International Conference on Computer Science and Engineering organized by World Congress on Engineering and International Association of Engineers (IAENG), 30th June -2nd July, 2010, London, UK.
240. **Bhatnagar, S.,** Beakta, S., and Manchanda, R.,”Comparative Analysis of the Techniques for reducing PAPR in OFDM,” IEEE Region 8 SIBIRCON-2010, pp. 233-238, July 11-15, 2010, Irkutsk Listvyanka, Russia.
241. **Singh, K.,** and **Bhattacharyya, A. B.,** “Transconductance Related Analysis of EKV MOSFET Model for a 0.35 μm CMOS Technology Node”, 17th International Conference, Mixed Design of Integrated Circuits and Systems - MIXDES 2010”, pp. 436-440, June 24-26, 2010.
242. **Kaur, G.,** “Anomaly Detection in Network Traffic and Role of Wavelets”, Proceedings of International Conference on Computer Engineering and Technology, Chengdu, China, pp. 746-751, April 2010.
243. Mehta, M., Goyal, C., **Srivastava, M. C.,** and **Jain, R. C.,** “Real Time Object Detection and Tracking: Histogram Matching and Kalman Filter Approach” In Proc. ICCAE 2010, Singapore, pp. 796-801, Feb. 26-28, 2010.
244. Khurana, R., and **Kaur, G.,** "Performance comparison of OOC codes at various receivers for OCDMA system", Proceedings of 10th International Conference on Laser and Fiber-Optical Networks Modeling (LFNM 2010), pp.87-89, 2010.
245. Kedia, D., Duhan, M., and **Maskara, S. L.,** “Evaluation of correlation properties of orthogonal spreading codes for CDMA wireless mobile communication”, IEEE 2nd International Advance Computing Conference,

- IACC 2010, pp. 325-330, 2010.
246. Agarwal, M., Agrawal, H., Jain, N., and **Kumar, M.**, “Face recognition using principle component analysis, eigenface and neural network”, International Conference on Signal Acquisition and Processing, ICSAP 2010, pp. 310-314, 2010.
 247. Nigam, V., Luthra, S., and **Bhatnagar, S.**, “A Comparative Study of Thresholding Techniques for Image Denoising,” International Conference on Computer & Communication Technology, ICCCT2010, pp. 173-176, 2010.
 248. Khulbe, M., Srivastava, P., and **Jain, R. C.**, “Parametric Protocol for Energy Efficient Cluster Head Selection (PPEECS) in WSNs,” Proceedings of 16th Asia-Pacific Conference on Communications (APCC), Auckland, pp. 200-203, 2010.
 249. **Srivastava, A. K.**, and **Srivastava, M. C.**, “A combinatorial digital circuit with evolutionary algorithm for evolvable hardware software codesign,” ICPCES 2010 - International Conference on Power, Control and Embedded Systems, article no. 5698690, 2010.

2009

Indexed in SCOPUS

250. **Goel, A.**, Gupta, N., and Kumar, P., “A Speed Based Adaptive Algorithm for Reducing Paging Cost in Cellular Networks”, Proceedings of 2nd IEEE International Conference on Computer Science and Information Technology (ICCSIT 2009), Beijing, China, vol. 3, pp. 22-258-11 August, 2009.
251. Gupta, V., Chaudhary, S., and **Dwivedi, V. K.**, “Performance comparison of BPSK in Rayleigh and AWGN channel by Monte Carlo simulation method”, ITNG 2009 - 6th International Conference on Information Technology: New Generations, pp 1588-1589, 2009.
252. Kumar, S., Mittal, N., Gupta, S., **Kalyanasundaram, N.**, and **Jain, R. C.**, “An energy-efficient and low latency (E2L2) MAC protocol for low traffic wireless sensor networks”, Proceedings of 2009 IEEE Student Conference on Research and Development, pp. 16-21, 2009.
253. **Negi, Y. S.**, ‘Evaluation of audio based searching for Indian traditional music’, ACT 2009 - International Conference on Advances in Computing, Control and Telecommunication, pp. 255-259, 2009.
254. Chana, N., Yati, A. K., and **Bhattacharyya A. B.**, “Extended-Sakurai-Newton MOSFET model for ultra-deep-submicrometer CMOS digital design “, Proceedings of 22nd International Conference on VLSI Design - Held Jointly with 7th International Conference on Embedded Systems, art. no. 4749682, pp. 247-252, 2009.
255. Agarwal, M., Jain, N., **Kumar, M.**, and Agrawal, H., “Face recognition

- using principle component analysis, eigenface and neural network”, Proceedings of the 2nd WSEAS International Conference on Sensors and Signals, SENSIG '09, Visualization, Imaging and Simulation, VIS '09, Materials Science, MATERIALS '09, pp. 204-208, 2009.
256. **Yadav, K.**, Goyal, H., and Khatri, B., “Weighted levenshtein distance based viterbi algorithm using bit framing tree (BFT) method”, in IET Conference Publications (552 CP), 2009.
 257. Agnihotri, S., Loomba, H., **Gupta, A.**, and **Khandelwal, V.**, “Automated segmentation of gallstones in ultrasound images”, in 2ndIEEE International Conference on Computer Science and Information Technology, ICCSIT 2009 , art. no. 5234996, pp. 56-59, 2009.
 258. Gupta, A., **Khandelwal, V.**, **Agarwal, N.**, and **Gupta, A.**, “Five neighbor stochastic error diffusion for digital halftoning”, in 2nd IEEE International Conference on Computer Science and Information Technology, ICCSIT 2009, art. no. 5234845, pp. 87-91, 2009
 259. **Khandelwal, V.**, **Gupta, A.**, Kashyap, M., Gandhi, H., and Dhawan, A., “Natural image matting using HSI framework”, in 2nd IEEE International Conference on Computer Science and Information Technology, ICCSIT 2009, art. no. 5234833, pp. 141-144, 2009
 260. **Gupta, A.**, Mangal, S., Nagori, P., Jain, A., and **Khandelwal, V.**, “Video matting by automatic scribbling using quaa directional filling of segmented frames”, in 2nd IEEE International Conference on Computer Science and Information Technology, ICCSIT 2009, art. no. 5234624, pp. 336-340, 2009.
 261. Kumar, K., Jain, A., and **Srivastava, A. K.**, “FPGA implementation of image enhancement techniques”, Proceedings of SPIE - The International Society for Optical Engineering, Volume 7502, Article number 750208, 25-31 May, 2009.
 262. Khanna, S., Noor, A., **Tyagi, M. S.**, and Neeleshwar, S., “Interface states and barrier heights on metal/4H-SiC interfaces”, Materials Science Forum, vol. 615 617, pp. 427-430, 2009.

Indexed in Web of Science but not in SCOPUS

263. Kamboj, A., **Dwivedi, V. K.**, and Singh, G., “Bandwidth efficient inter-carrier interference cancellation technique for OFDM digital communication systems”, Progress In Electromagnetic Research Symposium (PIERS 2009), Moscow, Russia, pp. 1244-48, Aug. 2009.
264. **Dwivedi, V. K.**, Gupta, A., Kumar, R., and Singh, G., “Performance analysis of coded OFDM system using various coding schemes”, Progress In Electromagnetic Research Symposium (PIERS 2009) pp.1249-1252

Moscow, Russia, Aug., 2009.

265. Sharma, A., **Dwivedi, V. K.**, and Singh, G., “THz rectangular microstrip patch antenna on multilayered substrate for advanced wireless communication systems”, Progress In Electromagnetic Research Symposium (PIERS 2009), Beijing, CHINA, pp. 627-631 March 2009.
266. **Dwivedi, V. K.**, Kumar, P., and Singh, G., “Performance Analysis of OFDM Communication over Correlated Nakagami Fading Channel”, in Progress In Electromagnetic Research Symposium (PIERS 2009), China, pp. 1495-1498, March 2009.
267. Kumar, P., **Dwivedi, V. K.**, Singh, G., and Bhooshan, S., “Input Impedance of Gap-coupled Circular Microstrip with Shorting Post”, in Progress in Electromagnetics Research Symposium (PIERS 2009), China. pp. 1634-1638, 23-27 March 2009.
268. **Babu, G. N.**, Rustogi, A., and Jain, K., "Modelling of Interconnect Structures," International conference IEEE Tencon 2009, Singapore, pp. 23-26, November 2009.

2008

Indexed in SCOPUS

269. **Dwivedi, V. K.**, and Singh, G. “Improved BER Analysis of OFDM Communication System on Correlated Nakagami Fading Channel,” in International Conference on Recent Advances in Microwave Theory and Applications, India, pp.536-539, Nov. 2008.
270. **Dwivedi, V. K.**, Kumar P., and Singh, G., “A Novel Blind Frequency Offset Estimation Method for OFDM Systems”, in International Conference on Recent Advances in Microwave Theory and Applications, India, pp.668-675 Nov., 2008.
271. Jain, A., Agarwal, M., **Gupta, A.**, and **Khandelwal, V.**, “A Novel Approach to Video Matting using Automated Scribbling by Motion Analysis”, in IEEE Conference on Virtual Environments, Human-Computer Interfaces and Measurement Systems (VECIMS 2008), Istanbul, Turkey, pp. 25-30, July 14-16, 2008.
272. **Yadav, K.**, **Singh, N.**, and **Maskara, S. L.**, “Analysis of Adaptive Pilot Symbol Assisted Modulation with Optimized Pilot Symbol Spacing for Rayleigh Fading Channel”, in International Conference on Signal Processing, Communications and Networking (ICSCN’08), pp. 405-410, Jan. 2008.
273. **Bhatnagar, S.**, Gupta, R., and Singla, K. K., “Apparatus for ensuring seat belt usage and checking blood alcohol concentration”, IEEE International

- Conference on Robotics, Automation and Mechatronics, RAM 2008, ISBN: 9781424416769, art. no. 4681330, pp. 353-357, 2008.
274. **Khare, V.**, Santhosh, J., and Anand, S., "Performance comparison using five ANN methods for classification of EEG signals of two mental states", in IEEE Conference and Exhibition on Control, Communications and Automation 1, art. no. 4768792, pp. 7-10, 2008.
275. Kumar, P., **Dwivedi, V. K.**, Singh, G., and Bhooshan, S., "Miniaturization of gap-coupled circular microstrip antennas", 2008 International Conference of Recent Advances in Microwave Theory and Applications, MICROWAVE 2008, art. no. 4762984, pp. 489-491, 2008.
276. Khanna, S., Noor, A., Neeleshwar, S., and **Tyagi, M. S.**, "Modeling aspects of current calculation of 4H-SiC schottky diode", International Conference of Recent Advances in Microwave Theory and Applications, MICROWAVE 2008, ISBN: 9781424426904, art. no. 4763160, pp. 857-860, 2008.
277. **Kumar, M.**, Kumar, U., and **Srivastava, M. C.**, "Simulation and Realization of High Input Impedence Biquad with a Minimum Number of Passive Elements", HPCNCS, pp. 1-3, 2008.

Indexed in Web of Science but not in SCOPUS

278. Kumar, P., **Dwivedi, V. K.**, Singh, G., Bhooshan, S., and Chauhan, D. S., "Miniaturization of Coupled Circular Microstrip Patch Antenna", in International Conference on Recent Advances in Microwave Theory and Applications, India, pp. 489-491, Nov. 2008.
279. **Dwivedi, V. K.**, and Singh, G., "An efficient BER analysis of OFDM systems with ICI conjugate cancellation method", in Progress In Electromagnetic Research Symposium, Cambridge USA., pp. 166-171, July 2008.
280. Sharma, A., **Dwivedi, V. K.**, and Singh, G., "THz Rectangular Patch Microstrip Antenna Design Using Photonic Crystal as Substrate", in Progress In Electromagnetics Research Symposium (PIERS 2008), Cambridge USA, pp. 161-166, July 2008.

Others

281. **Gupta, S. D.**, Sharma, V. K., and Jain, A. "Design of a Microstrip Patch TCAS Antenna," in XXIXth URSI General Assembly, Chicago, Illinois, USA, pp. 9-16, August 2008.

2007

Indexed in SCOPUS

282. Gupta, A., **Srivastava, M. C.**, **Khandelwal, V.**, and **Gupta, A.**, "A Novel

- Approach to Fetal ECG Extraction and Enhancement Using Blind Source Separation (BSS-ICA) And Adaptive Fetal ECG Enhancer (AFE)”, in Sixth IEEE International Conference on Information, Communications and Signal Processing, NTU Singapore, pp. 1-4, Dec. 2007.
283. Hemant, K., Hamsavahini, Upadhyay, P., and **Akhter, S.**, “Design and implementation of crypto-based inter leaver for viterbi encoder and decoder using turbo codes”, in 7th International Conference on ASIC Proceeding ASICON, art. no. 4415778, pp. 906-909, Oct. 2007.
 284. **Yadav, K., Singh, N., and Maskara, S. L.**, “Analysis of Pilot Symbol Assisted Modulation using Modified Long Range Prediction Algorithm for Rayleigh Fading Channel”, in International Conference in Central Asia on Internet(ICI 2007), 3rd IEEE/IFIP, pp. 1-4, 26-28 Sept. 2007.
 285. **Kumar, M., Kumar, U., and Srivastava, M. C.**, “Lowpass – Highpass and Highpass - Bandpass Transadmittance Filter using Operational Amplifier”, in International Conference on High Performance Computing Networking and Communication Systems (HPCNCS),Florida (USA), pp. 168-170, July 2007.
 286. Trikha, M., Bandari, A., Gandhi, T., and **Khare, V.**, “Multi Channel Electrooculogram Classification Using Automata”, in International workshop on medical measurement and application (MeMeA 2007), Warsaw Poland, pp. 33-37, May 4-5, 2007.
 287. **Gupta, A., Srivastava, M. C., Pandey, S. D., and Bhandari, V.**, “Modified runlength coding for improved JPEG performance,” in IEEE conference ICICT at Bangladesh University of Engineering and Technology (BUET), Dhaka, Bangladesh, pp. 263-267, 2007.
 288. Bhandari, A., **Garg, K. K.**, and Trikha, M., “Wavelet based novel technique for analysis of genomic sequences with matched filtering”, Fourth International Conference on Information Technology and Applications, ICITA 2007, pp. 569-573, 2007.
 289. **Akhter, S.**, “VHDL implementation of fast NxN multiplier based on Vedic mathematic”, in 18th European Conference on Circuit Theory and Design, Sevilla, Spain, pp. 472-475, August 26-30, 2007.
 290. Bhandari, A., **Kumar, M.**, and Trikha, M.,” Squared pulse shaping filters for ISI-free communication in optical intensity channels “, IEEE 15th Signal Processing and Communications Applications, SIU, Art. No. 429877, 2007.

Others

291. **Gupta, S. D.**, and Rahul, “Design & Optimization of Planar Microstrip Patch Antenna Array Operating at Lower S-Band based on Analysis in both H and E Plane”, in 11th International Symposium on Microwave and

- Optical Technology (ISMOT 2007) Italy, pp 769-773, Dec. 2007.
292. **Gupta, S. D.**, and Rahul, "Optimization of Planar Microstrip Patch Antenna Designed for Lower S-Band", in Eleventh URSI Commission F Triennial Open Symposium on Radio Wave Propagation and Remote Sensing at Rio de Janeiro, Brazil, pp. 350-356, Oct. 2007.

2006

Indexed in SCOPUS

293. Bhandari, A., **Khare, V.**, Trikha M., and Anand, S., "Wavelet based novel technique for signal conditioning of electro-oculogram signals", in Annual India Conference, INDICON, art. no. 4086322, 2006.

Others

294. **Khare, V.**, Santhosh, J., and Anand, S., "Wavelet Based Compression Technique of Electro-Oculogram Signals", in International conference on Biomedical Engineering, Kuala Lumpur, Malaysia, vol. 15, pp. 436-440, Dec. 11-14, 2006.

2005

Indexed in SCOPUS

295. **Bhattacharyya, A. B.**, "Tutorial: Compact MOSFET models for low power analog CMOS design", in Proceedings of the IEEE International Conference on VLSI Design, pp.1-5, 2005.

Publications in National Conferences

2013

Indexed in SCOPUS

296. Varshney, N., and **Jain, R. C.**, "An Adaptive Notch filter for narrowband interference removal", Proc. of Nineteenth National Conference on Communication (NCC-2013), IIT Delhi, India, Feb. 16-17, 2013. [**IEEE xplore**].
297. **Goel, A.**, and Agrawal, M., "Data Combining Scheme for ICI Conjugate Cancellation Scheme in OFDM Systems", Proc. of Nineteenth National Conference on Communication (NCC-2013), IIT Delhi, India, pp.1-5, Feb. 16-17, 2013. [**IEEE xplore**].

Others

298. Malhotra, A., and **Jain, R. C.**, "Fractal and their application in communication engineering", Proc. of National Conference on Emerging Trends in Mobile Communications (EMTC) 2013, Ajay Kumar Garg Engineering College (AKGEC), Ghaziabad (U.P), pp. 68-72, 15-16 March,

2013.

299. Agarwal, S., and **Jain, R. C.**, “Adaptive Polynomial Volterra Filters for Suppression of BPSK Broadband Interference”, Proc. of National Conference on Emerging Trends in Mobile Communications ETMC 2013, Ajay Kumar Garg Engineering College (AKGEC), Ghaziabad (U.P), pp. 108-115, 15-16 March, 2013.
300. Malhotra, A., and **Jain, R. C.**, “Fractals and their applications in Network Traffic Engineering,” Proc. of National Conference on Wireless Communication and Emerging Trends, Academy of Business and Engineering Sciences Engineering College (ABES), Ghaziabad (U.P), 8-9 Feb, 2013.
301. Agarwal, S., and **Jain, R. C.**, “Broadband interference suppression with 2nd order nonlinear adaptive Volterra filter”, Proc. of National Conference on Wireless Communication and Emerging Trends, Academy of Business and Engineering Sciences Engineering College (ABES), Ghaziabad (U.P) , 8-9 Feb, 2013.

2011

Others

302. Khare, A., Varshney, S., and **Karwal, V.**, "Bartlett Windowed fast computation of discrete trigonometric transforms for real-time data processing", Conference on Signal Processing and Real time operating system (SPRTOS), HBTI Kanpur, March 26-27, pp. 456-462, 2011.

2009

Others

303. Sharma, B. D, and **Gupta, R.**, “Directions in Optimal Error Correction Coding: Variable Length Error Coding, a class of Distances and Reversible Variable Length Codes”, Proceedings of the National Symposium on Mathematical Methods and Applications 2009, Indian Institute of Technology Maas, Chennai, pp 1-12, 2009.

2008

Others

304. Godha, A., Sharma, R., and **Yadav, P. K.**, “Development of LAPDm Protocol in Mode at MS side of GSM Network”, in National Conference on Modern Trends in Electronics and Communication Systems (MTECS-08), Aligarh Muslim University, Aligarh, pp. 213-217, March 2008.
305. **Kumar, M.**, Kumar, U., and **Srivastava, M. C.**, “Simulation of Highpass and Bandpass Filter Using FTFN”, in SPIT-IEEE International Conference, Mumbai, India, pp. 19-21, Feb. 2008.

Chapter in Edited Books:

1. **Shamim, A.** "High Speed Reconfigurable FPGA based Digital Filter" in Quality, Reliability, Security and Robustness in Heterogeneous Networks, Springer 2013, ISBN: 978-3-642-37948 -2.

Books with ISBN with details of publishers:

1. **Srivastava, M.C. and Srivastava A. K.**, "Digital Design: An HDL Based Approach", Cengage Learning, 2011. ISBN: 9788131511718
2. **Bhattacharyya, A. B.**, "Compact MOSFET Models for VLSI Design" Wiley and IEEE Publications, 2009. ISBN: 0470823429/ ISBN-13: 9780470823422.
3. Srivastava, M., **Srivastava, M. C. and Bhatnagar, S.**, "Control Systems" McGraw Hill Education (TMH), 2008. ISBN-09:0070087644.
4. **Akhter, S.**, "Digital Hardware Design" University Science Press, 2008. ISBN: 978-81-318-0435-3.

Number listed in International Database: 85

***Citation Index: Google**

Faculty Name	Publication Details	No. of Citations (as per Google Scholar)
Richa Gupta	Jain A. and R. Gupta, "Scaling the Uvm_Reg Model towards Automation and Simplicity of Use", Proceedings of the 28th International Conference on VLSI Design and 14th International Conference on Embedded Systems, Bangalore, pp 164-169, 2015	1
Richa Gupta	Jain A. and R. Gupta, "Scaling the Uvm_Reg Model towards Automation and Simplicity of Use", Proceedings of the 28th International Conference on VLSI Design and 14th International Conference on Embedded Systems, Bangalore, pp 164-169, 2015	1
Richa Gupta	Goel R., and Gupta R., "Redesigning of the construction of symmetric RVLCs based on graph model", International Journal of Information and Computation Technology, Vol. 4, No. 11, 2014, pp 1063-1068.	1

Jitendra Mohan	Mohan J., and Maheshwari S., "Additional high-input low-output impedance voltage-mode all-pass sections" Journal of Circuits, Systems and Computers, vol.23, 2014. DOI: 10.1142/S0218126614500777	2
Ashish Goel	Goel, A., Gupta Poddar, P., and Agrawal, M., "Generalized M-2M Mapping Scheme for SLM and PTS Based Methods for PAPR Reduction in OFDM Systems without Side-Information," Wireless Personal Communications, Springer, vol. 74, no. 2, pp. 285-305, Jan. 2014.	3
Shamim Akhter	Akhter, S., Karwal, V., and Jain, R. C., "Fast Windowed Update Algorithm for ODCT Computation," International Journal of Electronics Letters, Taylor & Francis, doi:10.1080/21681724.2014.880989, vol. 2, no. 2, 2014.	1
Shamim Akhter	Akhter, S., and Chaturvedi, S., "A Novel Method for Dual Output Dynamic Logic Using SCL Topology," Proc. of IEEE International Conference on Signal Processing and Integrated Networks (SPIN-2014), pp. 481-485, Feb. 20-21, 2014, Amity University, NOIDA, India.	1
Madhu Jain	Jain, Madhu, and N. K. Jain, "The Design of the IIR Differintegrator and its Application in Edge Detection." Journal of information processing systems 10, Vol. no. 2, pp. 223-239, 2014	1
Madhu Jain	Jain, Madhu, Maneesha Gupta, and N. K. Jain, "Analysis and Design of Digital IIR Integrators and Differentiators Using Minimax and Pole, Zero, and Constant Optimization Methods." ISRN Electronics 2013, 2013.	2
Abhishek Kashyap	Kashyap, A., Joshi, S. D., "Detection of copy-move forgery using wavelet decomposition," IEEE International Conference on Signal Processing and Communication (ICSC-2013), December 12-14, 2013, pp. 396-400, IIIT NOIDA, India.	1
Shamim Akhter	Akhter, S., Karwal, V. and Jain, R. C., "Implementation of Rectangular Windowed Odd Discrete Cosine Transform Update Algorithm Using Distributed Arithmetic Approach", International Conference on Signal Processing and Communication (ICSC-2013) December 12-14, 2013, pp. 381-386, IIIT NOIDA, India.	1

Samir Dev Gupta	Gupta, S. D., "Effect of Mutual Coupling in E and H plane on Microstrip Antenna Array Conformal on Cylindrical Surface", International Conference on Signal Processing and Communication (ICSC-2013) December 12-14, 2013, pp. 149-154, IIIT NOIDA, India. [IEEE Xplore]	1
Alok Joshi	Joshi A., Saini, D. S., "Peak-to-Average Power Ratio Reduction of OFDM signals Using Improved PTS Scheme with Low Computational Complexity", in WSEAS Trans. on Communication. Vol. 12, Issue 12, pp. 630-640, December 2013. (indexed in Scopus, Elsevier, Inspec/IET)	1
Pushpendra Singh	P. Singh, P. K. Srivastava, R. K. Patney, S. D. Joshi, K. Saha, "Nonpolynomial spline based Empirical Mode Decomposition," 2013 International Conference on Signal Processing and Communication (ICSC), pp. 435-440, Dec. 2013.	4
Vineet Khandelwal	Khandelwal, V., and Karmeshu, "A New Approximation for Average Symbol Error Probability over Log-Normal Channels," IEEE Wireless Communications Letters, Volume PP, Issue 99, pp.1-4, Nov.2013.	3
R. C. Jain	N. Varshney, R C Jain, "A Novel MMSE Based Predictive Method for Narrow Band Interference Removal," 16th International Conference on Network-Based Information Systems (NBIS), South Korea, Sept. 2013.	1
R. C. Jain	Varshney, N., and Jain, R. C., "An Adaptive Notch filter for narrowband interference removal", Proc. of Nineteenth National Conference on Communication (NCC-2013), IIT Delhi, India, Feb. 16-17, 2013.	7
Alok Joshi	Joshi A., Saini D. S., "Performance Analysis and PAPR Reduction of Coded OFDM System using Modified SLM, PTS and DHT Pre-coding in Fading Environments", in WSEAS trans. on Communication. Vol. 12, Issue 1, pp.14-28, January 2013. (indexed in Scopus, Elsevier, Inspec/IET)	1
Rajesh Dubey	R. K. Dubey and A. Kumar, "Non-intrusive Speech Quality Assessment using Several Combinations of Auditory Features", in International Journal of Speech Technology, Springer, Vol. 16, Issue 1, pp. 89-101, Jan. 2013.	3
Ashish Goel	A. Goel, P. Gupta and M. Agrawal, "SER Analysis of PTS Based Techniques for PAPR Reduction in OFDM Systems" Digital Signal Processing, Elsevier, vol. 23, no.1, pp. 302-313, Jan. 2013.	4

Jasmine Saini	Kalyanasundaram, N., and Jasmine Saini, "On small-signal amplification of a TM ₀₁ circular cylindrical wave-guide mode in a Gyro-TWT", IET Microwaves, Antennas & Propagation, vol.7, pp.644-655, 2013	1
Bhartendu Chaturvedi	S. Maheshwari and B. Chaturvedi, "Additional High Input Low Output Impedance Analog Networks," Active and Passive Electronic Components, vol. 2013, Article ID 574925, 9 pages, 2013. (doi:10.1155/2013/574925)	3
Jitendra Mohan	J. Mohan, S. Maheshwari, "Cascadable Current-Mode First-Order All-Pass Filter Based on Minimal Components," The Scientific World Journal, vol. 2013, Article ID 859784, 5 pages, 2013. doi:10.1155/2013/859784	6
Shruti Kalra	Kalra, Shruti. "Effect of temperature dependence on performance of Digital CMOS circuit technologies." 2013 International Conference on Signal Processing and Communication (ICSC), 2013.	1
Bhartendu Chaturvedi	B. Chaturvedi and S. Maheshwari, "Third-Order Quadrature Oscillator Circuit with Current and Voltage Outputs," ISRN Electronics, vol. 2013, Article ID 385062, 8 pages, 2013. (doi:10.1155/2013/385062)	2
Bhartendu Chaturvedi	J. Mohan, B. Chaturvedi, S. Maheshwari, "Novel Current-Mode All-Pass Filter with Minimum Component Count", International Journal of Image, Graphics and Signal Processing (IJIGSP), vol. 5, pp.32-37, 2013.	3
Jitendra Mohan	J. Mohan, "Single Active Element based Current-Mode All-Pass Filter." International Journal of Computer Applications, vol. 82, no. 1, pp. 23-27, 2013. DOI: 10.5120/14080-2074	1
Jitendra Mohan	J. Mohan, B. Chaturvedi, S. Maheshwari, "Novel Current-Mode All-Pass Filter with Minimum Component Count", International Journal of Image, Graphics and Signal Processing (IJIGSP), vol.5, no.12, pp.32-37, 2013.DOI: 10.5815/ijigsp.2013.12.05	3
Jitendra Mohan	J. Mohan, S. Maheshwari, "Two Active Elements Based All-Pass Section Suited for Current-Mode Cascading" World Academy of Science, Engineering and Technology, International Science Index 73, International Journal of Electrical Engineering, vol. 7, No. 1, pp. 1217 – 1221, 2013.	1
Vivek Dwivedi	Kumar, G., Yadav, P. K. and Dwivedi, V. K., "Joint Chunk, Power and Bit Allocation in Multicast OFDMA System with Average BER Constraint", in Proceeding of 3rd Nirma	1

	University International Conference on Engineering (NUiCONE), Nirma University, Ahmadabad, Dec. 2012.	
Madhu Jain	Gupta, Maneesha, Madhu Jain, and B. Kumar, "Wideband digital integrator and differentiator." IETE Journal of Research, Vol. 58.2 pp. 166-170, 2012	4
Madhu Jain	Jain, Madhu, Maneesha Gupta, and Nitin Jain, "Linear phase second order recursive digital integrators and differentiators." Radioengineering 21, Vol. no. 2 , 2012.	11
Ashish Goel	A. Goel and M. K. Garg, "Comparative Performance Evaluation of Convolutionally Coded and LDPC Coded OFDM System over AWGN and SUI-5 Fading Channel", In Proceedings of IEEE International Conference on Computer and Communication Technology (ICCCT) 2012, Nov 2012, Allahabad, India, pp. 250-254.	1
Smriti Bhatnagar	Tripathi, S., and Bhatnagar, S., "Speaker Recognition," International Conference on Computer & Communication Technology, ICCCT 2012, pp-283-287, Nov. 23-25, 2012	2
Alok Joshi	A. Joshi, Davinder S. Saini ,Dhruv Bhardwaj "PAPR reduction in OFDM with FEC (RS-CC and Turbo coding) system using DHT pre-coding" IEEE International symposium (IMSNA-2012), July,2012 Shenyang, China.(indexed in IEEE Xplore)	2
Vivek Dwivedi	Dwivedi, V. K., and Singh, G., "Marginal moment generating function based analysis of channel capacity over correlated Nakagami-m fading with maximal-ratio combining diversity," Progress In Electromagnetic Research B, vol. 41, pp. 333-356, June 2012	3
Monika	Monika and Manish Kumar Gupta, "Comparison between Metamaterial based circular patch antenna and Microstrip-patch Antenna" International Journal of Engineering Research and Applications (IJERA) Vol 2. Issue3, May – June 2012, pp. 574 – 579.	5
Ashish Goel	A. Goel, P. Gupta Poddar and M. Agrawal, "M-ary Chaotic Sequence Based SLM-OFDM System for PAPR Reduction without Side-Information" International Journal of Computer and Communication Engineering, WASET, vol. 6, pp. 299-304, May 2012.	2
Monika	Monika, Navita Singh and Avinash Kumar, "Recent Progress in Synthesis Techniques of Microstrip Bandpass Filter", International Journal of Electronics and Computer Science	1

	Engineering (IJECS) Vol.1 No. 2, pp 479 – 484, April 2012.	
Vivek Dwivedi	Dwivedi, V. K., and Singh, G., “Repeated correlative coding scheme for mitigation of ICI in OFDM systems,” IET Communications, vol.6, pp 599-603, April 2012.	6
Vivek Dwivedi	V. K. Dwivedi, G Singh, " A novel mgf based analysis of channel capacity of generalized-k fading with maximal-ratio combining diversity," Progress In Electromagnetics Research C, 26, pp. 153-165, 2012.	2
Jitendra Mohan	Mohan, J., and Garg, G., “Minimum grounded component based voltage-mode quature oscillator using a single plus-type DO-DDCC”, IEEE International Conference on Signal Processing, Computing and Control (ISPC), JUIT Solan, India, pp. 1-4, March 2012.	1
Jitendra Mohan	S. Maheshwari, J. Mohan, D. S. Chauhan, “Novel Voltage-Mode Cascadable All-Pass Sections Employing Grounded Passive Components” Journal of Circuits, Systems and Computers Vol. 22, No. 1, pp.1-12, 2012. DOI: 10.1142/S021812661250065X	6
Samir Dev Gupta	SD Gupta, MC Srivastava, "Multilayer Microstrip Antenna Quality Factor Optimization for Bandwidth Enhancement" Journal of Engineering Science and Technology 7(6), 756-773, 2012.	7
Shamim Akhter	Akhter, S., Karwal, V., and Jain, R. C., “Improved Algorithm for ODCCT Computation of a Running Data Sequence,” Journal of Electrical and Computer Engineering, vol. 2012, pp. 1-10, Article ID 879626, 2012. doi:10.1155/2012/879626.	4
Abhinav Gupta	Gupta, A., and Karmeshu, “Statistical characterisation of speckle in clinical echocardiographic images with Pearson family of distributions”, Defence Science Journal, vol. 61, issue 5, pp. 473-478, 2011.	3
Vivek Dwivedi	Dwivedi, V. K., and Singh, G., “A novel moment generating function based performance analysis over correlated Nakagami-m fading”, Journal of Computational Electronics, vol. 10, no. 4, pp.373-381, Dec. 2011.	3

Ashish Goel	A. Goel, P. Gupta Poddar and M. Agrawal, "Two New Phase Sequence Sets for PAPR Reduction in SLM-OFDM Systems Without Side Information" In Proceedings of IEEE International Conference on Wireless Technologies for Humanitarian Relief (ACWR2011), December 2011, Amritapuri, India, pp 41-46.	3
Jitendra Mohan	S. Maheshwari, J. Mohan "CMOS compatible voltage-mode all-pass filters using minimum and grounded components." IEEE International Conference on Multimedia, Signal Processing and Communication Technologies (IMPACT), Aligarh, India, pp 156-159, December 2011. DOI:	2
Alok Joshi	A. Joshi, Davinder S. Saini, " PAPR Analysis of Coded-OFDM System and Mitigating its Effect with Clipping, SLM and PTS ", in proceedings of IEEE International Conference (ICIMU-2011), Malaysia, Nov. 14-16, 2011. (indexed in IEEE Xplore)	4
Alok Joshi	A. Joshi, Davinder S. Saini, " Performance Analysis Of Coded-OFDM with RS-CC and Turbo codes in Various Fading Environment ", in proceedings of IEEE International Conference (ICIMU-2011), Malaysia, Nov. 14-16, 2011. (indexed in IEEE Xplore)	9
Alok Joshi	A. Joshi, Davinder S. Saini, "Performance Analysis of coded- OFDM with ICI due to Frequency Offset", IET UK/IEEE International conference on Advances in Recent Technologies in communication & Computing, ARTCOM-2011;Bangalore, Sept14-15,2011 (indexed in IEEE Xplore and IET DL)	3
Juhi Gupta	Juhi Gupta, Ishan Kumar and Anil Kacholiya, "Game Theoretic Approach to resolve Energy Conflicts in Ad-Hoc networks", ACC-2011, International Workshop on Identity: Security, Management & Applications (ID 2011) (Springer) Kochi, Kerala, India , ACC 2011, July 22-24, 2011.	3
Richa Gupta	Gupta, R. and Sharma, B.D., "Improved Combinatorial Bound on Variable Length Error Correcting Codes", Proceedings of the International Conference Statistics 2011 Canada-IMST 2011-FIM XX, July 2011, pp 109-121.	1

Vivek Dwivedi	Dwivedi, V. K., and Singh, G., "Analysis of Channel Capacity of Generalized-K Fading with Maximal-Ratio Combining Diversity Receivers", International Conference on Communication Systems and Network Technologies (CSNT), pp. 550 – 553, June 2011.	2
Jasmine Saini	N. Kalyanasundaram, Jasmine Saini, G. Naveen Babu., "Small-signal Field Analysis of Gyro-TWT Amplifier", IEEE International Conference on Vacuum Tubes (IVEC-2011), Bangalore, India, pp 337-338, Feb 2011.	1
Samir Dev Gupta	SD Gupta, A Singh, "Design and Analysis of Multidielectric Layer Microstrip Antenna with varying Superstrate Layer Characteristics", International Journal of Advances in Engineering Technology 3, 55-68, 2011.	10
Jasmine Saini	Kalyanasundaram, N., and Saini J., "On Small Signal Amplification in a Gyro-TWT", Progress in Electromagnetic Research C, vol. 21, pp. 75-86, 2011.	2
Vivek Dwivedi	V K Dwivedi, G Singh, "Error-rate analysis of the OFDM for correlated Nakagami-m fading channel by using maximal-ratio combining diversity," International Journal of Microwave and Wireless Technologies 3 (06), pp. 717-726, 2011.	5
Ashish Goel	A. Goel, P. Gupta and M. Agrawal, "Concentric Circle Mapping Based PTS for PAPR Reduction in OFDM without Side Information" In Proceedings of 6th IEEE Conference on Wireless Communication and Sensor Networks(WCSN2010), December 2010, Allahabad, India, pp 201-204.	5
Abhinav Gupta	Gupta, A., Gosain, B., and Kaushal, S., "A Comparison of Two Algorithms For Automated Stone Detection in Clinical B-Mode Ultrasound Images Of The Abdomen ", Journal of Clinical Monitoring And Computing, Springer, vol.25(5), pp.341-362, Aug 2010.	7
Smriti Bhatnagar	Bhatnagar, S., Beakta, S., and Manchanda, R., "Comparative Analysis of the Techniques for reducing PAPR in OFDM," IEEE Region 8 SIBIRCON-2010, pp. 233-238, July 11-15, 2010, Irkutsk Listvyanka, Russia	1
R. C. Jain	Mehta, M., Goyal, C., Srivastava, M. C., and Jain, R. C., "Real Time Object Detection and Tracking: Histogram Matching and Kalman Filter Approach" In Proc. ICCAE 2010, Singapore, pp. 796-801, Feb. 26-28, 2010.	13

R. C. Jain	Khulbe, M., Srivastava, P., and Jain, R. C., "Parametric Protocol for Energy Efficient Cluster Head Selection (PPEECS) in WSNs," Proceedings of 16th Asia-Pacific Conference on Communications (APCC), Auckland, pp. 200-203, 2010.	2
Samir Dev Gupta	S D Gupta, M C Srivastava, "Design of Frequency Agile Multidielectric Microstrip Antenna for Airborne Applications", International Journal of Microwave and Optical Technology (IJMOT) 5(5), 257-266, 2010.	1
Smriti Bhatnagar	Nigam, V., Luthra, S., and Bhatnagar, S., "A Comparative Study of Thresholding Techniques for Image Denoising," International Conference on Computer & Communication Technology, ICCCT 2010, pp. 173-176, 2010.	4
Vivek Dwivedi	Dwivedi, V. K., and Singh, G., "A novel bit error rate analysis and improved ICI reduction method in OFDM communication systems", Journal of Infrared, Millimeter, and Terahertz Waves, 30 (11), pp. 1170-1180, Nov 2009.	6
Abhinav Gupta	Garg, P., Gupta, A., Srivastava, M. C., Gupta, A., and Agarawal, N., "Edge Directed Error Diffused Digital Halftoning: A Steerable Filter Approach", International Journal of Signal Processing, Image Processing and Pattern Recognition, vol 2, No 3, pp. 43-56, Sept. 2009.	3
Vivek Dwivedi	Dwivedi, V. K., Gupta, A., Kumar, R., and Singh, G., "Performance analysis of coded OFDM system using various coding schemes", Progress In Electromagnetic Research Symposium (PIERS 2009) pp.1249-1252 Moscow, Russia, Aug., 2009.	2
Vivek Dwivedi	Kamboj, A., Dwivedi, V. K., and Singh, G., "Bandwidth efficient inter-carrier interference cancellation technique for OFDM digital communication systems", Progress In Electromagnetic Research Symposium (PIERS 2009), Moscow, Russia, pp. 1244-48, Aug. 2009.	2
Vivek Dwivedi	Dwivedi, V. K., Kumar, P., and Singh, G., "Performance Analysis of OFDM Communication over Correlated Nakagami Fading Channel", in Progress In Electromagnetic Research Symposium (PIERS 2009), China, pp. 1495-1498, March 2009	2

Ashish Goel	A. Goel, N. Gupta and P. Kumar “A Speed Based Adaptive Algorithm for Reducing Paging Cost in Cellular Networks” , In Proceedings of 2nd IEEE international Conference on Computer Science and Information Technology (ICCSIT 2009), Beijing, China, August, 2009, vol. 3 , pp. 22-25.	11
Vivek Dwivedi	Kumar, P., Dwivedi, V. K., Singh, G., and Bhooshan, S., “Input Impedance of Gap-coupled Circular Microstrip with Shorting Post”, in Progress in Electromagnetics Research Symposium (PIERS 2009), China. pp. 1634-1638, 23-27 March 2009.	3
Vivek Dwivedi	Sharma, A., Dwivedi, V. K., and Singh, G, “THz rectangular microstrip patch antenna on multilayered substrate for advanced wireless communication systems”, Progress In Electromagnetic Research Symposium (PIERS 2009), Beijing, CHINA, pp. 627-631 March 2009.	6
Vineet Khandelwal	Gupta, A., Mangal, S., Nagori, P., Jain, A., and Khandelwal, V., “Video matting by automatic scribbling using quadra directional filling of segmented frames”, in 2nd IEEE International Conference on Computer Science and Information Technology, ICCSIT 2009, art. no. 5234624, pp. 336-340, 2009.	6
Vineet Khandelwal	Agnihotri, S., Loomba, H., Gupta, A., and Khandelwal, V., “Automated segmentation of gallstones in ultrasound images”, in 2nd IEEE International Conference on Computer Science and Information Technology, ICCSIT 2009 , art. no. 5234996, pp. 56-59, 2009.	3
Vineet Khandelwal	Khandelwal, V., Gupta, A., Kashyap, M., Gandhi, H., and Dhawan, A., “Natural image matting using HSI framework”, in 2nd IEEE International Conference on Computer Science and Information Technology, ICCSIT 2009, art. no. 5234833, pp. 141-144, 2009	3
Vivek Dwivedi	Dwivedi, V. K., and Singh, G. “Improved BER Analysis of OFDM Communication System on Correlated Nakagami Fading Channel,” in International Conference on Recent Advances in Microwave Theory and Applications, India, pp.536-539, Nov. 2008.	7
Vineet Khandelwal	Jain, A., Agarwal, M., Gupta, A., and Khandelwal, V., “A Novel Approach to Video Matting using Automated Scribbling by Motion Analysis”, in IEEE Conference on Virtual Environments, Human-Computer Interfaces and	8

	Measurement Systems (VECIMS 2008), Istanbul, Turkey, pp. 25-30, July 14-16, 2008.	
Vivek Dwivedi	Dwivedi, V. K., and Singh, G., "An efficient BER analysis of OFDM systems with ICI conjugate cancellation method", in Progress In Electromagnetic Research Symposium, Cambridge USA., pp. 166-171, July 2008.	20
Vivek Dwivedi	Sharma, A., Dwivedi, V. K., and Singh, G., "THz Rectangular Patch Microstrip Antenna Design Using Photonic Crystal as Substrate", in Progress In Electromagnetics Research Symposium (PIERS 2008), Cambridge USA, pp. 161-166, July 2008.	20
Vineet Khandelwal	Gupta, A., Khandelwal, V., Srivastava, M. C., and Gupta, A., "Image Processing Methods for the restoration of Digitized Paintings", Thamasat Int. Journal of Science & Technology, ISSN: 0859-4074, vol. 13, No.3, pp. 66-72, July 2008.	18
Neetu Singh	Yadav, K.; Singh, N.; Srivastava, M.C., "Analysis of Adaptive Pilot Symbol Assisted Modulation with Optimized Pilot Symbol Spacing for Rayleigh Fading Channel", International Conference on Signal Processing, Communications and Networking, 2008. ICSCN '08. 4-6 Jan. 2008, Page(s): 405 – 410	2
Samir Dev Gupta	S D Gupta, A Garg, A P Saran, " Improvement in Accuracy for Design of Mutidielectric Layer Microstrip Patch Antenna" International Journal of Microwave and Optical Technology (IJMOT) 3(5), 498-504, 2008.	3
Vineet Khandelwal	M Agarwal, S Gahlaut, V Khandelwal, M C Srivastava, "Exemplar based method to remove cracks in digitized paintings," Proc. of the Second International Conference on Information Processing, pp. 227-233, 2008.	1
Pushpendra Singh	P Singh, S D Joshi, R K Patney, K Saha, "Some studies on nonpolynomial interpolation and error analysis," Applied Mathematics and Computation, 244, pp. 809-821.	4
Vineet Khandelwal	Gupta, A., Srivastava, M. C., Khandelwal, V., and Gupta, A., "A Novel Approach to Fetal ECG Extraction and Enhancement Using Blind Source Separation (BSS-ICA) And Adaptive Fetal ECG Enhancer (AFE)", in Sixth IEEE International Conference on Information, Communications and Signal Processing, NTU Singapore, pp. 1-4, Dec. 2007.	6
Neetu Singh	Yadav, K.; Singh, N.; Maskara, S.L.," Analysis of pilot symbol assisted modulation using modified long range	1

	prediction algorithm for rayleigh fading channel”, International Conference in Central Asia on Internet, 2007. ICI 2007, 3rd IEEE/IFIP, 26-28 Sept. 2007 Page(s): 1 – 4.	
Shamim Akhter	Akhter, S., “VHDL implementation of fast NxN multiplier based on Vedic mathematic”, in 18th European Conference on Circuit Theory and Design, Sevilla, Spain, pp. 472-475, 26-30 August , 2007, .	35
Abhinav Gupta	Gupta, A., Srivastava, M. C., Pandey, S. D., and Bhandari, V., “Modified runlength coding for improved JPEG performance,” in IEEE conference ICICT at Bangladesh University of Engineering and Technology (BUET), Dhaka, Bangladesh, pp. 263-267, 2007.	7
	Total Google Scholar Citations	363

Conferences, Workshops, Seminars attended by faculty outside JIIT

A. B. Bhattacharyya

1. "Tutorial: Low-Power Digital CMOS Design", (Faculty Development Program on Low Power VLSI Design), February 20-22, 2014, ABES Engineering College, Ghaziabad.

R. C. Jain

1. 16th International Conference on Network-Based Information Systems (NBiS), South Korea, Sept. 4-6, 2013
2. Nineteenth National Conference on Communication (NCC)-2013 at IIT New Delhi, Feb. 18-19, 2013.
3. Workshop on "Embedded Guidance, Navigation and Control in Aerospace at Aerospace Department, IISc, Bangalore, 13-15 February, 2012
4. "University day" organized by ST Microelectronics and Synopsys, Noida. Chip Designing and EDA software, 16th November, 2010.

Jitendra Mohan

1. IEEE International Conference on Multimedia, Signal Processing and Communication Technologies (IMPACT-2013), Aligarh, India, November 2013.
2. International Conference on Advances in Electronics, Electrical and Computer Science Engineering - EEC 2012, Dehardun, India, July 2012.
3. IEEE International Conference on Signal Processing, Computing and Control (ISPCC), Solan, India, March 2012.
4. IEEE International Conference on Multimedia, Signal Processing and Communication Technologies (IMPACT), Aligarh, India December 2011.

Vipin Balyan

1. IEEE International Conference on Signal Processing and Integrated Network (SPIN-2014), Amity University, Noida-India (20-21 Feb. 2014).
2. IEEE International Conference on Software Telecommunication and Computer Networks, SoftCom 2011, Sep 15-17, 2011, Croatia.

Sajai Vir Singh

1. IEEE International Conference on Signal Processing and Integrated Network (SPIN-2014), Amity University, Noida-India (20-21 Feb. 2014).
2. IEEE International Conference on Multimedia, Signal Processing and communication Technologies (IMPACT-2013) on 23-25 Nov. 2013 at Aligarh (U.P.)
3. IEEE International Conference on Multimedia, Signal Processing and communication Technologies (IMPACT-2012) on 17-19 Dec., 2011 at Aligarh (U.P.)

Alok Joshi

1. Attended and presented paper in International Conference (ARTCOM 2011), Bangalore.

Bhartendu Chaturvedi

1. IEEE International Conference on Multimedia, Signal Processing and Communication Technologies (IMPACT-2013), Aligarh, India, November 2013.

Satyendra Kumar

1. IEEE SSCS Delhi Chapter "Distinguished Lecture Colloquia" on 19 Dec. 2014, STMicroelectronics Greater Noida, India.
2. Synopsys University Symposium 2014 on "Custom IC Design & Device Modelling", on 7th March 2014, Synopsys and EIGEN Technologies Pvt. Ltd., New Delhi

Vinay Tikkiwal

1. IEEE SSCS Delhi Chapter "Distinguished Lecture Colloquia" on 19 Dec. 2014, STMicroelectronics Greater Noida, India.
2. Synopsys University Symposium 2014 on "Custom IC Design & Device Modelling", on 7th March 2014, Synopsys and EIGEN Technologies Pvt. Ltd., New Delhi.

Raaziyah Shamim

1. 9th International Conference on Heterogeneous Networking for Quality, Reliability, Security and Robustness, GBU, Greater Noida (11-12 Jan. 2013).

Shruti Kalra

1. Workshop on “Enhancing the outreach of Electronic System Design through e-learning” held at CDAC Noida On 20th Nov 2014.
2. Distinguished Lecture Colloquia on Analog/Digital Circuits at STMicroelectronics, Greater NOIDA, 23-24 July, 2012.
3. One day workshop on “COMSOL Multiphysics simulations” organized by COMSOL, Delhi on 28th April, 2011.
4. “University day” organized by STMicroelectronics and Synopsys, Noida in which Chip Designing and EDA software was discussed held on 16th November, 2010.

Ashish Goel

1. Nineteenth National Conference on Communication (NCC)-2013 at IIT New Delhi, Feb. 18-19, 2013.
2. Ninth IEEE International Conference on Wireless and Optical Communications Networks (WOCN)-2012 at IIT Indore, Sept. 20-22, 2012.

Richa Gupta

1. IEEE International Conference on Recent Advances and Innovations in Engineering (ICRAIE 2014), 9-11 May 2014, Jaipur.
2. Twenty First International Conference of Forum for Interdisciplinary Mathematics, Statistics and Computational Techniques, IMSCT 2012- FIM XXI, Chandigarh, Dec. 15-17, 2012.

Manish Kumar

1. IEEE SSCS Delhi Chapter seminar at STMicroelectronics, Greater NOIDA on June 21, 2013.

Abhishek Srivastava

1. Workshop “TECHWEEK 2011”, University Day Agenda, at ST Microelectronics, 11th March 2011.

2. “University day” organized by STMicroelectronics and Synopsys, Noida in which Chip Designing and EDA software was discussed held on 16th November, 2010.

Kirmender Singh

1. Workshop “TECHWEEK 2011”, University Day Agenda, at ST Microelectronics, 11th March 2011.
2. “University day” organized by STMicroelectronics and Synopsys, Noida in which Chip Designing and EDA software was discussed held on 16th November, 2010

Bhawna Gupta

1. Workshop on Medical Imaging: Techniques & Clinical Applications organized by Centre for Biomedical Engineering Indian Institute of Technology Delhi, 3rd & 4th April, 2015.
2. “Confluence-2011”, International Conference on Information Technology, New Delhi, from 27th Jan.-28th Jan., 2011.
3. “INDIA TELECOM-2010”, International Conference on Telecom, New Delhi, 9th Dec.-11th Dec., 2010.

Smriti Bhatnagar

1. Workshop on Medical Imaging: Techniques & Clinical Applications organized by Centre for Biomedical Engineering Indian Institute of Technology Delhi, 3rd & 4th April, 2015.
2. “Confluence-2011”, International Conference on Information Technology, New Delhi, from 27th Jan.-28th Jan., 2011.
3. “INDIA TELECOM-2010”, International Conference on Telecom, New Delhi, 9th Dec.-11th Dec., 2010

Vikram Karwal

1. Attended SystemC training at Cadence Design Systems, September 2014.
2. Workshop on “Advanced DSP Design Techniques” at IIT Kharagpur, 11-15 July, 2011.
3. Workshop on "VLSI Chip Design, Verification and Registration Issues" at CDAC NOIDA, 20th June, 2011.

4. Workshop on “Fundamentals of Automatic Speech Recognition” at CDAC Noida, 7th -8th March, 2011.
5. Workshop on “Altium Seminar Series - Modern FPGA and PCB design processes” at New Delhi, 9th Feb., 2011.
6. Session Chair and TPC member in “International Conference on Signal Processing and Integrated Networks (SPIN 2014)” technically co-sponsored by IEEE, Feb 2014.

Shamim Akhter

1. Workshop on "VLSI Chip Design, Verification and Registration Issues" at CDAC NOIDA, 20th June, 2011.
2. Workshop on “Fundamentals of Automatic Speech Recognition” at CDAC Noida, 7th-8th March, 2011.
3. Workshop “TECHWEEK 2011”, University Day Agenda, at ST Microelectronics, 11th March 2011.
4. Workshop on “Altium Seminar Series - Modern FPGA and PCB design processes” at New Delhi, 9th Feb., 2011.

Gaurav Verma

1. Faculty Development Program on “Low-Power VLSI Design" at ABES Engineering College, Ghaziabad, February 20-22, 2014

Rajesh Kumar Dubey

1. Attended 6 days International Conference, Acoustics 2013 New Delhi, organized by the Acoustical Society of India (ASI) and the French Acoustical Society (SFA) under the aegis of the CSIR-National Physical Laboratory, New Delhi during 10th-15th Nov. 2013.
2. Workshop on “Fundamentals of Automatic Speech Recognition” at CDAC Noida, 7th -8th March, 2011.

Pankaj Kumar Yadav

1. Short Term Course on “Next Generation Networks” at IIT Delhi, 9th-10th Jan, 2010.

Rahul Kaushik

1. Five Day ICT based Faculty Development Programme on Optical Fiber Communication from 22nd to 26th September, 2014 at ABES Engineering College, organized by NITTTR, CHANDIGARH under NMICT, MHRD, Govt. of India.
2. Short Term Course on “Next Generation Networks” at IIT Delhi, 9th-10th Jan, 2010.

Juhi Gupta

1. Short Term Course on “Next Generation Networks” at IIT Delhi, 9th-10th Jan, 2010.

Sandeep Joshi

1. Short Term Course on “Next Generation Networks” at IIT Delhi, 9th-10th Jan, 2010.

Saurabh Chaturvedi

1. Saurabh Chaturvedi attended and presented papers in “International Conference on Signal Processing and Integrated Networks (SPIN-2014)”, Amity University, NOIDA, India during Feb. 20-21, 2014.

Shradha Saxena

1. Workshop on, “Recent Trends in Semiconductor Devices and Technology” at Deen Dayal Dayal Upadhyaya College, Karampura, New Delhi, 13-14 Sept, 2014.

Shivaji Tyagi

1. IEEE CAS Workshop on, “Advanced Topics in VLSI Circuit Design” at IIT, Roorkee, 18-19th Oct, 2014.
2. Synopsys University Symposium on “SoC Architecture Design & Embedded Software Development with Virtual Prototyping”, 30th Apr, 2015
3. IEEE EDS Workshop on, “GaN Devices Fabrication, Device Modeling and Power Amplifiers” at IIT, Roorkee, 10th June, 2015.