

Detailed Syllabus

Lecture-wise Breakup

Subject Code	17M27EC126/17M17 EC129/17M27EC330	Semester	Even	Semester 2nd& 9th Session 2020-21 Month from July 20 to Dec 20
Subject Name	Project Based Learning-2 & 3			
Credits	2	Contact Hours	2	

Faculty (Names)	Coordinator(s)	Dr. Gaurav Verma
	Teacher(s) (Alphabetically)	NA

COURSE OUTCOMES		COGNITIVE LEVELS
C210.1	Summarize the contemporary scholarly literature, activities, and explored tools/ techniques/software/hardware for hands-on in the respective project area in various domain of Embedded Systems, Signal Processing, VLSI, Communication, Artificial Intelligence and Machine Learning/Deep Learning etc.	Understanding (Level II)
C210.2	Analyze/ Design the skill for obtaining the optimum solution to the formulated problem with in stipulated time and maintain technical correctness with effective presentation.	Analysing (Level IV)
C210.3	Use latest techniques and software tools for achieving the defined objectives.	Evaluating (Level V)
C210.4	Evaluate /Validate sound conclusions based on analysis and effectively document it in correct language and proper format.	Evaluating (Level V)

Evaluation Criteria	
Components	Maximum Marks
Mid Sem Evaluation	40
Final Evaluation	40

Report	20
Total	100

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Course Outcomes

Course Code	17I17EC511/17M17EC219/ 17M27EC215/17M17EC222 /17I17EC511	Semester ODD& EVEN	Semester 3rd& 4th for M.Tech / 11th for Dual Degree
Course Name	Dissertation		
Credits	M.Tech-4 & 16 DD - 22	Contact Hours	8 & 32
Session	2020 - 2021		
Month from	July to Dec/Jan to May		

Faculty (Names)	Coordinator(s)	Ms. Bhawna Gupta, Dr. Rachna Singh
	Teacher(s) (Alphabetically)	All faculty of ECE Deptt.

COURSE OUTCOMES		COGNITIVE LEVELS
C213.1	Summarize the contemporary scholarly literature, activities, and explored tools/ techniques/software/hardware for hands-on in the respective project area in various domain of Electronics Engineering.	Understanding (Level II)
C213.2	Gain knowledge of the State-of-Art in the chosen field of study. Analyze various feasible methods of solving a problem to slot a suitable solution methodology	Analyzing and Designing (Level IV)
C213.3	Use latest techniques and software tools for achieving the defined objectives. Evaluate /Validate sound conclusions based on evidence and analysis	Evaluating (Level V)
C213.4	Demonstrate the oral and written communication skills. Describe the importance of possible future developments in the selected domain	Create Level (Level VI)

Evaluation Criteria

(Dissertation at the end of third semester for M.Tech only)

Components	Maximum Marks
End Term Viva	60
Day to Day	40
Total	100

(Dissertation at the end of final semester for M.Tech/DD)

Components	Maximum Marks
End Term Viva	50
Special Contribution	10
Day to Day	40
Total	100

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Course Outcomes

Course Code	17I17EC512/17M27EC216/17M17EC220	Semester ODD& EVEN	Semester 3 rd & 4 th for M.Tech / 11 th for Dual Degree Session 2020 -2021 Month from July to Dec/Jan to May
Course Name	Industrial Project		
Credits	M.Tech –4 &16 DD - 22	Contact Hours	8 & 32

Faculty (Names)	Coordinator(s)	Ms. Bhawna Gupta, Dr. Rachna Singh
	Teacher(s) (Alphabetically)	All faculty of ECE Deptt.

COURSE OUTCOMES		COGNITIVE LEVELS
C214.1	Summarize the contemporary scholarly literature, activities, and explored tools/ techniques/software/hardware for hands-on in the respective project area in various domain of Electronics Engineering.	Understanding (Level II)
C214.2	Gain knowledge of the State-of-Art in the chosen field of study. Analyze various feasible methods of solving a problem to slot a suitable solution methodology	Analyzing and Designing (Level IV)
C214.3	Use latest techniques and software tools for achieving the defined objectives. Evaluate /Validate sound conclusions based on evidence and analysis	Evaluating (Level V)
C214.4	Demonstrate the oral and written communication skills. Describe the importance of possible future developments in the selected domain	Create Level (Level VI)

Evaluation Criteria

(Industrial Project at the end of final semester for M.Tech/DD)

Components	Maximum Marks
End Term Viva	30
Day To Day	20 (Awarded by Internal Supervisor)
Day To Day	50 (Awarded by Supervisor from Industry)
Total	100

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Course Code	17M17EC218 17M27EC211	Semester Odd (specify Odd/Even)	Semester 10 th for dual degree and 3 rd for M.Tech. Session 2020-2021 Month from July to December
Course Name	Seminar and Term Paper		
Credits	4	Contact Hours	

Faculty (Names)	Coordinator(s)	Dr Saurabh Chaturvedi
	Teacher(s) (Alphabetically)	

COURSE OUTCOMES - At the end of the course, students will be able to:		COGNITIVE LEVELS
C212.1	Understand relevant theories, methods and research design relating to the seminar topic selected by a student	Understanding Level (C2)
C212.2	Analyze the work of other authors/researchers and contribute to the field of knowledge with the cooperation of the supervisor	Analyzing Level (C4)
C212.3	Evaluate the previously published research works, findings and conclusions	Evaluating Level (C5)
C212.4	- Develop and refine the master's dissertation topic and proposal - Develop the effective technical writing, communication and presentation skills	Creating Level (C6)

Evaluation Criteria	
Components	Maximum Marks
Day to day work done prior to mid-term	20
Mid-term seminar/presentation	20
Day to day work done prior to end-term	20
End-term seminar/presentation	20
End-term report - Term Paper	20
Total	100