Detailed Syllabus
Course Outcomes

Course Code	17I17EC512/17M27EC216/17M17EC220	Semester ODD& EVEN	Semester 3rd & 4th for M.Tech / 11th for Dual Degree Session 2020 -2021 Month from July to Dec/Jan to May
Course Name	Industrial Project		
Credits	M.Tech –4 &16 DD - 22	Contact Hours	8 & 32

Faculty (Names)	Coordinator(s)	Ms. Bhawna Gupta, Dr. Rachna Singh
	Teacher(s) (Alphabetically)	All faculty of ECE Deptt.

COURSE OUTCOMES		COGNITIVE LEVELS
C214.1	Summarize the contemporary scholarly literature, activities, and explored tools/ techniques/software/hardware for hands-on in the respective project area in various domain of Electronics Engineering.	Understanding (Level II)
C214.2	Gain knowledge of the State-of-Art in the chosen field of study. Analyze various feasible methods of solving a problem to slot a suitable solution methodology	Analyzing and Designing (Level IV)
C214.3	Use latest techniques and software tools for achieving the defined objectives. Evaluate /Validate sound conclusions based on evidence and analysis	Evaluating (Level V)
C214.4	Demonstrate the oral and written communication skills. Describe the importance of possible future developments in the selected domain	Create Level (Level VI)

Evaluation Criteria

(Industrial Project at the end of final semester for M.Tech/DD)
Components   **Maximum Marks**
End Term Viva   30
Day To Day   20 (Awarded by Internal Supervisor)
Day To Day   50 (Awarded by Supervisor from Industry)
Total   100

Detailed Syllabus
Course Outcomes

Course Code	17I17EC511/17M17EC219/ 17M27EC215/17M17EC222 /17I17EC511	Semester ODD& EVEN	Semester 3rd& 4th for M.Tech / 11th for Dual Degree Session 2020 - 2021 Month from July to Dec/Jan to May
Course Name	Dissertation		
Credits	M.Tech–4 &16 DD - 22	Contact Hours	8 & 32

Faculty (Names)	Coordinator(s)	Ms. Bhawna Gupta, Dr. Rachna Singh
	Teacher(s) (Alphabetically)	All faculty of ECE Deptt.

COURSE OUTCOMES		COGNITIVE LEVELS
C213.1	Summarize the contemporary scholarly literature, activities, and explored tools/ techniques/software/hardware for hands-on in the respective project area in various domain of Electronics Engineering.	Understanding (Level II)
C213.2	Gain knowledge of the State-of-Art in the chosen field of study. Analyze various feasible methods of solving a problem to slot a suitable solution methodology	Analyzing and Designing (Level IV)
C213.3	Use latest techniques and software tools for achieving the defined objectives. Evaluate /Validate sound conclusions based on evidence and analysis	Evaluating (Level V)

	Demonstrate the oral and written communication skills. Describe	Create Level
C213.4	the importance of possible future developments in the selected domain	(Level VI)

Evaluation Criteria

(Dissertation at the end of third semester for M.Tech only)

Components	Maximum Marks
End Term Viva	60
Day to Day	40
Total	**100**

(Dissertation at the end of final semester for M.Tech/DD)

Components	Maximum Marks
End Term Viva	50
Special Contribution	10
Day to Day	40
Total	**100**