ONE WEEK ONLINE FACULTY DEVELOPMENT PROGRAM
On
OUTCOME-BASED RESEARCH: VLSI DEVICES, CIRCUIT ANLYSIS, LATEST DESIGN TOOLS AND APPLICATIONS

July 21st- July 27th, 2022

Department of Electronics and Communication Engineering
Jaypee Institute of Information Technology
A-10, Sector-62, Noida,
UP-201309, INDIA

PROGRAM COORDINATORS

Dr. Garima Kapur
Assistant Professor (Sr. Grade), JIIT Noida
Email: *garima.kapur@jiit.ac.in*

Dr. Archana Pandey
Assistant Professor (Sr. Grade), JIIT Noida
Email: *archana.pandey@jiit.ac.in*

JAYPEE INSTITUTE OF INFORMATION TECHNOLOGY AND DEPARTMENT OF ELECTRONICS AND COMMUNICATION

Jaypee Institute of Information Technology (JIIT), Noida, an AICTE approved, NIRF (MHRD) ranked and NAAC accredited institute which has been constantly ranked amongst the top engineering institutes in north India *(http://www.jiit.ac.in).*To keep pace with the rapidly evolving electronics industry requires unwavering passion and urge to innovate. This precisely is what Electronics and Communication Engineering (ECE) Department at JIIT is about. Started in the year 2002, the department of ECE runs full time 4-year BTech degree program at UG level. The department also offers 5-year Integrated degree program in ECE and two PG degree programs with specialization in ECE and microelectronics & embedded systems. The department is having a team of highly motivated 72 faculty members having extensive experience in industry, research as well as teaching. Our strength is a reflection of their skills, innovation and drive. At present, there are around 40 Research scholars involved in thrust areas of research such as VLSI Design, 4G and 5G Technologies, channel modeling, VLSI DSP architectures and many more.

FACULTY DEVELOPMENT PROGRAM

CMOS processes have emerged as a viable choice for the integration of today's complex mixed-signal and SoC systems. This FDP will introduce innovative teaching practices in IC design both front-end and back-end design methodologies to the faculty members and researchers to get firsthand experience in this area. The faculty development program "Outcome based Research: VLSI Devices, Circuit Analysis, Latest Design Tools and Applications" aims in developing faculty skills in various aspects of microelectronics and VLSI, encompassing materials research, device development and building complex digital and analog blocks. The program also aims in establishing strong research links with industry in India and abroad. The main focus of the FDP will be on physics and technology of devices, circuit design, and system architecture used in CAD tools. The FDP is catering two days hand-on workshop on latest technology tools of Cadence.

THE OBJECTIVE OF FDP IS TO:

1. Perceive the gaps in attaining learning outcomes as per bloom's taxonomy.
2. To build up research activities in order to bridge the Gap between Academics and Industries through project-based learning in VLSI domain.
3. To analyze teaching contents, teaching methods and experimental modes for core VLSI courses effectively.

AFTER THE COMPLETION OF FDP:

1.The faculty members would be able to conduct research-based activities to build up the VLSI domain of their respective institutes
2. The faculty members can relook Experiments, Assignments, and projects in VLSI domain to recognize the gaps in attainting learning outcomes as per Bloom's Taxonomy.

WHO CAN PARTICIPATE?

1. The program is open to all the faculty members/ scientists/engineers working in educational institutes/ industries/ R&D organizations.
2. Single registration to attend all lectures
3. Registration fee can be paid online.
4. E-certificate will be issued to the participants on successful participation of the course.
5. Google meet link and other instructions will be shared via e-mail to all registered participants.

REGISTRATION FEE

Registration Fee: Rs. 500/- (faculty/Industry)
                 Rs 200 (Research Scholars)
Registration Fee (Members of JIIT): NIL

BANK DETAILS FOR NEFT

Bank and Branch: IDBI Bank, Sector 18, Noida
Beneficiary Name: Jaypee Institute of Information Technology
Account Number: 109102000007436
IFSC Code: IBKL0000109

HOW TO REGISTER

To complete the registration, the applicants are requested to fill the Google form provided in the link below.

https://forms.gle/ewQVXiPjo4t23snK6

Last Date of Registration: July 12th, 2022.

DISTINGUISHED SPEAKERS

Dr. Anup Dandapat
Associate Professor
Department of Electronics
Communication
NIT Meghalaya

Mr Arsh Josan
Senior Physical Design
Engineer, Nvidia, USA

Dr.Prabir Saha
Assistant Professor
Department of Electronics
Communication
NIT Meghalaya

Dr.Purnima Sethi
Director,
SuperQ Technologies India
Private Limited

Mr. Prayes Jain
Application Engineer
Cadence Design Systems

Mr S.K Aggarwal
Regional Manager
Entuple Technologies
Bangalore

Dr. Ekta Goel
Assistant Professor
NIT Warangal
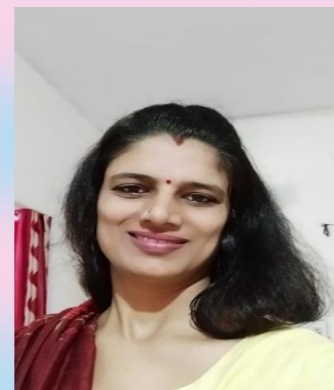
Dr. Menka Yadav
Assistant Professor
MNIT Jaipur

Dr. Savitesh Madhulika Sharma
Professor and Dean (R&D)
Siddartha Institute of Science and
Technology, Puttur (A.P.)

Dr. K P Pradhan
Assistant Professor
Indian Institute of Information
Technology Design &
Manufacturing Kancheepuram