

M.Tech in ECE with specialization in Microelectronic systems and IoT

First Semester

S.No	Sub Code	Subject	Contact hours	Credits
1.	17M11EC118	Advanced Digital Signal Processing	3	3
1	New Course	Introduction to IoT System Design	3	3
2		DE-I	3	3
3		DE-II	3	3
4		DE-III	3	3
5	18M11GE111	Research Methodology and Intellectual Property Rights	2	2
6	New Course	Microelectronics and IoT Lab-1	6	3
		Total	23	20

Second Semester

S.No	Sub Code	Subject	Contact hours	Credits
1	17M21EC115	Analogue Integrated Circuit Design	3	3
2	New course	IoT Perspective: Cloud Computing and Machine Learning	3	3
3		DE-IV	3	3
4		DE-V	3	3
5	New Course	Microelectronics and IoT Lab-2	6	3
6	17M11EC120	Project Based Learning - I	4	2
		Audit-I	2	Qualifying
		Total	22	17

Third Semester

Sl. No.	Course Code	Title	Contact Hours				Credits
			L	T	P	Total	
		Open Elective	3			3	3
1.	17M17EC218	Seminar & Term Paper OR Earn credits by transfer eg. MOOCs, Course Work at another Institute, Supervised Study				4	4
2	17M15EC114	Project Based Learning - II				8	4
3.	17M17EC219/ 17M17EC220/ 17M17EC221	Dissertation /Industrial Project / Entrepreneurial Project				8	4
		Audit-II	2			2	Qualifying
		TOTAL				25	15

Fourth Semester

S.No	Sub Code	Subject	Contact hours	Credits
1	17M17EC222/	Dissertation /Industrial Project/	32	16
	17M17EC223/ 17M17EC224	Entrepreneurial Project	32	16

Courses for Audit-I and II:

1. English for Research Paper Writing

2. Disaster Management
3. Sanskrit for Technical Knowledge
4. Value Education
5. Constitution of India
6. Pedagogy Studies
7. Stress Management by Yoga
8. Personality Development through life enlightenment skills

List of some Elective Subjects

1. Digital Integrated Circuit Design
2. HDL Based Digital System Design
3. Semiconductor Device Modelling
4. Digital System Testing
5. Advanced Embedded System
6. Fundamentals of Semiconductor devices
7. VLSI Physical Design
8. Mixed Signal IC Design
9. Big Data Analytics for IoT
10. IoT Security
11. VLSI Architecture for DSP Applications
12. Optoelectronic and Photonics Materials & Devices
13. Low Power VLSI Design
14. ASIC Verification using System Verilog