

Department of Electronics and Communication Engineering

VLSI Design Automation Lab

VLSI Design Automation lab allows the theoretical concepts studied as part of subjects CMOS VLSI Design, Microelectronics Circuits and HDL, to experience in practical with the help of Cadence tool framework. The lab introduces the complete custom IC design flow, ASIC design flow and AMS (Analog and Mixed Signal) flow for Analog circuits, Digital circuits and Analog and mixed signal circuits design respectively.

In VLSI Design Automation lab is equipped with computer with the simulation software loaded. Students work individually and maintain individual laboratory notebooks and submit individual reports

Software:-

Vivado System Edition+SDSOC:-25 user
Synopsys EDA Tool